TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

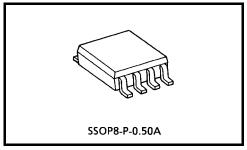
TC7WBL126AFK

Low-Voltage Dual Bus Switch

The TC7WBL126AFK provides two bits of low-voltage high-speed bus switching. The low ON-resistance of the switch allows connections to be made with minimal propagation delay and while maintaining CMOS low power dissipation.

The device comprises dual 2-bit switches with separate bus enable (OE) signals. When OE is high, the switch is on and port A is connected to port B. When OE is low, the switch is off and a high-impedance state exists between the two ports.

All inputs are equipped with protection circuits to guard against static discharge.

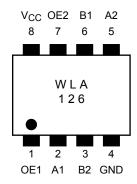


Weight: 0.01 g (typ.)

Features

- Operating voltage range: V_{CC} = 2 to 3.6 V
- High speed: $t_{pd} = 0.31 \text{ ns (max)} @ 3 \text{ V}$
- Ultra-low ON-resistance: $R_{ON} = 5 \Omega$ (typ.) @ 3 V
- ESD performance: Machine model $\geq \pm 200~V$ Human body model $\geq \pm 2000~V$
- Power-down protection provided on inputs (OE input only)
- Package: US8

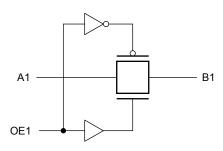
Pin Assignment (top view)

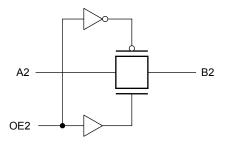


Truth Table

Inputs	Function	
OE	FullCuon	
Н	A port = B port	
L	Disconnect	

Logic Diagram





Absolute Maximum Ratings (Note) (Ta = 25°C)

Characteristic		Symbol	Rating	Unit	
Power supply range		V _{CC}	-0.5~4.6	V	
Control pin input v	roltage	V _{IN}	-0.5~4.6	V	
Switch terminal I/O voltage		VS	-0.5~Vcc+0.5	V	
Clump diode	Control input pin	l	-50	mA	
current	Switch terminal	lik	±50	MA	
Switch I/O current		IS	128	mA	
Power dissipation		PD	200	mW	
DC V _{CC} /GND current		I _{CC} /I _{GND}	±100	mA	
Storage temperature		T _{stg}	-65~150	°C	

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Operating Ranges (Note)

Characteristic	Symbol	Rating	Unit
Power supply voltage	V _{CC}	2.0~3.6	V
Control pin input voltage	V _{IN}	0~3.6	V
Switch I/O voltage	Vs	0~Vcc	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either VCC or GND.

Electrical Characteristics

TOSHIBA

DC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristic	Symbol	Test Condition		V _{CC} (V)	Min	Тур.	Max	Unit
High-level control input voltage	V _{IH}	_		2.0 to 3.6	0.7 × V _{CC}	_	_	V
Low-level control input voltage	V _{IL}	_		2.0 to 3.6	_	_	0.3 × V _{CC}	V
Control input current	I _{IN}	V _{IN} = 0 to 3.6 V		2.0 to 3.6	_	_	±1.0	μΑ
Power off leakage current	l _{OFF}	OE = 0 to 3.6 V		0	_	_	±1.0	μΑ
Off-stage leakage current (switch off)	I _{SZ}	A, B = 0 to V_{CC} , OE = GND		2.0 to 3.6	_	_	±1.0	μΑ
		$V_{IS} = 0 \text{ V}, I_{IS} = 30 \text{ mA}$	(Note 1)	3.0	_	2	7	
		$V_{IS} = 3.0 \text{ V}, I_{IS} = 30 \text{ mA}$	(Note 1)	3.0	_	4	9	
Switch ON-resistance	Pou	$V_{IS} = 2.4 \text{ V}, I_{IS} = 15 \text{ mA}$	(Note 1)	3.0	_	5	15	Ω
(Note 2)	R _{ON}	$V_{IS} = 0 \text{ V}, I_{IS} = 24 \text{ mA}$	(Note 1)	2.3	_	3	10	22
		$V_{IS} = 2.3 \text{ V}, I_{IS} = 24 \text{ mA}$	(Note 1)	2.3	_	5	15	
		V _{IS} = 2.0 V, I _{IS} = 15 mA	(Note 1)	2.3	_	8	25	
Quiescent supply current	Icc	$V_{IN} = V_{CC}$ or GND, $I_{OUT} = 0$		3.6	_	_	10	μА

Note 1: All typical values are at $Ta = 25^{\circ}C$.

Note 2: Measured by voltage drop between A and B pins at indicated current through the switch. ON-resistance is determined by the lower of the voltages on the two pins (A or B).

AC Characteristics ($Ta = -40 \text{ to } 85^{\circ}\text{C}$)

Characteristic	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay (bus to bus)	t _{pLH}	Figure 1, Figure 2 (Note)	3.3 ± 0.3	_	0.31	ns
Tropagation delay (bus to bus)	t _{pHL}	rigure 1, rigure 2 (Note)	2.5 ± 0.2		0.52	115
Output enable time	t _{oZL}	tpZL Figure 1, Figure 3	3.3 ± 0.3		7	ns
Output enable time	t _{pZH}	rigure 1, rigure 3	2.5 ± 0.2		10	20
Output disable time	Figure 1, Figure 3	3.3 ± 0.3		8	ns	
		i igure 1, i igure 3	2.5 ± 0.2		9	115

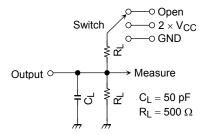
Note: This parameter is guaranteed by design but is not tested. The bus switch contributes no propagation delay other than the RC delay of the typical ON-resistance of the switch and the 50 pF load capacitance when driven by an ideal voltage from the source (zero output impedance).

Capacitance (Ta = 25°C)

Characteristic	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Control input capacitance	C _{IN}		3.0	3	pF
Switch terminal capacitance	C _{I/O}	OE = GND	3.0	23	pF

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AC Test Circuit



Test	Switch
t _{pLH} , t _{pHL}	Open
t_{pLZ} , t_{pZL}	$2 \times V_{CC}$
t _{pHZ} , t _{pZH}	GND

Figure 1

AC Waveforms

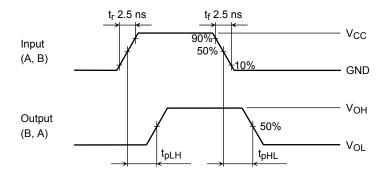


Figure 2 t_{pLH}, t_{pHL}

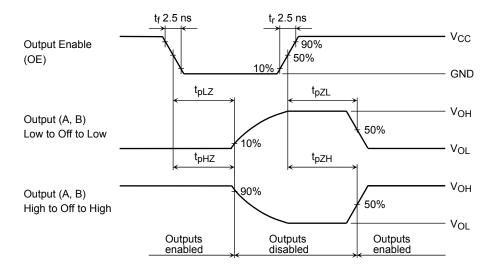
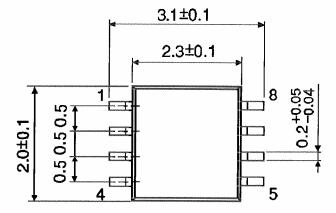
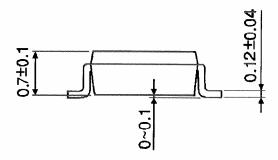


Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

Package Dimensions

SSOP8-P-0.50A Unit: mm





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Weight: 0.01 g (typ.)

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20070701-EN GENERAL

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