TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7WBD126AFK

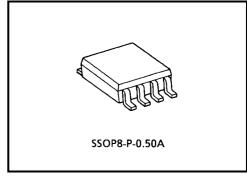
Dual Bus Switch with Level Shift

The TC7WBD126AFK is a low on-resistance, high-speed CMOS 2-bit bus switch. This bus switch allows the connections or disconnections to be made with minimal propagation delay while maintaining Low power dissipation which is the feature of CMOS.

When output enable (OE) is at High level, the switch is on; when at Low level, the switch is off.

The device is enable to realize the shift of signal level from 5 V to 3.3 V.

All inputs are equipped with protector circuits to protect the device from static discharge.

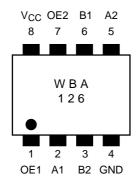


Weight: 0.01 g (typ.)

Features

- Operating voltage: $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed operation: $t_{pd} = 0.32 \text{ ns (max)}$
- Ultra-low on resistance: $R_{ON} = 5 \Omega$ (typ.)
- Electro-static discharge (ESD) performance: ±200 V or more (JEITA) ±2000 V or more (MIL)
- TTL level input (control input)
- Low Power Dissipation: Icc = $10 \mu A (max.)$
- Package: US8

Pin Assignment (top view)



Truth Table

Inputs	Function
OE	1 diletion
L	Disconnect
Н	A port = B port

System Diagram



Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC switch voltage	V _S	-0.5~7.0	V
Input diode current	I _{IK}	-50	mA
Continuous channel current	IS	128	mA
Power dissipation	P _D	200	mW
DC V _{CC} /GND current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~5.5	V
Switch voltage	Vs	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V



Electrical Characteristics

DC Characteristics ($Ta = -40 \sim 85$ °C)

Charac	teristics	Symbol	Test Condition V		V _{CC} (V)	Min	Typ. (Note 1)	Max	Unit				
Innut valtage	"H" level	V _{IH}	_	-	4.5~5.5	2.0	_	_	V				
Input voltage	"L" level	VIL	_	-	4.5~5.5	_	_	0.8	V				
Lligh lovel outp	ut voltoge		1011 4		4.75	2.3	2.8	3.2					
High-level outp	(Note 2)	V _{OH}	IOH=-1μA V _{IS} = V _{CC}		5.0	2.5	3.0	3.4	V				
	(Note 2)		VIS - VCC		5.25	2.7	2.7 3.2 3.6						
Input leakage of	current	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5	_	_	±1.0	μΑ				
Power off leaka	age current	l _{OFF}	A, B, OE = 0~5.5 V		0	_	_	±1.0	μΑ				
Off-STATE leal (switch off)	kage current	I _{SZ}	A, B = 0~5.5 V, OE = V _{CC}		4.5~5.5	_	_	±1.0	μА				
				V 0.V	I _{IS} = 64 mA	4.5	_	5	9				
					V 0.V	V 0.V	$V_{IS} = 0 V$	_	IIS = 64 IIIA	4.75	_	5	8
ON resistance	(Note 3)	R _{ON}	VIS = 0 V	I.a. 20 m A	4.5	_	5	9	0				
				$I_{IS} = 30 \text{ mA}$	4.75	_	5	8	Ω				
			\/ 22\/ 15 m	^	4.5	_	35	65					
			$V_{IS} = 2.3 \text{ V}, I_{IS} = 15 \text{ m}$, ης – 13 IIIA		_	35	50					
Quiescent supp	oly current	ICC	VIN = VCC or GND,I _{OUT} = 0		5.5		_	10	μА				
Increase in I _{CC}	per input	Δlcc	V _{IN} = 3.4 V (one input)		5.5		_	2.5	mA				

Note 1: Typical values are at $V_{CC} = 5 \text{ V}$, $Ta = 25^{\circ}C$.

Note 2: It recommends that this device uses Pull-up resistance when adding and using resistance for an output terminal. Since it couses to drop a VOH voltage level when using Pull-down resistance for an output terminal.

Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

AC Characteristics ($Ta = -40 \sim 85$ °C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time (bus to bus)	t _{pLH}	Figure 1, Figure 2 (Note 4)	4.5	١	0.32	ns
Output enable time	t _{pZL} t _{pZH}	Figure 1, Figure 3	4.5		4.5	ns
Output disable time	t _{pLZ} t _{pHZ}	Figure 1, Figure 3	4.5		5.5	ns

Note 4: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

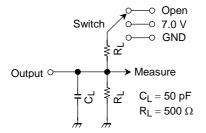
Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition		V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}	4)	Note 5)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	OE = V _{CC} (N	Note 5)	5.0	10	pF

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Note 5: This parameter is guaranteed by design.

AC Test Circuit



Parameter	Switch		
t _{pLH} , t _{pHL}	Open		
t_{pLZ}, t_{pZL}	7.0 V		
t _{pHZ} , t _{pZH}	Open		

Figure 1

AC Waveform

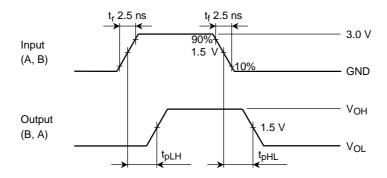


Figure 2 t_{pLH}, t_{pHL}

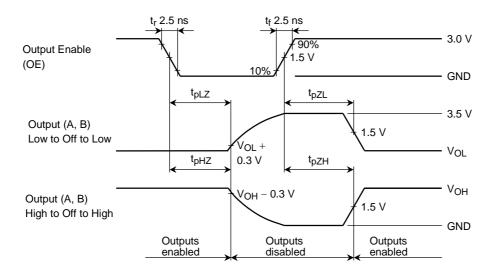
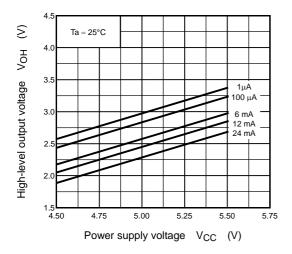
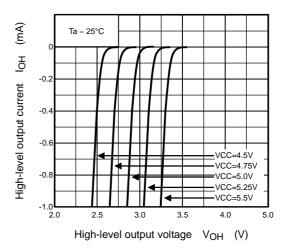
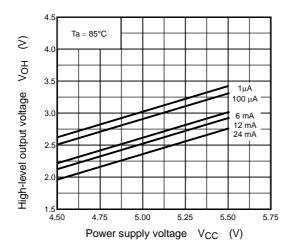


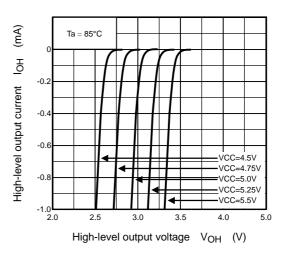
Figure 3 t_{pLZ} , t_{pHZ} , t_{pZL} , t_{pZH}

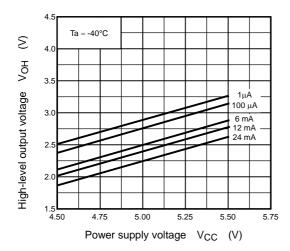
V_{OH} - V_{CC} Characteristics (typ.)











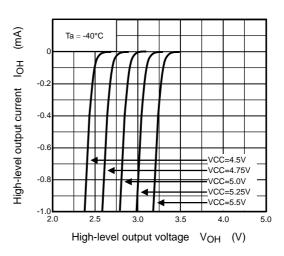


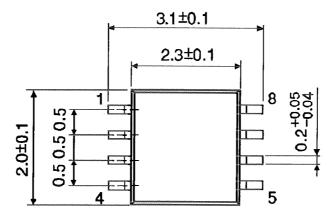
Figure 4

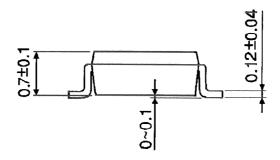
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Package Dimensions

TOSHIBA

SSOP8-P-0.50A Unit: mm





Weight: 0.01 g (typ.)

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