Odin K8F8/K8F8P/K8F8G

USER'S MANUAL

M/B For Socket 754 64-bit AMD Athlon64 Processor

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Trademark:

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TABLE OF CONTENT

| USER'S | S NOTICE | ii |
|--------|--|----|
| MANU. | AL REVISION INFORMATION | ii |
| COOLI | ING SOLUTIONS | ii |
| СНАРТ | TER 1 INTRODUCTION OF MOTHERBOARD | |
| 1-1 | FEATURE OF MOTHERBOARD | 1 |
| 1-1 | 1-1.1 SPECIAL FEATURES OF MOTHERBOARD | 1 |
| 1-2 | SPECIFICATION | |
| 1-2 | PERFORMANCE LIST | |
| | LAYOUT DIAGRAM & JUMPER SETTING | 4 |
| 1-4 | | 5 |
| CHAPT | TER 2 HARDWARE INSTALLATION | |
| 2-1 | HARDWARE INSTALLATION STEPS | 7 |
| 2-2 | CHECKING MOTHERBOARD'S JUMPER SETTING | |
| 2-3 | INSTALL CPU | 8 |
| | 2-3-1 GLOSSARY | 8 |
| | 2-3-1 GLOSSARY2-3-2 About AMD Athlon64 754-pin CPU | 9 |
| 2-4 | INSTALL MEMORY | 10 |
| 2-5 | EXPANSION CARD | 11 |
| | 2-5-1 PROCEDURE FOR EXPANSION CARD INSTALLATION | 11 |
| | 2-5-2 ASSIGNING IRQS FOR EXPANSION CARD | |
| | 2-5-3 INTERRUPT REQUEST TABLE FOR THIS MOTHERBOARD | 12 |
| | 2-5-4 PCI-Express SLOT | |
| 2-6 | CONNECTORS, HEADERS | |
| 2-0 | 2-6-1 CONNECTORS | |
| | | |
| 2.7 | 2-6-2 HEADERSSTARTING UP YOUR COMPUTER | |
| 2-7 | | 19 |
| CHAPT | TER 3 INTRODUCING BIOS | |
| 3-1 | ENTERING SETUP | |
| 3-2 | GETTING HELP | |
| 3-3 | THE MAIN MENU | |
| 3-4 | STANDARD CMOS FEATURES | 22 |
| 3-5 | ADVANCED BIOS FEATURES | 23 |
| 3-6 | ADVANCED CHIPSET FEATURES | |
| | 3-6-1 DRAM TIMING SETTINGS | |
| | 3-6-2 LDT & PCI TIMING SETTINGS | |
| 3-7 | INTEGRATED PERIPHERALS | |
| 0 / | 3-7-1 ONCHIP IDE FUNCTION | |
| | 3-7-2 ONCHIP DEVICE FUNCTION | |
| | 3-7-3 ONCHIP SUPER IO FUNCTION | |
| 3-8 | POWER MANAGEMENT SETUP | |
| 3-0 | 3-8-1 WAKE UP EVENTS | |
| | 3-8-1.1 IROS ACTIVITIES | |
| 3-9 | MISCELLANEOUS CONTROL | |
| 3-3 | 3-9-1 IRQ RESOURCES. | 33 |
| 3-10 | | |
| 3-10 | 3-10-1 SMART FAN CONFIGURATIONS | 34 |
| 3-11 | THERMAL THROTTLING OPTIONS | |
| 3-11 | | |
| 3-12 | | |
| 3-13 | LOAD STANDARD/OPTIMIZED DEFAULTS | 40 |
| _ | | 40 |
| CHAPI | TER 4 DRIVER & FREE PROGRAM INSTALLATION | |
| | GIC INSTALL SUPPORTS WINDOWS 9X/NT/2000/XP | 41 |
| 4-1 | NFOCE INSTALL NFORCE INTEGRATED DRIVER | |
| 4-2 | SOUND INSTALL ALC850 8-CHANNEL AUDIO DRIVER | |
| 4-3 | LAN MARVELL YUKON GIGABITETNEMET NIC DRIVER | |
| 4-4 | USB2.0 INSTALL USB2.0 DRIVER | 45 |
| 4-5 | PC-CILLIN INSTALL PC-CILLIN2005 ANTI-VIRUS PROGRAM | 46 |
| 4-6 | PC-HEALTH INSTALL MYGUARD HARDWARE MONITOR UTILITY | 47 |
| 4-7 | HOW TO UPDATE BIOS | |
| ΔΡΡΕΝ | DIX: Debug Port Post Code | |
| | 171/A. 1761/WE I UILI UNL CUUC | LL |

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Manual Revision Information

| Rev | ersion Re | evision History | Date |
|--------------|-----------------|-------------------|--------------|
| 1.0 | Fii | st Edition | October 2005 |
| Iten | n Checklist | | |
| \checkmark | Motherboard | | |
| \checkmark | Cable for IDE/ | Floppy | |
| \checkmark | CD for mother | board utilities | |
| | Cable for USB | Port 3/4 (Option) | |
| \checkmark | Cable for Seria | al ATA IDE Port | |
| | SPDIF-IN/SPI | OIF-OUT Adaptor | |
| \checkmark | User's Manual | | |

AMD K8 Processor Family Cooling Solutions

As processor technology pushes to faster speeds and higher performance with increasing operation clock, thermal management becomes increasingly crucial while building computer systems. Maintaining the proper computing environment without thermal increasing is the key to reliable, stable, and 24 hours system operation. The overall goal is keeping the processor below its specified maximum case temperature. Heatsinks induce improved processor heat dissipation through increasing surface area and concentrated airflow from attached active cooling fans. In addition, interface materials allow effective transfers of heat from the processor to the heatsink. For optimum heat transfer, AMD recommends the use of thermal grease and mounting clips to attach the heatsink to the processor.

Please refer to the website below for collection of heatsinks evaluated and recommended for Socket-939 processors by AMD. In addition, this collection is not intended to be a comprehensive listing of all heatsinks that support Socket-939 processors.

For vendor list of heatsinks and Active cooling fans, please visit: http://www.amd.com/us-en/Processors/DevelopWithAMD/0,,30_2252_869_9460^9515,00.html

Chapter 1

Introduction of Motherboards

1-1 Features of motherboard

Odin K8F8 /K8F8P /K8F8G motherboard is based on NVIDIA nForce4 SLI single chip technology which supports the innovative 64-bit AMD Athlon64 FX with HyperTransport Technology. These motherboards series deliver the leading-edge performance with NVIDIA Scalable Link Interface Technology with the latest Dual PCI Express for NVIDIA Dual Graphic Processing Unit ready platform. Utilize the 64-bit socket 754 solution and PC3200 DDR memory size expandable to 3.0GB, These motherboards series meet the demanding usage of computing in the future.

Odin K8F8 /K8F8P /K8F8G motherboards carry the advanced dual GPU supported NVIDIA nForce4 SLI single chip with 1000 MHz HyperTransport system bus of data transferring and provided with 133MHz / 166MHz / 200MHz memory clock frequency for DDR266/DDR333/DDR400 DDR Module. NVIDIA nForce4 single chipset offers ULTRA ATA 133 and Serial ATA RAID 0, 1, 0+1 functions to accelerate hard disk drives and guarantee the data security without failed in advanced computing performance.

Odin K8F8G motherboard provides Marvell 88E1111 controller which supports 10M / 100M / 1Gbps data transfer rate for software bandwidth Load-balance and full duplex, half duplex packages transportation. Embedded 8-channel AC'97 CODEC is fully compatible with Sound Blaster $Pro^{\mathbb{R}}$ standards that offers you with the home cinema quality and absolutely software compatibility.

Colorful and Intuitional SLI placement design offers three PCI-Express x16 graphics slots (Two symmetrical PCI-Express x16 graphics slots in light green deliver up to 2Gbyte/sec data transfer rate at each relative direction which is fully compatible with the latest NVIDIA SLI Technology. One PCI-Express x16 graphics slot offers 4Gbyte/sec data transfer rate at each relative direction which get 3.5 times of bandwidth more than AGP8X and up to 8Gbyte/sec concurrent bandwidth at full speed.) to guarantee the fully operational multi-GPUs graphics power and avoid the possible SLI hardware installation error. One PCI Express x1 I/O slot offers 512Mbyte/sec concurrently, over 3.5 times more bandwidth than PCI at 133Mbye/sec, tackling the most demanding multimedia tasks nowadays.

Embedded USB controller as well as capability of expanding to 10 of USB2.0 functional ports delivering 480Mb/s bandwidth and rich connectivity, these motherboards meet the future USB demands which are also equipped with hardware monitor function on system to monitor and protect your system and maintain your non-stop business computing.

Some special features---*CPU Thermal Throttling/ CPU Vcore 7-shift/ CPU Smart Fan/ Debug Port(Option)* in this motherboard are designed for power user to use the over-clocking function in more flexible ways. But please be caution that the over-clocking maybe cause the fails in system reliabilities. This motherboard provides the guaranteed performance and meets the demands of the next generation computing. But if you insist to gain more system performance with variety possibilities of the components you choose, please be careful and make sure to read the detailed descriptions of these value added product features, please get them in the coming section.

1-1.1 Special Features of motherboard

CPU Thermal Throttling Technology---(The CPU Overheat Protection Technology) To prevent the increasing heat from damage of CPU or accidental shutdown while at high workload, the CPU Thermal Throttling Technology will force CPU to enter partially idle mode from 87.5% to 12.5% according to preset CPU operating temperature in BIOS (from 40 $^{\circ}$ C to 90 $^{\circ}$ C). When the system senses the CPU operating temperature reaching the preset value, the CPU operating bandwidth will be decreased to the preset idle percentage to cool down the processor. When at throttling mode the beeper sound can be optionally selected to indicate it is in working.

Debug Port (Option)--- (The Professional Hardware Diagnosis System)

Being bugged of abnormal system failure through the tossed and turned nights no more, the embedded Hardware Debug Port offers you the real-time visual system healthy for the demanding usage of computing. No more bugging by unknown system failure and no more time wasted in the first moment of 24-hour nonstop ping business computing, the embedded Debug Port will turn you into a well training hardware professional with the seeing system situation. (The Post Code please refer Appendix)

CPU Smart Fan---(The Noise Management System)

It's never been a good idea to gain the performance of your system by sacrificing its acoustics. CPU Smart Fan Noise Management System is the answer to control the noise level needed for now-a-day's high performance computing system. The system will automatically increase the fan speed when CPU operating loading is high, after the CPU is in normal operating condition, the system will low down the fan speed for the silent operating environment. The system can provide the much longer life cycle for both CPU and the system fans for game use and business requirements.

CPU Vcore 7-Shift--- (Shift to Higher Performance)

The CPU voltage can be adjusted up by 7 steps for the precisely over-clocking of extra demanding computing performance.

1-2 Specification

| Spec | Description |
|-----------------------|--|
| Design | * ATX form factor 4 layers PCB size: 30.5x24.4cm |
| Chipset | * NVIDIA nForce4 SLI Single Chip Technology |
| CPU Socket 939 | * Support 64bit AMD Athlon64 754-Pin package utilizes Flip- Chip Pin Grid Array package processor |
| | * Support CPU Frequency 1G/800MHz |
| | * Support up to 4000+ processor |
| | * Reserves support for future AMD Athlon64 754-pin processors |
| Memory Socket | * 184-pin DDR Module socket x 3 |
| | * Support 3pcs DDR266/DDR33/DDR400 DDR Modules Expandable to 3.0GB |
| Expansion Slot | * PCI-Express x16 slot 3pcs deliver up to 8GB/s concurrent |
| | bandwidth in Non-SLI mode and 4GB/s concurrent bandwidth |
| | while SLI mode being activated |
| | * PCI-Express x1 slot 1pcs delivers up to 512MB/s concurrent bandwidth |
| | * 32-bit PCI slot x 3pcs |
| Integrate IDE and | Two PCI IDE controllers support PCI Bus Mastering, ATA |
| Serial ATA RAID | PIO/DMA and the ULTRA DMA 33/66/100/133 functions that |
| Seriai ATA KAID | deliver the data transfer rate up to 133 MB/s; Four Serial ATA |
| | ports provide 150 MB/sec data transfer rate for Four Serial |
| | ATA Devices and offer RAID 0, 1, 0+1 functions |
| LAN | * Odin K8F8G Supports Fast Ethernet LAN function provide |
| | 10Mb/100Mb/ 1Gb /s data transfer rate |
| 8CH-Audio | * AC'97 Digital Audio controller integrated |
| | * 8-channel AC'97 Audio CODEC on board |
| | * Audio driver and utility included |
| BIOS | * Award 4MB Flash ROM |
| Multi I/O | * PS/2 keyboard and PS/2 mouse connectors |
| | * Floppy disk drive connector x1 |
| | * Parallel port x1 |
| | * Serial port x2 |
| | * USB2.0 port x 4 and headers x 4 (connecting cable option) |
| | * Audio connector (Line-in, Line-out, MIC/8CH Audio) |

1-3 Performance List

The following performance data list is the testing result of some popular benchmark testing programs. These data are just referred by users, and there is no responsibility for different testing data values gotten by users (the different Hardware & Software configuration will result in different benchmark testing results.)

Performance Test Report

CPU: AMD K8 Athlon64 3200+ Support (Socket754, 512k L2 cache)

DRAM: KingMax MPTC220D-38HT 512MB DDR500 X 2 (1Gbyte) Memory **VGA Card:** GeForce6600GT 128M PCI-E VGA CARD X 2 (1024X768X32BIT Color)

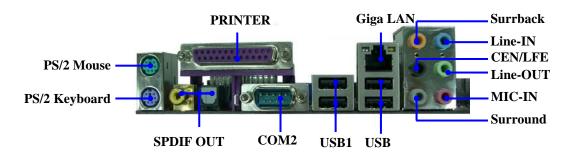
Hard Disk Driver: Seagate Barracude 7200.7 SATA150

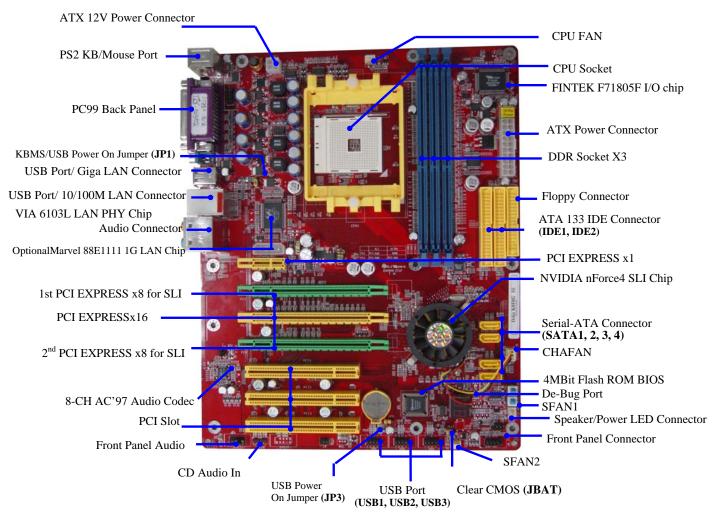
BIOS: Award Optimal default

OS: Windows XP Professional (SERVICE PACK 2)

| | SLI / 200/200 | Ultra / 200/200 | STD / 200/200 | |
|--|---------------------|------------------------|------------------------|--|
| 3D Mark 2001SE | 18975 | 18286 | 18280 | |
| 3D Mark 2003 | 13642 | 8101 | 8137 | |
| 3D Mark 2005 | 6184 | 3532 | 3545 | |
| AQUAMRK3 | 63572 (9999 / 8727) | 53014 (7545 / 8912) | 53336 (7569 / 9021) | |
| PCMark2004 | | | | |
| System / CPU / Memory | 3810 / 3829 / 4386 | 4072 / 3846 / 4401 | 4089 / 3854 / 4427 | |
| Graph / HDD | 5096 / 4676 | 4436 / 4663 | 4391 / 4619 | |
| Content Creation Winstone 2004 | 31.1 | 31.5 | 31.7 | |
| Business Winstone 2004 | 23 | 22.8 | 23 | |
| Winbench 99 V2.0: | | | | |
| Business/Hi-end Disk Winmark99 | 20100 / 42800 | 18700 / 41000 | 20300 / 41900 | |
| Business/Hi-end Graphic Winmark | 1120 / 1930 | 1240 / 1930 | 1170 / 1990 | |
| SISMark 2004: SISMark Rating(Internet Content Creation / Office Productivity) | | | | |
| SISMark 2004 | 171 (182 / 161) | 175 (184 / 167) | 178 (184 / 172) | |
| 3D Creation / 2D Creation | 168 / 222 | 169 / 255 | 170 / 228 | |
| / Web publication | 161 | 163 | 161 | |
| Communication / Document Creation | 181 / 156 | 179 / 175 | 191 / 175 | |
| / Data Analysis | 149 | 150 | 153 | |
| SISOFT Sandra 2004 : 1.CPU Arit Multi-Media Benchmark | hmetic Benchmark 2. | Memory bandwidth l | Benchmark 3.CPU | |
| 1.Dhrystone ALU MIPS | 10366 | 8576 | 8574 | |
| Whetstone FPU iSSE2 FLOPS | 8531 | 3293 / 4275 | 3279 / 4256 | |
| 2.Int/Float Buffered iSSE2 MB/S | 3278 / 4237 | 4828 / 4828 | 4640 / 4643 | |
| 3.Integer/Floating-Point SSE2 IT/S | 4810 / 4800 | 19137 / 20650 | 19117 / 20669 | |
| UT2003 Benchmark (flyby/botmatch) | 297.82 / 94.94 | 247.92 / 83.34 | 253.46 / 85.33 | |
| Quake3 DEMO1 / DEMO2 FPS | 234.04 / 81.74 | N/A | N/A | |
| Return to Castle Wolfenstein FPS | 85.4 | 75.0 | 75.4 | |
| Super Pi (1M) Second | 44s | 44s | 43s | |
| CPUZ System / CPU Clock | 200 / 200 / 2000.9 | 200.9 / 200.9 / 2009.3 | 200.9 / 200.9 / 2009.3 | |

1-4 Layout Diagram & Jumper Setting





Jumpers

| Jumper | Jumper Name | | | |
|--------|---------------------------------------|-------------|--|--|
| JBAT | CMOS RAM Clear | 3-pin Block | | |
| JP1 | Keyboard Power On Enabled/Disabled | 3-pin Block | | |
| JP2 | USB Power On Enabled/Disabled | 3-pin Block | | |

Connectors

| Connector | Name | Description |
|-----------|---------------------------------|------------------------|
| ATXPWR24P | ATX Power Connector | 24-pin Block |
| ATX12V | ATX 12V Power Connector | 4-pin Block |
| PS2KBMS | PS/2 Mouse & PS/2 Keyboard | 6-pin Female |
| | Connector | |
| USB/USB1 | USB Port Connector | 4-pin Connector |
| UL1 | 10/100M LAN Port Connector | RJ-45 Connector |
| USBLAN | Giga-LAN Port Connector | RJ-45 Connector |
| PARALLEL | Parallel Port Connector | 25-pin Female |
| J1 | 8-CH Audio Connector | 6 phone jack Connector |
| COM1/COM2 | Serial Port COM1/COM2 Connector | 9-pin Connector |
| FDD | Floppy Driver Connector | 34-pin Block |
| IDE1/IDE2 | Primary/Secondary IDE Connector | 40-pin Block |
| SATA1~4 | Serial ATA IDE Connector | 7-pin Connector |

Headers

| Header | Name | Description |
|-------------------|------------------------------------|-------------|
| AUDIO | SPEAKER, MIC header | 9-pin Block |
| USB2, USB3 | USB Port Headers | 9-pin Block |
| SPEAK | PC Speaker connector | 4-pin Block |
| PWR LED | Power LED | 3-pin Block |
| JW_FP | Front Panel Header | 9-pin Block |
| (Power LED/Reset/ | (including Power LED/ IDE activity | |
| IDE LED/Power | LED/Reset switch / Power On Button | |
| Button) | lead) | |
| SFAN1, SFAN2 | FAN Headers | 3-pin Block |
| CPUFAN | | |
| CDIN | CD Audio-In Header | 4-pin Block |

Expansion Sockets

| Socket/Slot | Name | Description | | |
|----------------|----------------------|--------------------------------------|--|--|
| ZIF Socket 754 | CPU Socket | 754-pin mPGAB Athlon64 CPU | | |
| | | Socket | | |
| DIMM1~3 | DDR Module Socket | 184-pin DDR Module Socket | | |
| PCI1~ PCI3 | PCI Slot | 32-bit PCI Local Bus Expansion slots | | |
| PE1 | PCI-Express x1 Slot | PCI-Express x1 Expansion Slot | | |
| PE3 | PCI-Express x16 Slot | PCI-Express x16 Expansion Slot | | |
| PE2,PE4 | PCI-Express x16 Slot | PCI-E x 8 Bandwidth for SLI Tech. | | |

Chapter 2

Hardware Installation

2-1 Hardware installation Steps

Before using your computer, you had better complete the following steps:

- 1. Check motherboard jumper setting
- 2. Install CPU and Fan
- 3. Install System Memory (DIMM)
- 4. Install Expansion cards
- 5. Connect IDE and Floppy cables, Front Panel /Back Panel cable
- 6. Connect ATX Power cable
- 7. Power-On and Load Standard Default
- 8. Reboot
- 9. Install Operating System
- 10. Install Driver and Utility

2-2 Checking Motherboard's Jumper Setting

(1) CMOS RAM Clear (3-pin): JBAT

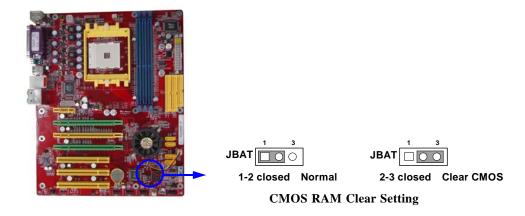
A battery must be used to retain the motherboard configuration in CMOS RAM short 1-2 pins of JBAT to store the CMOS data.

To clear the CMOS, follow the procedure below:

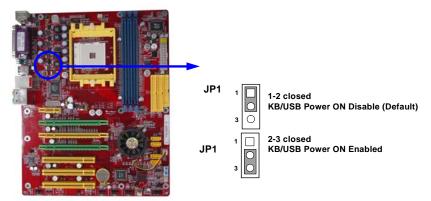
- 1. Turn off the system and unplug the AC power
- 2. Remove ATX power cable from ATX power connector
- 3. Locate JBAT and short pins 2-3 for a few seconds
- 4. Return JBAT to its normal setting by shorting pins 1-2
- 5. Connect ATX power cable back to ATX power connector

Note: When should clear CMOS

- 1. Troubleshooting
- 2. Forget password
- 3. After over clocking system boot fail

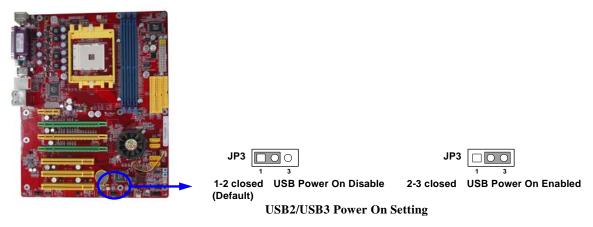


(2) Keyboard function Enabled/Disabled: JP1



Keyboard/Mouse & USB Power On Setting

(3) USB Power On function Enabled/Disabled: JP3



2-3 Install CPU 2-3-1 Glossary

Chipset (or core logic) - two or more integrated circuits which control the interfaces between the system processor, RAM, I/O devises, and adapter cards.

Processor slot/socket - the slot or socket used to mount the system processor on the motherboard.

Slot (**PCI-E**, **PCI**, **RAM**) - the slots used to mount adapter cards and system RAM.

AGP - Accelerated Graphics Port - a high speed interface for video cards; runs at 1X (66MHz), 2X (133MHz), or 4X (266MHz), or 8X (533MHz).

PCI - **P**eripheral Component Interconnect - a high speed interface for video cards, sound cards, network interface cards, and modems; runs at 33MHz.

PCI Express- Peripheral Component Interconnect Express- a high speed interface for video cards, sound cards, network interface cards, and modems.

Serial Port - a low speed interface typically used for mouse and external modems.

Parallel Port - a low speed interface typically used for printers.

PS/2 - a low speed interface used for mouse and keyboards.

USB - Universal Serial Bus - a medium speed interface typically used for mouse, keyboards, scanners, and some digital cameras.

Sound (interface) - the interface between the sound card or integrated sound connectors and speakers, MIC, game controllers, and MIDI sound devices.

LAN (interface) - Local Area Network - the interface to your local area network.

BIOS (Basic Input/Output System) - the program logic used to boot up a computer and establish the relationship between the various components.

Driver - software, which defines the characteristics of a device for use by another device or other software.

Processor - the "central processing unit" (CPU); the principal integrated circuit used for doing the "computing" in "personal computer"

Front Side Bus Frequency - the working frequency of the motherboard, which is generated by the clock generator for CPU, DRAM and PCI BUS.

CPU L2 Cache - the flash memory inside the CPU, normal it depend on CPU type.

2-3-2 About AMD Athlon64 754-pin CPU

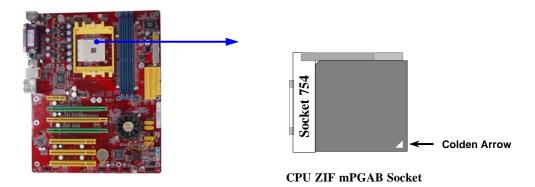
This motherboard provides a 754-pin surface mount, Zero Insertion Force (ZIF) socket, referred to as the mPGA754 socket supports AMD Athlon64 processor in the 754 Pin package utilizes Flip-Chip Pin Grid Array package technology.

The CPU that comes with the motherboard should have a cooling FAN attached to prevent overheating. If this is not the case, then purchase a correct cooling FAN before you turn on your system.

WARNING!

Be sure that there is sufficient air circulation across the processor's heatsink and CPU cooling FAN is working correctly, otherwise it may cause the processor and motherboard overheat and damage, you may install an auxiliary cooling FAN, if necessary.

To install a CPU, first turn off your system and remove its cover. Locate the ZIF socket and open it by first pulling the level sideways away from the socket then upward to a 90-degree angle. Insert the CPU with the correct orientation as shown below. The notched corner should point toward the end of the level. Because the CPU has a corner pin for two of the four corners, the CPU will only fit in the orientation as shown.



When you put the CPU into the ZIF socket. No force require to insert of the CPU, then press the level to Locate position slightly without any extra force.

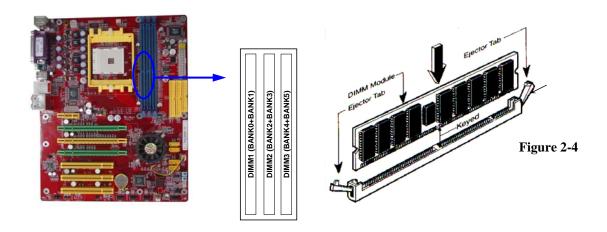
2-4 Install Memory

This motherboard provides four 184-pin DDR DUAL INLINE MEMORY MODULES (DIMM) sites for DDR memory expansion available from minimum memory size of 64MB to maximum memory size of 3.0GB DDR SDRAM.

Valid Memory Configurations

| Bank | 184-Pin DIMM | PCS | Total Memory |
|-------------------|----------------------------|-----|--------------|
| Bank 0, 1 (DIMM1) | DDRDDR266/DDR333/DDR400 | X1 | 128MB~1.0GB |
| | DDR SDRAM Module | | |
| Bank 2, 3 (DIMM2) | DDRDDR266/DDR333/DDR400 | X1 | 128MB~1.0GB |
| | DDR SDRAM Module | | |
| Bank 4, 5 (DIMM3) | DDRDDR266/DDR333/DDR400 | X1 | 128MB~1.0GB |
| | DDR SDRAM Module | | |
| Total | System Memory (Max. 3.0GB) | 3 | 128MB~3.0GB |

Generally, installing DDR SDRAM modules to your motherboard is very easy, you can refer to figure 2-4 to see what a 184-Pin DDR266/DDR333/DDR400 DDR SDRAM module looks like.



| NOTE! | should be locked into the DIMM module very firmly and fit into its indention on both sides. |
|----------|--|
| WARNING! | For the DDR SDRAM CLOCK is set at 200MHz, use only DDR400- compliant DDR Modules. When this motherboard operate at 200Mhz, most system will not even boot if non-compliant modules are used because of the strict timing issues, if your DDR Modules are not DDR400-compliant, set the SDRAM clock to 133MHz to ensure system stability. |

2-5 Expansion Cards

WARNING! Turn off your power when adding or removing expansion cards or other

system components. Failure to do so may cause severe damage to both

your motherboard and expansion cards.

2-5-1 Procedure For Expansion Card Installation

1. Read the documentation for your expansion card and make any necessary hardware or software setting for your expansion card such as jumpers.

- 2. Remove your computer's cover and the bracket plate on the slot you intend to use.
- 3. Align the card's connectors and press firmly.
- 4. Secure the card on the slot with the screen you remove above.
- 5. Replace the computer system's cover.
- 6. Set up the BIOS if necessary.
- 7. Install the necessary software driver for your expansion card.

2-5-2 Assigning IRQs For Expansion Card

Some expansion cards need an IRQ to operate. Generally, an IRQ must exclusively assign to one use. In a standard design, there are 16 IRQs available but most of them are already in use.

Standard Interrupt Assignments

| IRQ | Priority | Standard function |
|------|----------|-----------------------------|
| 0 | N/A | System Timer |
| 1 | N/A | Keyboard Controller |
| 2 | N/A | Programmable Interrupt |
| 3 * | 8 | Communications Port (COM2) |
| 4 * | 9 | Communications Port (COM1) |
| 5 * | 6 | Sound Card (sometimes LPT2) |
| 6 * | 11 | Floppy Disk Controller |
| 7 * | 7 | Printer Port (LPT1) |
| 8 | N/A | System CMOS/Real Time Clock |
| 9* | 10 | ACPI Mode when enabled |
| 10 * | 3 | IRQ Holder for PCI Steering |
| 11 * | 2 | IRQ Holder for PCI Steering |
| 12 * | 4 | PS/2 Compatible Mouse Port |
| 13 | N/A | Numeric Data Processor |
| 14 * | 5 | Primary IDE Channel |
| 15 * | 1 | Secondary IDE Channel |

^{*} These IRQs are usually available for ISA or PCI devices.

2-5-3 Interrupt Request Table For This Motherboard

Interrupt request are shared as shown the table below:

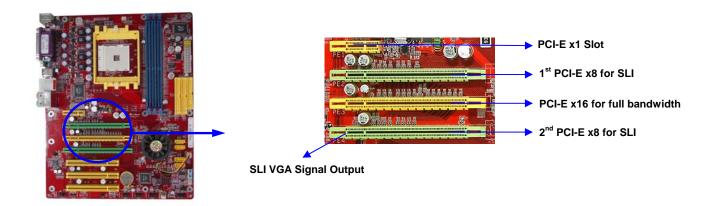
| | INT A | INT B | INT C | INT D | INT E | INT F | INT G | INT H |
|---------------|-------|-------|-----------|-----------|-------|-------|-------|-------|
| Slot 1 | | | | | | | | |
| Slot 2 | | | | | | | | |
| Slot 3 | | | | $\sqrt{}$ | | | | |
| Onboard USB 1 | √ | | | | | | | |
| Onboard USB 2 | | | | | | | | |
| AC97/MC97 | | | $\sqrt{}$ | | | | | |

IMPORTANT!

If using PCI cards on shared slots, make sure that the drivers support "Shared IRQ" or that the cards don't need IRQ assignments. Conflicts will arise between the two PCI groups that will make the system unstable or cards inoperable.

2-5-4 PCI Express Slot

This motherboard provides three 16-lane PCI Express slot intended for Graphics Attach(Two symmetrical PCI-Express x16 graphics slots in light green deliver up to 2Gbyte/sec data transfer rate at each relative direction which is fully compatible with the latest NVIDIA SLI Technology. One PCI-Express x16 graphics slot offers 4Gbyte/sec data transfer rate at each relative direction.), and one x1 PCI Express Slot. Fully compliant to the *PCI Express Base Specification revision 1.0a*, support PCI Express VGA card, and other PCI Express device.



IMPORTANT!

Please make sure the major display output is out from the NVIDIA SLI Supported VGA Card installed in the 2^{nd} PCI EXPRESS x8 for SLI slot in light green while activating the SLI technology with one pair NVIDIA SLI Supported VGA Cards . And install the single PCI EXPRESS x16 interface supported VGA card to get the full speed of x16 bandwidth in the PCI EXPRESS x16 slot in light yellow as being marked above.

2-6 Connectors, Headers

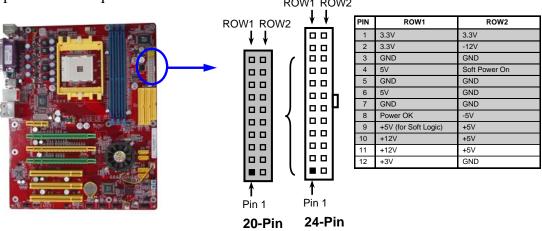
2-6-1 Connectors

(1) Power Connector (24-pin block): ATXPWR24P

ATX Power Supply connector. This is a new defined 24-pins connector that usually comes with ATX case. The ATX Power Supply allows to use soft power on momentary switch that connect from the front panel switch to 2-pins Power On jumper pole on the motherboard. When the power switch on the back of the ATX power supply turned on, the full power will not come into the system board until the front panel switch is momentarily pressed. Press this switch again will turn off the power to the system board.

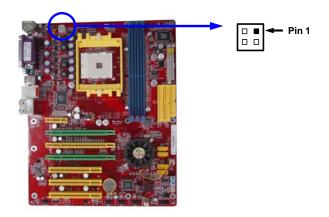
- ** We recommend that you use an ATX 12V Specification 2.0-compliant power supply unit (PSU) with a minimum of 350W power rating. This type has 24-pin and 4-pin power plugs.
- ** If you intend to use a PSU with 20-pin and 4-pin power plugs, make sure that the 20-pin power plug can provide at least 15A on +12V and the power supply unit has a minimum power rating of 350W. The system may become unstable or may not boot up if the power is inadequate.

 ROW1 ROW2



(2) ATX 12V Power Connector (4-pin block): ATX12V

This is a new defined 4-pins connector that usually comes with ATX Power Supply. The ATX Power Supply which fully support Pentium 4 processor must including this connector for support extra 12V voltage to maintain system power consumption. Without this connector might cause system unstable because the power supply can not provide sufficient current for system.



(3) PS/2 Mouse & PS/2 Keyboard Connector: PS2KBMS

The connectors for PS/2 keyboard and PS/2 Mouse.

(4) USB Port connector: USB0/USB1

The connectors are 4-pin connector that connect USB devices to the system board.

(5) LAN Port connector: UL1/USBLAN

This connector is standard RJ45 connector for Network
The UL1 support 10M/100Mb/s data transfer rate (only for Odin K8F8P)
The USBLAN support 10M/100M/1G b/s data transfer rate (only for K8F8G)

(6) Parallel Port Connector (25-pin female): PARALLEL

Parallel Port connector is a 25-pin D-Subminiature Receptacle connector. The Onboard Parallel Port can be disabled through the BIOS SETUP. Please refer to Chapter 3 "INTEGRATED PERIPHERALS SETUP" section for more detail information.

(7) Audio Line-In, Lin-Out, MIC, Surrback, Surround, CEN/LEF Connector: J1

This Connector are 6 phone Jack for LINE-OUT, LINE-IN, MIC, Surrback, Surround, CEN/LEF

Line-in: (BLUE)Audio input to sound chipLine-out: (GREEN)Audio output to speakerMIC: (PINK)Microphone Connector

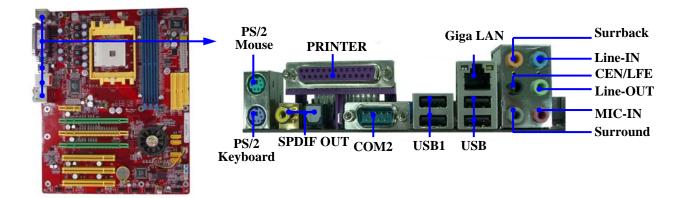
Surrback : (ORANGE) Audio output to speaker-Rear speaker out

CEN/LEF: (BLACKNESS) Audio output to speaker-Center/Subwoofer speaker out

Surround: (**GRAY**) Audio output to speaker-Side speaker out

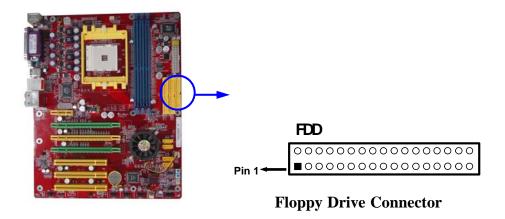
(8) Serial Port COM1/COM2 : COM1/COM2

COM1 is the 9-pin D-Subminiature male connector. The On-board serial port can be disabled through BIOS SETUP. Please refer to Chapter 3 "INTEGRATED PERIPHERALS SETUP" section for more detail information.



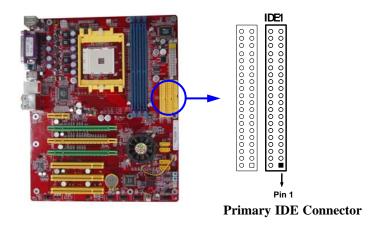
(9) Floppy drive Connector (34-pin block): FDD

This connector supports the provided floppy drive ribbon cable. After connecting the single plug end to motherboard, connect the two plugs at other end to the floppy drives.



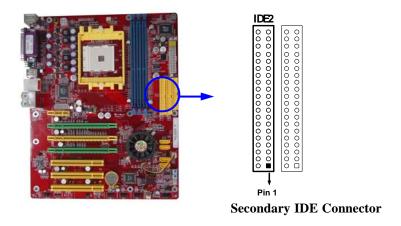
(10) Primary IDE Connector (40-pin block): IDE1

This connector supports the provided IDE hard disk ribbon cable. After connecting the single plug end to motherboard, connect the two plugs at other end to your hard disk(s). If you install two hard disks, you must configure the second drive to Slave mode by setting its jumpers accordingly. Please refer to the documentation of your hard disk for the jumper settings.



(11) Secondary IDE Connector (40-pin block): IDE2

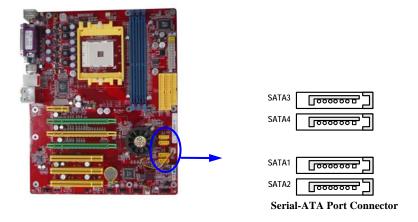
This connector connects to the next set of Master and Slave hard disks. Follow the same procedure described for the primary IDE connector. You may also configure two hard disks to be both Masters using one ribbon cable on the primary IDE connector and another ribbon cable on the secondary IDE connector.



- Two hard disks can be connected to each connector. The first HDD is referred to as the "Master" and the second HDD is referred to as the "Slave".
- For performance issues, we strongly suggest you don't install a CD-ROM or DVD-ROM drive on the same IDE channel as a hard disk. Otherwise, the system performance on this channel may drop.

(12) Serial-ATA Port connector: SATA1/SATA2

This connector support the provided Serial ATA IDE hard disk cable to connecting the motherboard and serial ATA hard disk.



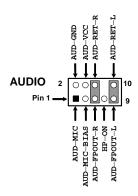
2-6-2 Headers

(1) Line-Out/MIC Header for Front Panel (9-pin): AUDIO

This header connect to Front Panel Line-out, MIC connector with cable.

Without install the cable, this header default setting is 5-6 short, 9-10 short. When you install the cable you have take off these jumpers.

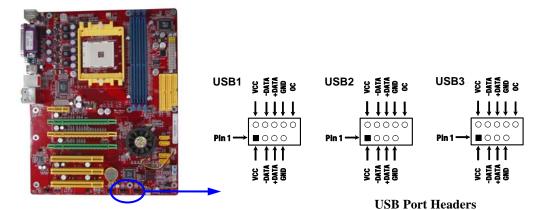




Line-Out, MIC Headers

(2) USB Port Headers (9-pin): USB1/USB2/USB3

These headers are used for connecting the additional USB port plug. By attaching an option USB cable, your can be provided with two additional USB plugs affixed to the back panel.



(3) Speaker connector: SPEAK

This 4-pin connector connects to the case-mounted speaker. See the figure below.

(4) Power LED: PWR LED

The Power LED is light on while the system power is on. Connect the Power LED from the system case to this pin.

(5) IDE Activity LED: HD LED

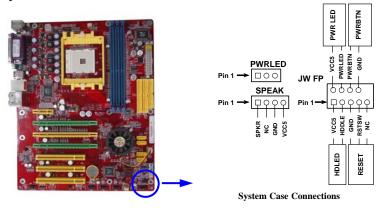
This connector connects to the hard disk activity indicator light on the case.

(6) Reset switch lead: RESET

This 2-pin connector connects to the case-mounted reset switch for rebooting your computer without having to turn off your power switch. This is a preferred method of rebooting in order to prolong the lift of the system's power supply. See the figure below.

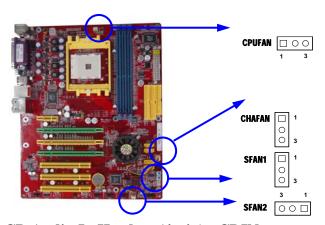
(7) Power switch: PWR BTN

This 2-pin connector connects to the case-mounted power switch to power ON/OFF the system.



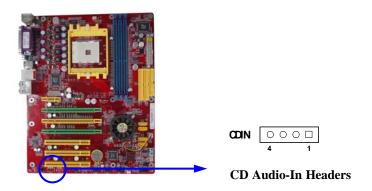
(8) FAN Headers (3-pin): CHAFAN, SFAN1, SFAN2, CPUFAN

These connectors support cooling fans of 350mA (4.2 Watts) or less, depending on the fan manufacturer, the wire and plug may be different. The red wire should be positive, while the black should be ground. Connect the fan's plug to the board taking into consideration the polarity of connector.



(9) CD Audio-In Headers (4-pin): CDIN

CDIN are the connectors for CD-Audio Input signal. Please connect it to CD-ROM CD-Audio output connector.



2-7 Starting Up Your Computer

- 1. After all connection are made, close your computer case cover.
- 2. Be sure all the switch are off, and check that the power supply input voltage is set to proper position, usually in-put voltage is 220V~240V or 110V~120V depending on your country's voltage used.
- 3. Connect the power supply cord into the power supply located on the back of your system case according to your system user's manual.
- 4. Turn on your peripheral as following order:
 - a. Your monitor.
 - b. Other external peripheral (Printer, Scanner, External Modem etc...)
 - c. Your system power. For ATX power supplies, you need to turn on the power supply and press the ATX power switch on the front side of the case.
- 5. The power LED on the front panel of the system case will light. The LED on the monitor may light up or switch between orange and green after the system is on. If it complies with green standards or if it is has a power standby feature. The system will then run power-on test. While the test are running, the BIOS will alarm beeps or additional message will appear on the screen.

If you do not see any thing within 30 seconds from the time you turn on the power. The system may have failed on power-on test. Recheck your jumper settings and connections or call your retailer for assistance.

| Веер | Meaning |
|---------------------------------------|---|
| One short beep when displaying logo | No error during POST |
| Long beeps in an endless loop | No DRAM install or detected |
| One long beep followed by three short | Video card not found or video card memory |
| beeps | bad |
| High frequency beeps when system is | CPU overheated |
| working | System running at a lower frequency |

- 6. During power-on, press <Delete> key to enter BIOS setup. Follow the instructions in BIOS SETUP.
- 7. **Power off your computer:** You must first exit or shut down your operating system before switch off the power switch. For ATX power supply, you can press ATX power switching after exiting or shutting down your operating system. If you use Windows 9X, click "Start" button, click "Shut down" and then click "Shut down the computer?" The power supply should turn off after windows shut down.

Chapter 3

Introducing BIOS

The BIOS is a program located on a Flash Memory on the motherboard. This program is a bridge between motherboard and operating system. When you start the computer, the BIOS program gain control. The BIOS first operates an auto-diagnostic test called POST (power on self test) for all the necessary hardware, it detects the entire hardware device and configures the parameters of the hardware synchronization. Only when these tasks are completed done it gives up control of the computer to operating system (OS). Since the BIOS is the only channel for hardware and software to communicate, it is the key factor for system stability, and in ensuring that your system performance as its best.

In the BIOS Setup main menu of Figure 3-1, you can see several options. We will explain these options step by step in the following pages of this chapter, but let us first see a short description of the function keys you may use here:

- Press <Esc> to quit the BIOS Setup.
- Press $\uparrow \downarrow \longleftrightarrow$ (up, down, left, right) to choose, in the main menu, the option you want to confirm or to modify.
- Press <F10> when you have completed the setup of BIOS parameters to save these parameters and to exit the BIOS Setup menu.
- Press Page Up/Page Down or +/– keys when you want to modify the BIOS parameters for the active option.

3-1 Entering Setup

Power on the computer and by pressing immediately allows you to enter Setup.

If the message disappears before your respond and you still wish to enter Setup, restart the system to try again by turning it OFF then ON or pressing the "RESET" button on the system case. You may also restart by simultaneously pressing <Ctrl>, <Alt> and <Delete> keys. If you do not press the keys at the correct time and the system does not boot, an error message will be displayed and you will again be asked to

Press <F1> to continue, <Ctrl-Alt-Esc> or to enter Setup

3-2 Getting Help

Main Menu

The on-line description of the highlighted setup function is displayed at the bottom of the screen.

Status Page Setup Menu/Option Page Setup Menu

Press F1 to pop up a small help window that describes the appropriate keys to use and the possible selections for the highlighted item. To exit the Help Window, press <Esc>.

3-3 The Main Menu

Once you enter Award® BIOS CMOS Setup Utility, the Main Menu (Figure 3-1) will appear on the screen. The Main Menu allows you to select from fourteen setup functions and two exit choices. Use arrow keys to select among the items and press <Enter> to accept or enter the sub-menu.

Phoenix - AwardBIOS CMOS Setup Utility

| Standard CMOS Features | Thermal Throttling Options |
|------------------------------|--|
| Advanced BIOS Features | Power User Overclock Settings |
| Advanced Chipset Features | Password Settings |
| Integrated Peripherals | Load Optimized Defaults |
| Power Management Setup | Load Standard Defaults |
| Miscellaneous Control | Save & Exit Setup |
| PC Health Status | Exit Without Saving |
| Esc : Quit F9 : Menu in BIOS | $\uparrow \downarrow \rightarrow \leftarrow$: Select Item |
| F10 : Save & Exit Setup | |
| | |

Figure 3-1

Standard CMOS Features

Use this Menu for basic system configurations.

Advanced BIOS Features

Use this menu to set the Advanced Features available on your system.

Advanced Chipset Features

Use this menu to change the values in the chipset registers and optimize your system's performance.

Integrated Peripherals

Use this menu to specify your settings for integrated peripherals.

Power Management Setup

Use this menu to specify your settings for power management.

Miscellaneous Control

Use this menu to specify your settings for Miscellaneous control.

PC Health Status

This entry shows your PC health status.

Thermal Throttling Options

The selection is set for activating the active CPU Thermal Protection by flexible CPU loading adjustment in the arrange of temperature you define.

Power User Overclock Settings

Use this menu to specify your settings (frequency, Voltage) for overclocking demand

Password Settings

This entry for setting Supervisor password and User password

Load Optimized Defaults

Use this menu to load the BIOS default values these are setting for optimal performances system operations for performance use.

Load Standard Defaults

Use this menu to load the BIOS default values for the stable performance system operation that are factory settings for normal use.

Save & Exit Setup

Save CMOS value changes to CMOS and exit setup.

Exit Without Saving

Abandon all CMOS value changes and exit setup.

3-4 Standard CMOS Features

The items in Standard CMOS Setup Menu are divided into several categories. Each category includes no, one or more than one setup items. Use the arrow keys to highlight the item and then use the <PgUp> or <PgDn> keys to select the value you want in each item.

Phoenix - AwardBIOS CMOS Setup Utility
Standard CMOS Features

| - | beandard CHOD reacures | |
|---|--|---|
| Date (mm:dd:yy) Time (hh:mm:ss) | Wed, Mar, 09 2005 16: 48: 35 | Item Help |
| > IDE Channel 0 Master > IDE Channel 0 Slave > IDE Channel 1 Master > IDE Channel 1 Slave > SATA Channel 1 > SATA Channel 2 > SATA Channel 3 > SATA Channel 4 | None None None None None None None None | Menu Level > Change the day, month, year and century |
| Drive A Drive B Halt On Base Memory Extended Memory Total Memory | 1.44M, 3.5 in. None All, But Keyboard 640K 522240K 523264K | |
| ↑↓→← Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5:Previous Values F6:Optimized Defaults F7:Standard Defaults | | |

Date

The date format is <day><month><date><year>.

Day Day of the week, from Sun to Sat, determined by BIOS. Read-only.

Month The month from Jan. through Dec.

Date The date from 1 to 31 can be keyed by numeric function keys.

Year The year depends on the year of the BIOS.

Time

The time format is <hour><minute><second>.

IDE Channel 0 Master / Channel 0 Slave / Channel 1 Master / Channel 0 Slave SATA Channel 1, 2, 3, 4

Press PgUp/<+> or PgDn/<-> to select Manual, None, Auto type. Note that the specifications of your drive must match with the drive table. The hard disk will not work properly if you enter improper information for this category. If the type of hard disk drives is not matched or listed, you can use Manual to define your own drive type manually.

If you select Manual, related information is asked to be entered to the following items. Enter the information directly from the keyboard. This information should be provided in the documentation from your hard disk vendor or the system manufacturer.

If the controller of HDD interface is SCSI, the selection shall be "None".

If the controller of HDD interface is CD-ROM, the selection shall be "None"

Access Mode The settings are Auto Normal, Large, and LBA.

Cylinder number of cylinders

Head number of headsPrecomp write precompLanding Zone landing zoneSector number of sectors

3-5 Advanced BIOS Features

Phoenix - AwardBIOS CMOS Setup Utility

Advanced BIOS Features

| Removable Device Priority | Enter | |
|--|--------------------------|-------------------------|
| Hard Disk Boot Priority | Enter | Item Help |
| Virus Warning | Disabled | |
| L1 Cache | Enabled | |
| L2 Cache | Enabled | Menu Level > |
| Quick Power On Self Test | Enabled | |
| SATA &SCSI Boot Order | SATA, SCSI | |
| First Boot Device | Floppy | |
| Second Boot Device | HDD-0 | |
| Third Boot Device | CDROM | |
| Boot other Device | Enabled | |
| Swap Floppy Drive | Disabled | |
| Boot Up Floppy Seek | Enabled | |
| Boot Up NumLock Status | On | |
| Typematic Rate Setting | Disabled | |
| Typematic Rate (Chars/Sec) | 6 | |
| Typematic Delay (Msec) | 250 | |
| Security Option | Setup | |
| APIC Mode | Enabled | |
| MPS Version Control For OS | 1.4 | |
| OS Select For DRAM > 64MB | Non-OS2 | |
| HDD S.M.A.R.T. Capability | Disabled | |
| Report No FDD For Windows | Yes | |
| Small Logo (EPA) Show | Enabled | |
| | | |
| $\uparrow \downarrow \rightarrow \leftarrow$ Move Enter:Select +/- | -/PU/PD:Value F10:Save E | SC:Exit F1:General Help |
| F5:Previous Values F6 | :Optimized Defaults | F7:Standard Defaults |

Virus Warning

Allows you to choose the VIRUS Warning feature for IDE Hard Disk boot sector protection. If this function is enabled and someone attempt to write data into this area, BIOS will show a warning message on screen and alarm beep.

Disabled (default) No warning message to appear when anything attempts to access the

boot sector or hard disk partition table.

Enabled Activates automatically when the system boots up causing a warning

message to appear when anything attempts to access the boot sector

of hard disk partition table.

CPU Internal Cache

The default value is Enabled.

Enabled (default) Enable cache **Disabled** Disable cache

Note: The internal cache is built in the processor.

External Cache

Choose Enabled or Disabled. This option enables the Level 2 cache memory.

Quick Power On Self-Test

This category speeds up Power On Self Test (POST) after you power on the computer. If this is set to Enabled. BIOS will shorten or skip some check items during POST.

Enabled (default) Enable quick POST **Disabled** Normal POST

First/Second/Third/Fourth Boot Device

The BIOS attempts to load the operating system from the devices in the sequence selected in these items. The settings are Floppy, LS/ZIP, HDD-0/HDD-1/HDD-3, SCSI, CDROM, LAD and Disabled.

Swap Floppy Drive

Switches the floppy disk drives between being designated as A and B. Default is Disabled.

Boot Up Floppy Seek

During POST, BIOS will determine if the floppy disk drive installed is 40 or 80 tracks. 360K type is 40 tracks while 760K, 1.2M and 1.44M are all 80 tracks.

Boot Up NumLock Status

The default value is On.

On (default) Keypad is numeric keys.

Off Keypad is arrow keys.

Typematic Rate Setting

Keystrokes repeat at a rate determined by the keyboard controller. When enabled, the typematic rate and typematic delay can be selected. The settings are: Enabled/Disabled.

Typematic Rate (Chars/Sec)

Sets the number of times a second to repeat a keystroke when you hold the key down. The settings are: 6, 8, 10, 12, 15, 20, 24, and 30.

Typematic Delay (Msec)

Sets the delay time after the key is held down before is begins to repeat the keystroke. The settings are 250, 500, 750, and 1000.

Security Option

This category allows you to limit access to the system and Setup, or just to Setup.

System The system will not boot and access to Setup will be denied if the

correct password is not entered at the prompt.

Setup (default) The system will boot, but access to Setup will be denied if the correct

password is not entered prompt.

3-6 Advanced Chipset Features

The Advanced Chipset Features Setup option is used to change the values of the chipset registers. These registers control most of the system options in the computer.

Phoenix - AwardBIOS CMOS Setup Utility
Advanced Chipset Features

| > DRAM Timing Settings > LDT & PCI Timing Settings | s Press Enter | Item Help |
|--|---------------|--------------|
| System BIOS Cacheable Memory Hole | | Menu Level > |
| | | |
| | | |
| ↑↓→← Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5:Previous Values F6:Optimized Defaults F7:Standard Defaults | | |

DRAM Timing Settings

Please refer to section 3-6-1

LDT & PCI Timing Settings

Please refer to section 3-6-2

System BIOS Cacheable

Selecting Enabled allows caching of the system BIOS ROM at F0000h-FFFFFh, resulting in better system performance. However, if any program writes to this memory area, a system error may result. The settings are: Enabled and Disabled.

Memory Hole

You can reserve this area of system memory for ISA adapter ROM. When this area is reserved, it cannot be cached. The user information of peripherals that need to use this area of system memory usually discusses their memory requirements. The settings are: Enabled and Disabled.

3-6-1 DRAM Timing Settings

Phoenix - AwardBIOS CMOS Setup Utility

DRAM Timing Settings

| Timing Mode DRAM CAS Latency | Auto 2.5T | Item Help |
|--|---|---------------|
| SDRAM Cycle Time SDRAM RAS-to-CAS Delay SDRAM Precharge Time | 8T 4T 2T | Menu Level >> |
| MTRR Mapping Mode | Continuous | |
| | | |
| | | |
| ↑↓→← Move Enter:Select +/- F5:Previous Values | -/PU/PD:Value F10:Save F F6:Optimized Defaults | |

SDRAM RAS-to-CAS Delay

This field let's you insert a timing delay between the CAS and RAS strobe signals, used when DRAM is written to, read from, or refreshed. *Fast* gives faster performance; and *Slow* gives more stable performance. This field applies only when synchronous DRAM is installed in the system. The settings are: 4T and 3T.

SDRAM Precharge Time

If an insufficient number of cycles is allowed for the RAS to accumulate its charge before DRAM refresh, the refresh may be incomplete and the DRAM may fail to retain date. *Fast* gives faster performance; and *Slow* gives more stable performance. This field applies only when synchronous DRAM is installed in the system. The settings are: 2T and 3T.

DRAM CAS Latency

When synchronous DRAM is installed, the number of clock cycles of CAS latency depends on the DRAM timing. The settings are: 2T and 2.5T.

3-6-2 LDT & PCI Timing Settings

Phoenix - AwardBIOS CMOS Setup Utility

LDT & PCI Timing Settings

PCI Delay Transaction

The chipset has an embedded 32-bit posted write buffer to support delay transactions cycles. Select Enabled to support compliance with PCI specification version 2.1. The settings are: Enabled and Disabled.

3-7 Integrated Peripherals

Phoenix - AwardBIOS CMOS Setup Utility

Integrated Peripherals

| > OnChip IDE Function > OnChip Device Function | Press Enter Press Enter | Item Help |
|---|----------------------------|-------------------------|
| > Onboard Super IO Function Init Display First | Press Enter PCI Slot | Menu Level > |
| ↑↓→← Move Enter:Select +/-/ | /PII/PD:Value F10:Save F | SC:Exit El:General Help |
| F5:Previous Values F6: | | - |

OnChip IDE Function

Please refer to section 3-7-1

OnChip Device Function

Please refer to section 3-7-2

Onboard Super IO Function

Please refer to section 3-7-3

Init Display First

This item allows you to decide to activate whether PCI Slot or AGP VGA first. The settings are: PCI Slot, AGP Slot.

3-7-1 OnChip IDE Function

Phoenix - AwardBIOS CMOS Setup Utility

OnChip IDE Function

| SATA Function OnChip IDE Channel0 | Enabled Enabled | Item Help |
|--------------------------------------|---------------------------|-------------------------|
| OnChip IDE Channel1 | Enabled | |
| Primary Master PIO | Auto | Menu Level >> |
| Primary Slave PIO | Auto | nona zevez 🔑 |
| Secondary Master PIO | Auto | |
| Secondary Slave PIO | Auto | |
| Primary Master UDMA | Auto | |
| Primary Slave UDMA | Auto | |
| Secondary Master UDMA | Auto | |
| Secondary Slave UDMA | Auto | |
| IDE DMA Transfer Access | Enabled | |
| IDE HDD Block Mode | Enabled | |
| IDE Prefetch Mode | Disabled | |
| | | |
| | | |
| ↑↓→← Move Enter:Select + | /-/PU/PD:Value F10:Save E | SC:Exit F1:General Help |
| F5:Previous Values | F6:Optimized Defaults | F7:Standard Defaults |

OnChip IDE Channal0/Channel1

The integrated peripheral controller contains an IDE interface with support for two IDE channels. Select *Enabled* to activate each channel separately. The settings are: Enabled and Disabled.

Primary/Secondary Master/Slave PIO

The four IDE PIO (Programmed Input/Output) fields let you set a PIO mode (0-4) for each of the four IDE devices that the onboard IDE interface supports. Modes 0 through 4 provide successively increased performance. In Auto mode, the system automatically determines the best mode for each device. The settings are: Auto, Mode 0, Mode 1, Mode 2, Mode 3, Mode 4.

Primary/Secondary Master/Slave UDMA

Ultra DMA/33 implementation is possible only if your IDE hard drive supports it and the operating environment includes a DMA driver (Windows 95 OSR2 or a third-party IDE bus master driver). If your hard drive and your system software both support Ultra DMA/33 and Ultra DMA/66, select Auto to enable BIOS support. The settings are: Auto, Disabled.

IDE HDD Block Mode

Block mode is also called block transfer, multiple commands, or multiple sector read/write. If your IDE hard drive supports block mode (most new drives do), select Enabled for automatic detection of the optimal number of block read/writes per sector the drive can support. The settings are: Enabled, Disabled.

3-7-2 OnChip Device Function

Phoenix - AwardBIOS CMOS Setup Utility
OnChip Device Function

| VIA SATA Function RealTek LAN BootROM | Enabled Disabled | Item Help |
|--|---|---------------|
| VIA LAN Function VIA LAN BootROM VIA LAN BootROM Boot Option | | Menu Level >> |
| VIA LAN BootROM PXERPL Option Current VIA MAC Address is 00 VIA MAC Address Input AC97 Sound Device USB Host Controller USB 2.0 Support USB Device Legacy Support USB Keyboard Legacy Support USB Mouse Legacy Support | 3018-xxxxxx Press Enter Auto All Enabled Enabled All Off Disabled | |
| ↑↓→← Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5:Previous Values F6:Optimized Defaults F7:Standard Defaults | | |

AC97 Sound Device

This item allows you to decide to enable/disable the chipset family to support AC97 Audio. The settings are: Enabled, Disabled.

USB Host Controller

Select *Enabled* if your system contains a Universal Serial Bus (USB) controller and you have a USB peripherals. The settings are: Enabled, Disabled.

USB Keyboard Legacy Support

Select *Enabled* if your system contains a Universal Serial Bus (USB) controller and you have a USB keyboard. The settings are: Enabled, Disabled.

3-7-3 Onboard Super IO Function

Phoenix - AwardBIOS CMOS Setup Utility

Onboard Super IO Function

| Onboard FDD Controller Onboard Serial Port 1 | Enabled 3F8/IRQ4 | Item Help |
|--|----------------------------|---------------|
| Onboard Serial Port 2 UART Mode Select IR Duplex Mode | 2F8/IRQ3 Normal Half | Menu Level >> |
| Onboard Parallel Port Parallel Mode ECP Mode Use DMA | 378/IRQ7 SPP 3 | |
| ↑↓→← Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5:Previous Values F6:Optimized Defaults F7:Standard Defaults | | |

Onboard FDD Controller

Select Enabled if your system has a floppy disk controller (FDD) installed on the system board and you wish to use it. If you install add-on FDC or the system has no floppy drive, select Disabled in this field. The settings are: Enabled and Disabled.

Onboard Serial Port 1/Port 2

Select an address and corresponding interrupt for the first and the second serial ports. The settings are: 3F8/IRQ4, 2E8/IRQ3, 3E8/IRQ4, 2F8/IRQ3, Disabled, Auto.

Onboard Parallel Port

There is a built-in parallel port on the on-board Super I/O chipset that Provides Standard, ECP, and EPP features. It has the following option:

Disabled

(3BCH/IRQ7)/ Line Printer port 0 (278H/IRQ5)/ Line Printer port 2 (378H/IRQ7) Line Printer port 1

Parallel Port Mode

SPP : Standard Parallel PortEPP : Enhanced Parallel PortECP : Extended Capability Port

SPP/EPP/ECP/ECP+EPP

To operate the onboard parallel port as Standard Parallel Port only, choose "SPP." To operate the onboard parallel port in the EPP modes simultaneously, choose "EPP." By choosing "ECP", the onboard parallel port will operate in ECP mode only. Choosing "ECP+EPP" will allow the onboard parallel port to support both the ECP and EPP modes simultaneously. The ECP mode has to use the DMA channel, so choose the onboard parallel port with the ECP feature. After selecting it, the following message will appear: "ECP Mode Use DMA" at this time, the user can choose between DMA channels 3 to 1. The onboard parallel port is EPP Spec. compliant, so after the user chooses the onboard parallel port with the EPP function, the following message will be displayed on the screen: "EPP Mode Select." At this time either EPP 1.7 spec. or EPP 1.9 spec. can be chosen.

3-8 Power Management Setup

The Power Management Setup allows you to configure your system to most effectively save energy saving while operating in a manner consistent with your own style of computer use.

Phoenix - AwardBIOS CMOS Setup Utility

| ys Off SYNC+Blank Menu Level > ant-Off ys Off |
|---|
| ant-Off |
| |
| |
| vs Off |
| 2 |
| |
| s Enter |
| alue F10:Save ESC:Exit F1:General Help |
| 3 |

ACPI Function

This item allows you to Enabled/Disabled the Advanced Configuration and Power Management (ACPI). The settings are Enabled and Disabled.

Video Off Option

This determines the manner in which the monitor is blanked. The choice are Suspend \rightarrow off, All Modes \rightarrow Off, and Always On.

Video Off Method

This determines the manner in which the monitor is blanked.

DPMS (default) Initial display power management signaling. **Blank Screen** This option only writes blanks to the video buffer.

V/H SYNC+Blank This selection will cause the system to turn off the vertical and horizontal synchronization ports and write blanks to the video buffer.

Modem Use IRQ

This determines the IRQ in which the MODEM can use.

The settings are: 3, 4, 5, 7, 9, 10, 11, NA.

Power Button Function

Pressing the power button for more than 4 seconds forces the system to enter the Soft-Off state. The settings are: Delay 4 Sec, Instant-Off.

Wake Up Events

Please refer to section 3-8-1

3-8-1 Wake up Events

Phoenix - AwardBIOS CMOS Setup Utility
Wake Up Events

| VGA LPT & COM | OFF LPT/COM | Item Help |
|---|---------------------------------------|---------------|
| HDD & FDD PCI Master Wake-Up on Ring | ON OFF Disabled | Menu Level >> |
| PS2 KB Wake up Selection Wake-Up on Hot Key (PS2 KB) Wake-Up on USB Device Wake-Up on RTC Alarm Date of Month Alarm Time (hh:mm:ss) Alarm | Disabled Disabled Disabled 0 | |
| ↑↓→← Move Enter:Select +/-/F F5:Previous Values F6: | | - |

Wake Up On Ring/PME

During Disabled, the system will ignore any incoming call from the modem. During Enabled, the system will boot up if there's an incoming call from the modem.

Wake-Up on RTC Alarm

This function is for setting date and time for your computer to boot up. During Disabled, you cannot use this function. During Enabled, choose the Date and Time Alarm:

Date(of month) Alarm

You can choose which month the system will boot up. Set to 0, to boot every day.

Time(hh:mm:ss) Alarm

You can choose what hour, minute and second the system will boot up.

Note: If you have change the setting, you must let the system boot up until it goes to the operating system, before this function will work.

IROs Activities

Please refer to section 3-8-1.1

3-8-1.1 IRQs Activities

Phoenix - AwardBIOS CMOS Setup Utility
IRQs Activities

| Primar | y INTR | ON | Th 77-1- |
|----------|---------------------|-------------------------|----------------------------|
| IRQ3 | (COM 2) | Disabled | Item Help |
| IRQ4 | (COM 1) | Enabled | |
| IRQ5 | (LPT 2) | Enabled | W 7 7 |
| IRQ6 | (Floppy Disk) | Enabled | Menu Level >>> |
| IRQ7 | (LPT 1) | Enabled | |
| IRQ8 | (RTC Alarm) | Disabled | |
| IRQ9 | (IRQ2 Redir) | Disabled | |
| IRQ10 | (Reserved) | Disabled | |
| IRQ11 | (Reserved) | Disabled | |
| IRQ12 | (PS/2 Mouse) | Enabled | |
| IRQ13 | (Coprocessor) | Enabled | |
| IRQ14 | (Hard Disk) | Enabled | |
| IRQ15 | (Reserved) | Disabled | |
| <u> </u> | Move Enter:Select + | /-/PII/PD:Value F10:Sav | e ESC:Exit F1:General Help |
| | | F6:Optimized Defaults | _ |

3-9 Miscellaneous Control

This section is for setting CPU Frequency/Voltage Control.

Phoenix - AwardBIOS CMOS Setup Utility
Miscellaneous Control

| Auto Detect PCI Clock Spread Spectrum | Enabled Disabled | Item Help | | | |
|--|---------------------|----------------------|--|--|--|
| Flash Part Write Protect | Enabled | | | | |
| > TPO Regerrange | Press Enter | Menu Level > | | | |
| > IRQ Resources | Fress Enter | | | | |
| PCI/VGA Palette Snoop | Disabled | | | | |
| *** PCIExpress Relative Items ** | * | | | | |
| Maximum Payload Size | 4096 | | | | |
| ↑↓→← Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help | | | | | |
| F5:Previous Values F6:Op | ptimized Defaults | F7:Standard Defaults | | | |

Auto Detect PCI Clock

This item allows you to enable/disable auto detect PCI Clock.

Spread Spectrum

This item allows you to set the CPU Host/PCI clock and Spread Spectrum.

The settings are: Enabled, Disabled.

IRQ Resources

When resources are controlled manually, assign each system interrupt a type, depending on the type of device using the interrupt.

Please refer to section 3-9-1

3-9-1 IRQ Resources

Phoenix - AwardBIOS CMOS Setup Utility

IRQ Resources

| IRQ-3 | assigned to | PCI Device | Item Help |
|--|-------------------|--------------------------|--------------------------|
| IRQ-4 | assigned to | PCI Device | icem Heip |
| IRQ-5 | assigned to | PCI Device | |
| IRQ-7 | assigned to | PCI Device | Menu Level >> |
| IRQ-9 | assigned to | PCI Device | |
| IRQ-10 | assigned to | PCI Device | |
| IRQ-11 | assigned to | PCI Device | |
| IRQ-12 | assigned to | PCI Device | |
| IRQ-14 | assigned to | PCI Device | |
| IRQ-15 | assigned to | PCI Device | |
| | | | |
| $\uparrow \downarrow \rightarrow \leftarrow$ | Move Enter:Select | +/-/PU/PD:Value F10:Save | ESC:Exit F1:General Help |
| F5 | :Previous Values | F6:Optimized Defaults | F7:Standard Defaults |

3-10 PC Health Status

This section shows the Status of you CPU, Fan, Warning for overall system status. This is only available if there is Hardware Monitor onboard.

Phoenix - AwardBIOS CMOS Setup Utility
PC Health Status

| Show PCHealth in Post | Enabled | Item Help |
|----------------------------|------------|--------------|
| > Smart FAN Configurations | | |
| Vcore | 1.39V | Menu Level > |
| VDIMM 2.5V | 2.53V | |
| +3.3V | 3.28V | |
| +5V | 4.99V | |
| +12V | 11.9V | |
| 3VSB | 3.22V | |
| 5VSB | 4.89V | |
| VBAT | 3.12V | |
| CPU Temperature | 40°C/104°F | |
| System Temperature | 29°C/84°F | |
| CPUFAN | 4440 RPM | |
| SFAN1 | 3375 RPM | |
| SFAN2 | 3375 RPM | |

Shutdown Temperature

This item can let users setting the Shutdown temperature, when CPU temperature over this setting the system will auto shutdown to protect CPU.

Show PC Health in Post

During Enabled, it displays information list below. The choice is either Enabled or Disabled

Smart FAN Configurations

Please refer to section 3-10-1

Current CPU Temperature/Current System Temp/Current FAN1, FAN2 Speed/Vcore/Vdd/3.3V/+5V/+12V/-12V/VBAT(V)/5VSB(V)

This will show the CPU/FAN/System voltage chart and FAN Speed.

3-10-1 Smart FAN Configurations

Phoenix - AwardBIOS CMOS Setup Utility
Smart FAN Configurations

| CPUFAN Smart Mode CPU SmartFAN Full-Speed | Enabled 50 | Item Help |
|---|---------------------|--------------------------|
| CPU SmartFAN Idle Temp SFAN1 Smart Mode | 30 Enabled | Menu Level > |
| SFAN1 SmartFAN Full-Speed Temp | | |
| | | |
| ↑↓→← Move Enter:Select +/-/Pi | J/PD:Value F10:Save | ESC:Exit F1:General Help |
| F5:Previous Values F6:0 | | |

CPU/SFAN1 Smart FAN Full-Speed Temp

This item allows you setting the FAN works in full speed when the temperature over the value which out set. If the temperature below the value but over the Idle Temperature, the FAN will works over 60% of full speed, and the higher temperature will gain higher FAN speed, after over the temperature which this item setting, the FAN works in full speed.

CPU/SFAN1 Smart FAN Idle Temp

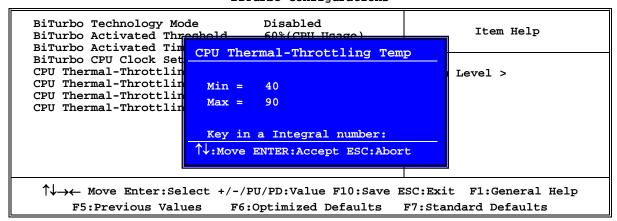
This item allows you setting the FAN works in 60% of full speed, when the temperature lower than the temperature which you setting.

3-11 Thermal Throttling Options

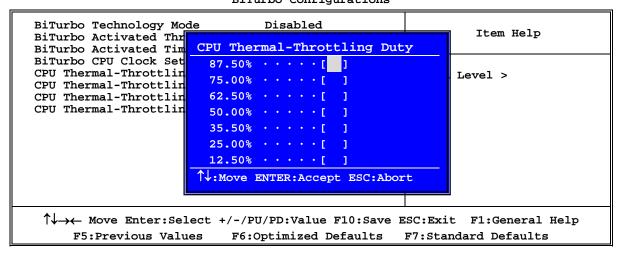
Phoenix - AwardBIOS CMOS Setup Utility
BiTurbo Configurations

```
BiTurbo Technology Mode
                               Disabled
                                                            Item Help
BiTurbo Activated Threshold
                                60%(CDII IIe
BiTurbo Activated Tim CPU Thermal-Throttling
BiTurbo CPU Clock Set
                       Disabled · · · · [ ]
CPU Thermal-Throttlin
                                                        u Level >
                       Enabled
CPU Thermal-Throttlin
CPU Thermal-Throttlin
CPU Thermal-Throttlin
                     ↑:Move ENTER:Accept ESC:Abort
 ↑ Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help
                          F6:Optimized Defaults F7:Standard Defaults
     F5:Previous Values
```

Phoenix - AwardBIOS CMOS Setup Utility
BiTurbo Configurations



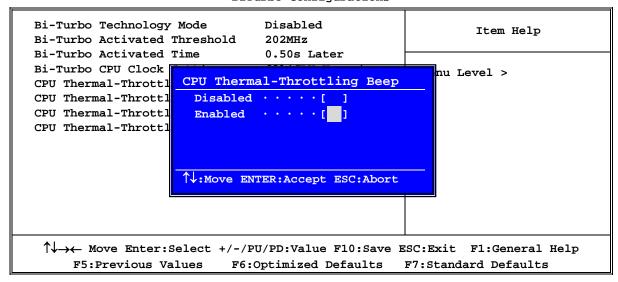
Phoenix - AwardBIOS CMOS Setup Utility
BiTurbo Configurations



CPU Thermal Throttling Temp

This item allows you to activate the CPU Thermal Throttling function when the CPU temperature is over the value which you set to low down the CPU temperature when at high workload to protect processor from damage or accidental shutdown.

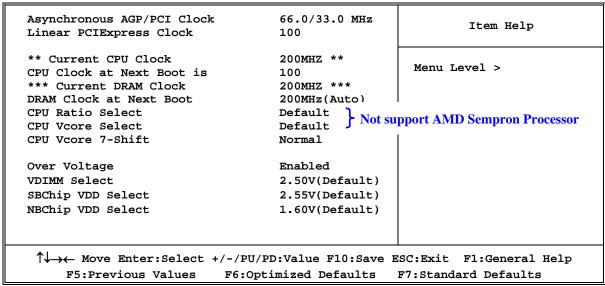
Phoenix - AwardBIOS CMOS Setup Utility
BiTurbo Configurations



3-12 Power User Overclock Settings

Phoenix - AwardBIOS CMOS Setup Utility

Power User Overclock Settings



CPU/DRAM Clock at next Boot is

This item allows you change the CPU Host /DRAM clock for overclock demand. When the CPU Host clock is over the CPU default value BIOS will auto disabled Bi-Turbo function.

CPU Vcore 7-Shift

This item allows you select the CPU Vcore Voltage xx% more than the standard value, by this function for the precise over-clocking for extra demanding of performance.

VDIMM Select

This item allows you to select 2.5V of the DDR Module. The choice are: 2.55V, 2.6V, 2.65V, 2.7V.

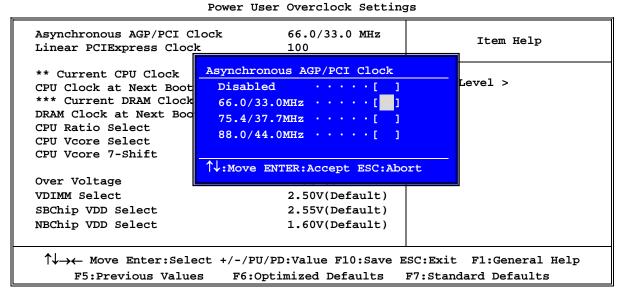
NBChip VDD Select

This item allows you to select 1.5V of the AGP 4X/8X VGA card. The choice are: 1.5V, 1.6V, 1.7V, 1.8V.

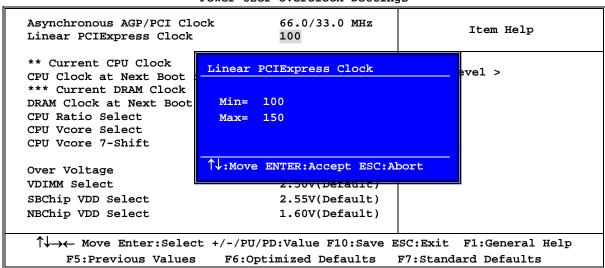
SBChip VDD Select

This item allows you to select 2.5V of the VCC2.5V Voltage. The choice are: 2.55V, 2.6V, 2.65V, 2.7V.

Phoenix - AwardBIOS CMOS Setup Utility



Phoenix - AwardBIOS CMOS Setup Utility
Power User Overclock Settings



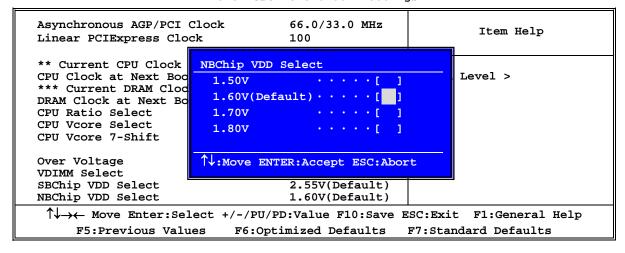
Phoenix - AwardBIOS CMOS Setup Utility
Power User Overclock Settings

| Asynchronous AGP/PCI Linear PCIExpress Cl | Clock 66.0/33.0 MHz | Item Help | |
|--|---|-------------|--|
| ** Current CPU Clock CPU Clock at Next Bo *** Current DRAM Clo DRAM Clock at Next E CPU Ratio Select CPU Vcore Select | Normal · · · · · [] + 5% · · · · · [] + 10% · · · · · [] + 15% · · · · · [] + 20% · · · · · [] | enu Level > | |
| CPU Vcore Select CPU Vcore 7-Shift Over Voltage VDIMM Select SBChip VDD Select | + 25% · · · · · [] + 30% · · · · · [] + 35% · · · · · [] \$\tau_{\text{:Move ENTER:Accept ESC:Abort}}\$ 2.55V(Default) | | |
| ↑↓→← Move Enter:Select +/-/PU/PD:Value F10:Save ESC:Exit F1:General Help F5:Previous Values F6:Optimized Defaults F7:Standard Defaults | | | |

Phoenix - AwardBIOS CMOS Setup Utility
Power User Overclock Settings

| Asynchronous AGP/PCI (Linear PCIExpress Clo | | 66.0/33.0 MHz 100 | | Item Help |
|---|--|--|---------|-------------------|
| ** Current CPU Clock CPU Clock at Next Book *** Current DRAM Clock DRAM Clock at Next Book CPU Ratio Select CPU Vcore Select CPU Vcore 7-Shift Over Voltage | 2.65V (Defa 2.60V 2.65V 2.70V | Select ault) · · · · · [] · · · · · [] · · · · · [] · · · · · [] | rt | . Level > |
| VDIMM Select SBChip VDD Select | | 2.50V(Derault) 2.55V(Default) | | |
| NBChip VDD Select | | 1.60V(Default) | | |
| $\uparrow\downarrow\rightarrow\leftarrow$ Move Enter:Sel | ect +/-/PU/P | D:Value F10:Save | ESC:Exi | t F1:General Help |
| F5:Previous Valu | es F6:Opt | imized Defaults | F7:Sta | ndard Defaults |

Phoenix - AwardBIOS CMOS Setup Utility
Power User Overclock Settings



3-13 Password Settings

Phoenix - AwardBIOS CMOS Setup Utility

Password Settings

| Set Supervisor Password Set User Password | Press Enter Press Enter | Item Help |
|---|---|--------------|
| bee ober rappword | Trobb Enteer | Menu Level > |
| | | |
| ↑↓→← Move Enter:Select +/* F5:Previous Values | -/PU/PD:Value F10:Save F F6:Optimized Defaults | |

You can set either supervisor or user password, or both of them. The differences are:

Supervisor password: Can enter and change the options of the setup menus.

User password: Can only enter but do not have the right to change the options of the

setup menus. When you select this function, the following message will appear at the center of the screen to assist you in creating a

password.

ENTER PASSWORD:

Type the password, up to eight characters in length, and press <Enter>. The password typed now will clear any previously entered password from CMOS memory. You will be asked to confirm the password. Type the password again and press <Enter>. You may also press <Esc> to abort the selection and not enter a password.

To disable a password, just press <Enter> when you are prompted to enter the password. A message will confirm that the password will be disabled. Once the password is disabled, the system will boot and you can enter Setup freely.

PASSWORD DISABLED.

When a password has been enabled, you will be prompted to enter it every time you try to enter Setup. This prevents an unauthorized person from changing any part of your system configuration. Additionally, when a password is enabled, you can also require the BIOS to request a password every time your system is rebooted. This would prevent unauthorized use of your computer.

You determine when the password is required within the BIOS Features Setup Menu and its Security option. If the Security option is set to "System", the password will be required both at boot and at entry to Setup. If set to "Setup", prompting only occurs when trying to enter Setup.

3-14 Load Standard/Optimized Defaults

Load Standard Defaults

When you press <Enter> on this item, you get confirmation dialog box with a message similar to: Load Standard Defaults (Y/N)? N

Pressing <Y> loads the BIOS default values for the most stable, minimal-performance system operations.

Load Optimized Defaults

When you press <Enter> on this item, you get a confirmation dialog box with a message similar to:

Load Optimized Defaults (Y/N)? N

Pressing $\langle \hat{Y} \rangle$ loads the default values that are factory settings for optimal performance system operations.

Chapter 4

DRIVER & FREE PROGRAM INSTALLATION

Check your package and there is A MAGIC INSTALL CD included. This CD consists of all DRIVERS you need and some free application programs and utility programs. In addition, this CD also include an auto detect software which can tell you which hardware is installed, and which DRIVERS needed so that your system can function properly. We call this auto detect software MAGIC INSTALL.

MAGIC INSTALL supports WINDOWS 9X/NT/2K/XP

Insert CD into your CD-ROM drive and the MAGIC INSTALL Menu should appear as below. If the menu does not appear, double-click MY COMPUTER / double-click CD-ROM drive or click START / click RUN / type X:\SETUP.EXE (assuming X is your CD-ROM drive).



From MAGIC INSTALL MENU you may take 8 selections:

| Ι. | nFORCE | install nFORCE integrated driver |
|----|--------|----------------------------------|
|----|--------|----------------------------------|

2. SOUND install ALC850 AC97' Codec Audio driver

3. LAN install Marvell Yukon Gigabit Ethernet NIC Driver

4. USB2.0 install USB 2.0 driver

5. DirectX9 install Microsoft DirectX 9 driver

6. PC-CILLIN install PC-CILLIN2005 anti-virus program

7. PC-HEALTH install My Guard PC-Health utility8. BROWSE CD to browse the contents of the CD

9. EXIT to exit from MAGIC INSTALL menu

4-1 nForce Install nForce Integrated Driver

* nForce Integrated driver pack include following device driver:

NVIDIA GART driver: If you are using an AGP VGA Card, please install NVIDIA

AGP GART driver which provides service routines to your VGA driver and interface directly to the hardware for speedy

graphic access.

NVIDIA SMBUS driver: Install NVIDIA SMBUS driver

NVIDIA ETHERNET driver: Install NVIDIA 10/100 Fast Ethernet device driver.

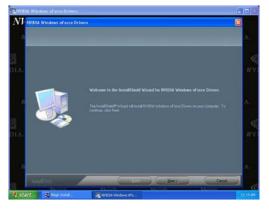
Install NVIDIA firewall and Forceware Network Access

Manager utility.

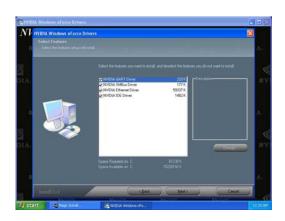
NVIDIA IDE driver : Install NVIDIA IDE driver



1. Click nForce in the MAGIC INSTALL MENU



2. Click NEXT when nForce Integrated driver Install windows appears



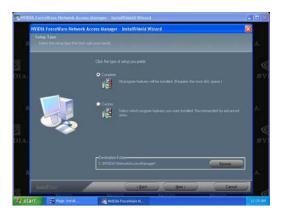
3. Please select the features you wish to install.



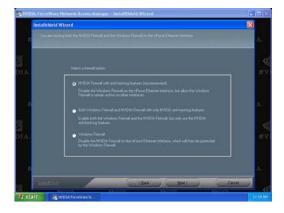
4. Click NEXT to install IDE driver and click "Yes" to proceed while system asking "Do you want to install the NVIDIA IDE SW Driver?"



5. Please choose to install the NVIDIA firewall and Forceware Network Access Manager Utility Driver for demanding on you own.



7. Select install complete software and Click NEXT



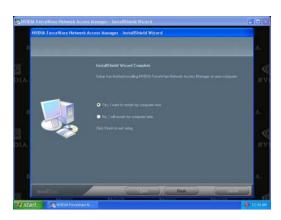
- 9. Please choose NVIDIA or Windows firewall for your system.
- 10. Select Finish and restart your computer
- * The path of the file is X:\NFORCE4\DRIVER\SETUP.EXE



6. If you want to install NVIDIA firewall and Forceware Network Access Manager utility



8. Select interface language and Click NEXT



4-2 SOUND install ALC850 8-Channel Audio Driver



1. Click SOUND when MAGIC INSTALL MENU appears



3. Click FINISH and restart your computer



5. Speaker configuration setting



2. Click NEXT When Realtek AC97Audio driver windows appears



4. Manual Sound Effect Setting



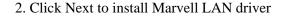
6. SPDIF N/OUT setting

4-3 LAN Marvell Yukon Gigabit Ethernet NIC Driver





1. Click LAN when Magic Install Menu appear





3. Please Accept the license agreement and read the "Readme" file for detailed information, then click Next to proceed.



4. Click Install to install driver and Click Finish end the installation

4-4 USB2.0 Install USB2.0 DRIVER

Windows 2000 OS

Please install Windows 2000 service pack 4 or later.

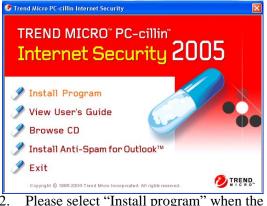
Windows XP OS

Please install Windows XP service pack 1 or later.

4-5 PC-CILLIN Install PC-CILLIN 2005 Anti-virus program



1. Click PC-CILLIN when MAGIC INSTALL MENU appears



 Please select "Install program" when the "Trend Micro internet security" installshield wizard windows appears



3. Click NEXT and Enter your Customer Information, Click NEXT or choose Change to change the path for the file to be stored



Please select install "FULL" function or install "Antivirus software" only



5. We suggest to use "Recommend configuration".



Click Install to install PCCILLIN 2005 internet security software, then select to restart your computer

Note: Please install ACROBAT READER for reading PC-CILLIN 2005 User Manual which locates at the path "X:\acrobat\adberdr6_enu_full.exe".

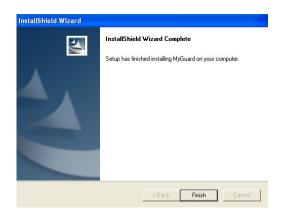
4-6 PC-HEALTH Install Myguard Hardware monitor Utility



MENU appears



1. Click PC-HEALTH when MAGIC INSTALL 2. Click Next when Install shield wizard Window appears, Choose destination location and click Next, when the start copy file windows appear, click next



3. Select Finish after setup complete



4. Execute MY GUARD utility, On-time Monitoring your system health

NOTE:

MAGIC INSTALL will auto detect file path X:\NFORCE4\MYGUARD\SETUP.EXE

4-7 HOW TO UPDATE BIOS

Before updating the BIOS, users have to "Disable" the "Flash Part Write Protect" selection in "Miscellaneous Control" of BIOS SETUP. Otherwise the system the will not allow you to upgrade BIOS by Award Flash Utility.

- **STEP 1.** Prepare a boot disc. (you may make one by click START click RUN type SYS A: click OK)
- **STEP 2.** Copy utility program to your boot disc. You may copy from DRIVER CD X:\FLASH\AWDFLASH.EXE or download from our web site.
- **STEP 3.** Download and make a copy of the latest BIOS for motherboard series from the web site to your boot disc.
- **STEP 4.** Insert your boot disc into A:, start the computer, type "Awdflash A:\xxxxxx.BIN /SN/PY/CC/R" ,xxxxxx.BIN is the file name of latest BIOS

SN means don't save existing BIOS data

PY means renew existing BIOS data

CC means clear existing CMOS data

R means restart computer

STEP 5. Push ENTER to update and flash the BIOS, then the system will restart automatically.

APPENDIX

Debug Port Post Code

Normal POST Codes

NOTE: EISA POST codes are typically output to port address 300h. ISA POST codes are output to port address 80h.

| Code(hex) | Name | Description |
|-----------|----------------------------------|--|
| CO | Turn Off Chipset And CPU test | OEM Specific-Cache control cache Processor Status (1FLAGS) Verification. Tests the following processor status flags: Carry, zero, sign, overflow, The BIOS sets each flag, verifies They are set, then turns each flag off and verifies it is off. Read/Write/Verify all CPU registers except SS, SP, and BP with data pattern FF and 00. RAM must be periodically refreshed to keep the memory from decaying. This function ensures that the memory refresh function is working properly. |
| C1 | Memory Presence | First block memory detect OEM Specific-Test to size on-board memory. Early chip set initialization Memory presence test OEM chip set routines Clear low 64K of memory Test first 64K memory. |
| C2 | Early Memory Initialization | OEM Specific- Board Initialization |
| C3 | Extend Memory DRAM select | OEM Specific- Turn on extended memory Initialization Cyrix CPU initialization Cache initialization |
| C4 | Special Display Handling | OEM Specific- Display/Video Switch Handling so that Switch Handling display switch errors never occurs |
| C5 | Early Shadow | OEM specific- Early shadow enable for fast boot |
| C6 | Cache presence test | External cache size detection |
| CF | CMOS Check | CMOS checkup |
| В0 | Spurious | If interrupt occurs in protected mode. |
| B1 | Unclaimed NMI | If unmasked NMI occurs, display Press F1 to disable NMI, F2 reboot. |
| BF | Program Chip Set | To program chipset from defaults values |
| E1-EF | Setup Pages | E1- Page 1, E2 - Page 2, etc. |
| 1 | Force load Default to chipset | Chipset defaults program |
| 2 | Reserved | |
| 3 | Early Superio Init | Early Initialized the super IO |

| Code(hex) | Name | Description |
|-----------|-------------------------|--|
| | Reserved | 2000.15.1011 |
| 5 | Blank video | Reset Video controller |
| 6 | Reserved | Reset video controller |
| 7 | Init KBC | Keyboard controller init |
| 8 | KB test | Test the Keyboard |
| 9 | Reserved | Test the Reypoard |
| A | | Initialized the mouse |
| B | Mouse Init | Initialized the mouse Onboard audio controller initialize if |
| | Onboard Audio init | onboard audio controller initialize if exist |
| С | Reserved | |
| D | Reserved | |
| E | CheckSum Check | Check the intergraty of the ROM, BIOS and message |
| F | Reserved | |
| 10 | Auto detec EEPROM | Check Flash type and copy flash write/erase routines to 0F000h segments |
| 11 | Reserved | |
| 12 | Cmos Check | Check Cmos Circuitry and reset CMOS |
| 13 | Reserved | |
| 14 | Chipset Default load | Program the chipset registers with CMOS values |
| 15 | Reserved | |
| 16 | Clock Init | Init onboard clock generator |
| 17 | Reserved | |
| 18 | Identify the CPU | Check the CPU ID and init L1/L2 cache |
| 19 | Reserved | 5110011 0110 010 11 0110 11110 111, 11 00.0110 |
| 1A | Reserved | |
| 1B | Setup Interrupt | Initialize first 120 interrupt |
| | Vector Table | vectors with SPURIOUS_INT_HDLR and initialize INT 00h-1Fh according to INT TBL |
| 1C | Reserved | <u> </u> |
| 1D | Early PM Init | First step initialize if single CPU onboard |
| 1E | Reserved | |
| 1F | Re-initial KB | Re-init KB |
| 20 | Reserved | 110 1110 110 |
| 21 | HPM init | If support HPM, HPM get initialized here |
| 22 | Reserved | 11 Support min, min got imitalized more |
| 23 | Test CMOS | Verifies CMOS is working correctly, |
| | Interface and | detects bad battery. If failed, load |
| | Battery Status | CMOS defaults and load into chipset |
| 24 | Reserved | |
| 25 | Reserved | |
| 26 | Reserved | |
| 27 | KBC final Init | Final Initial KBC and setup BIOS data area |
| 28 | Reserved | |
| 29 | Initialize Video | Read CMOS location 14h to find out type |
| 23 | Interface | of video in use. Detect and Initialize Video Adapter. |
| 2A | Reserved | |
| 2B | Reserved | |
| 2C | Reserved | |
| 2D | Video memory test | Test video memory, write sign-on message to screen. |
| 2E | Reserved | Setup shadow RAM - Enable shadow according to Setup. |
| | | |

| Code(hex) | Name | Description |
|----------------|--------------------|---|
| 2F | Reserved | • |
| 30 | Reserved | |
| 31 | Reserved | |
| 32 | Reserved | |
| 33 | PS2 Mouse setup | Setup PS2 Mouse and reset KB |
| 34 | Reserved | becap 152 Mouse and 1esee NB |
| 35 | Test DMA | Test DMA channel 0 |
| 33 | Controller 0 | rest DMA Channel 0 |
| 36 | Reserved | |
| 37 | Test DMA | Test DMA channel 1 |
| 57 | Controller 1 | TOBO DIM CHAINCE I |
| 38 | Reserved | |
| 39 | Test DMA Page | Test DMA Page Registers. |
| | Registers | 1020 2.11 1 430 1.03120010. |
| 3A | Reserved | |
| 3B | Reserved | |
| 3C | Test Timer Counter | Test 8254 Timer 0 Counter 2. |
| | 2 | Test 6254 Timer 0 Counter 2. |
| 3D | Reserved | |
| 3E | Test 8259-1 Mask | Verify 8259 Channel 1 masked interrupts |
| | Bits | by alternately turning off |
| | | and on the interrupt lines. |
| 3F | Reserved | |
| 40 | Test 8259-2 Mask | Verify 8259 Channel 2 masked interrupts |
| | Bits | by alternately turning off |
| | | and on the interrupt lines. |
| 41 | Reserved | |
| 42 | Reserved | |
| 43 | Test Stuck8259's | Turn off interrupts then verify no |
| | Interrupt Bits | interrupt mask register is on. |
| | Test 8259 | Force an interrupt and verify the |
| | Interrupt | interrupt occurred. |
| | Functionality | |
| 44 | Reserved | |
| 45 | Reserved | |
| 46 | Reserved | |
| 47 | Set EISA Mode | If EISA non-volatile memory checksum is |
| | | good, execute EISA initialization. |
| | | If not, execute ISA tests an clear |
| | | EISA mode flag. |
| 48 | Reserved | |
| 49 | Size Base and | Size base memory from 256K to 640K and |
| · - | Extended Memory | extended memory above 1MB. |
| 4A | Reserved | · · · · · · · · · · · · · · · · · · · |
| 4B | Reserved | |
| 4C | Reserved | |
| 4D | Reserved | |
| 4E | Test Base and | Test base memory from 256K to 640K and |
| | Extended Memory | extended memory above 1MB using |
| | | various patterns. |
| | | |
| | | NOTE: This test is skipped in EISA |
| | | mode and can be skipped with ESC key |
| | | in ISA mode. |
| 4 F | Reserved | |
| 50 | USB init | Initialize USB controller |
| 51 | Reserved | INICIALIZE ODD CONCLOSICE |
| → ± | VCBCT ACM | |

| Code(hex) | Name | Description |
|-----------|---------------------|---|
| 52 | Memory Test | Test all memory of memory above 1MB |
| 32 | ricinoly rese | using Virtual 8086 mode, |
| | | page mode and clear the memory |
| 53 | Reserved | <u> </u> |
| 54 | Reserved | |
| 55 | CPU display | Detect CPU speed and display |
| | 1 1 | CPU vendor specific version string |
| | | and turn on all necessary CPU features |
| 56 | Reserved | • |
| 57 | PnP Init | Display PnP logo and PnP early init |
| 58 | Reserved | |
| 59 | Setup Virus Protect | Setup virus protect according to Setup |
| 5A | Reserved | |
| 5B | Awdflash Load | If required, will auto load |
| | | Awdflash.exe in POST |
| 5C | Reserved | |
| 5D | Onboard I/O Init | Initializing onboard superIO |
| 5E | Reserved | |
| 5F | Reserved | |
| 60 | Setup enable | Display setup message and enable setup |
| | - | functions |
| 61 | Reserved | |
| 62 | Reserved | |
| 63 | Initialize & | Detect if mouse is present, initialize |
| | Install Mouse | mouse, install interrupt |
| | | vectors. |
| 64 | Reserved | |
| 65 | PS2 Mouse special | Special treatment to PS2 Mouse port |
| 66 | Reserved | |
| 67 | ACPI init | ACPI sub-system initializing |
| 68 | Reserved | |
| 69 | Setup Cache | Initialize cache controller. |
| | Controller | |
| 6A | Reserved | |
| 6B | Setup Entering | Enter setup check and auto- |
| | | configuration check up |
| 6C | Reserved | |
| 6D | Initialize Floppy | Initialize floppy disk drive controller |
| | Drive & Controller | and any drives. |
| 6E | Reserved | |
| 6F | FDD install | Install FDD and setup BIOS data area parameters |
| 70 | Reserved | |
| 71 | Reserved | |
| 72 | Reserved | |
| 73 | Initialize Hard | Initialize hard drive controller and |
| | Drive & Controller | any drives. |
| 74 | Reserved | |
| 75 | Install HDD | IDE device detection and install |
| 76 | Reserved | |
| 77 | Detect & Initialize | Initialize any serial and parallel |
| | Serial/Parallel | ports (also game port). |
| | Ports | |
| 78 | Reserved | |
| 79 | Reserved | |
| 7A | Detect & Initialize | Initialize math coprocessor. |
| | Math Coprocessor | F 1000000 |
| 7B | Reserved | |
| | | |

| To | Code(hex) | Name | Description |
|--|-----------|-------------------|---|
| TE | 7C | | HDD check out |
| POST error check Check POST error and display them and ask for user intervention | 7D | Reserved | |
| and ask for user intervention 80 Reserved 81 Reserved 82 Security Check Ask password security (optional). 83 Write CMOS Write all CMOS values back to RAM and clear screen. 84 Pre-boot Enable Enable parity checker Enable NMI, Enable cache before boot. 85 Initialize Initialize any option ROMs present Option ROMs from C8000h to EFFFFh. NOTE: When FSCAN option is enabled, ROMs initialize from C8000h to F7FFFh. 86 Reserved 87 Reserved 88 Reserved 88 Reserved 88 Reserved 88 Reserved 88 Reserved 89 Reserved 80 Reserved 81 Reserved 82 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 83 Reserved 84 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 89 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 83 Reserved 84 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 89 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 83 Reserved 84 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 89 Reserved 80 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 83 Reserved 84 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 80 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 83 Reserved 84 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 80 Reserved 80 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 83 Reserved 84 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 80 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 83 Reserved 84 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 80 Reserved 80 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 83 R | 7E | Reserved | |
| 81 Reserved 82 Security Check Ask password security (optional). 83 Write CMOS Write all CMOS values back to RAM and clear screen. 84 Pre-boot Enable Enable parity checker Enable NMI, Enable cache before boot. 85 Initialize Initialize any option ROMs present Option ROMs from C8000h to EFFFFh. NOTE: When FSCAN option is enabled, ROMs initialize from C8000h to F7FFFh. 86 Reserved 87 Reserved 88 Reserved 88 Reserved 89 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 83 Reserved 84 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 80 Reserved 81 Reserved 82 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 89 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 89 Reserved 80 Reserved 80 Reserved 81 Reserved 82 Reserved 83 Reserved 84 Reserved 85 Reserved 86 Reserved 87 Reserved 88 Reserved 89 Reserved 90 Reserved 91 Reserved 92 Reserved 93 Boot Medium Read and store boot partition head and detection cylinders values in RAM 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Set system Set low stack Boot via INT 19h. | 7F | POST error check | |
| Security Check | 80 | Reserved | |
| Write CMOS Write all CMOS values back to RAM and clear screen. | 81 | Reserved | |
| Clear screen. 84 Pre-boot Enable Enable parity checker Enable NMI, Enable cache before boot. 85 Initialize Initialize any option ROMs present Option ROMs from C8000h to EFFFFh. NOTE: When FSCAN option is enabled, ROMs initialize from C8000h to F7FFFh. 86 Reserved 87 Reserved 88 Reserved 89 Reserved 88 Reserved 89 Reserved 8B Reserved 8B Reserved 8C Reserved 8E Reserved 8E Reserved 9D Reserved 91 Reserved 92 Reserved 93 Boot Medium Read and store boot partition head and detection cylinders values in RAM 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 82 | Security Check | |
| Enable NMI, Enable cache before boot. 85 | 83 | Write CMOS | |
| Initialize any option ROMs present Option ROMs from C8000h to EFFFFh. NOTE: When FSCAN option is enabled, ROMs initialize from C8000h to F7FFFh. Reserved ROMS from C8000h to F7FFFh. ROMS initialize any option ROMS present compact of the property o | 84 | Pre-boot Enable | |
| Reserved Res | 85 | Initialize | Option ROMs from C8000h to EFFFFh. NOTE: When FSCAN option is enabled, |
| Reserved Res | 86 | Reserved | |
| Reserved Set pinal init for last micro details before boot Setup NumLock status according to Setup Reserved Reserved Read and store boot partition head and cylinders values in RAM Final Init Final init for last micro details before boot Setup NumLock status according to Setup Reserved Set low stack Reserved Reserved Reserved Reserved Reserved Reserved Set low stack Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Set low stack Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved | 87 | Reserved | |
| Reserved Read and store boot partition head and cylinders values in RAM Read and store boot partition head and cylinders values in RAM Reserved Reserved Reserved Reserved Set system speed for boot before boot Setup NumLock status according to Setup Reserved Reserved Reserved Reserved Reserved Reserved Reserved Set system speed for boot setup NumLock status according to Setup Reserved R | 88 | Reserved | |
| Reserved Read and store boot partition head and cylinders values in RAM Final Init Final init for last micro details before boot Setup NumLock status according to Setup Reserved Set low stack Boot via INT 19h. | 89 | Reserved | |
| 8C Reserved 8D Reserved 8E Reserved 90 Reserved 91 Reserved 92 Reserved 93 Boot Medium Read and store boot partition head and detection cylinders values in RAM 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 8A | Reserved | |
| 8D Reserved 8E Reserved 8F Reserved 90 Reserved 91 Reserved 92 Reserved 93 Boot Medium Read and store boot partition head and detection cylinders values in RAM 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 8B | Reserved | |
| Reserved Reserved Reserved Reserved Reserved Reserved Reserved Reserved Read and store boot partition head and detection cylinders values in RAM Final Init Final init for last micro details before boot Setup NumLock status according to Setup Reserved Reserved Set system speed for boot Setup Reserved Set system speed for boot Setup NumLock status according to Setup Reserved Reserved Reserved Set low stack Boot via INT 19h. | 8C | Reserved | |
| 8F Reserved 90 Reserved 91 Reserved 92 Reserved 93 Boot Medium Read and store boot partition head and detection cylinders values in RAM 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 8D | Reserved | |
| 90 Reserved 91 Reserved 92 Reserved 93 Boot Medium Read and store boot partition head and detection cylinders values in RAM 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 8E | Reserved | |
| 91 Reserved 92 Reserved 93 Boot Medium Read and store boot partition head and detection cylinders values in RAM 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 8F | Reserved | |
| 92 Reserved 93 Boot Medium Read and store boot partition head and cylinders values in RAM 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 90 | Reserved | |
| 93 Boot Medium Read and store boot partition head and detection cylinders values in RAM 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 91 | Reserved | |
| detection cylinders values in RAM 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 92 | Reserved | |
| 94 Final Init Final init for last micro details before boot 95 Special KBC patch Set system speed for boot Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 93 | | |
| Setup NumLock status according to Setup 96 Boot Attempt Set low stack Boot via INT 19h. | 94 | Final Init | Final init for last micro details |
| Boot via INT 19h. | 95 | Special KBC patch | Setup NumLock status according to |
| FF Boot | 96 | Boot Attempt | |
| | FF | Boot | - |

Quick POST Codes

| Code(hex) | Name | Description |
|-----------|------------------------|--|
| 65 | Init onboard device | Early Initialized the super IO Reset Video controller Keyboard controller init Test the Keyboard Initialized the mouse Onboard audio controller initialize if exist. Check the intergraty of the ROM, BIOS and message Check Flash type and copy flash write/erase routines to 0F000h segments Check Cmos Circuitry and reset CMOS Program the chipset registers with CMOS values Init onboard clock generator |

| Codo(boy) | Nama | Description |
|----------------|----------------------|---|
| Code(hex) | Name | Description |
| 66 | Early System setup | Check the CPU ID and init L1/L2 cache |
| | | Initialize first 120 interrupt |
| | | vectors with SPURIOUS_INT_HDLR and |
| | | initialize INT 00h-1Fh according to |
| | | INT_TBL First step initialize if single |
| | | CPU onboard. Re-init KB |
| | | If support HPM, HPM get initialized |
| | | here |
| 67 | KBC and CMOS Init | Verifies CMOS is working correctly, |
| | | detects bad battery. If failed, load |
| | | CMOS defaults and load into chipset |
| | | Final Initial KBC and setup BIOS data |
| | | area. |
| 68 | Video Init | Read CMOS location 14h to find out |
| | | type of video in use. |
| | | Detect and Initialize Video Adapter. |
| | | Test video memory, write sign-on |
| | | message to screen. |
| | | Setup shadow RAM - Enable shadow |
| | 0050 7 1 | according to Setup. |
| 69 | 8259 Init | Init 8259 channel 1 and mask IRQ 9 |
| 6A | Memory test | Quick Memory Test |
| 6B | CPU Detect and IO | Detect CPU speed and display |
| | init | CPU vendor specific version string |
| | | and turn on all necessary CPU features |
| | | Display PnP logo and PnP early init |
| | | Setup virus protect according to |
| | | Setup. If required, will auto load |
| | | Awdflash.exe in POST |
| 60 | D | Initializing onboard superIO |
| 6C 6D | Reserved Reserved | |
| 6E | Reserved | |
| 6F | Reserved | |
| 70 | Setup Init | Display setup message and enable setup |
| 7 0 | becap init | functions Detect if mouse is present, |
| | | initialize mouse, install interrupt |
| | | vectors. Special treatment to PS2 |
| | | Mouse port |
| | | ACPI sub-system initializing |
| 71 | Setup Cache | Initialize cache controller. |
| , _ | Controller | initialize eache concretici. |
| 72 | Install FDD | Enter setup check and auto- |
| · - | | configuration check up |
| | | Initialize floppy disk drive |
| | | controller and any drives. |
| | | Install FDD and setup BIOS data area |
| | | parameters |
| 73 | Install HDD | Initialize hard drive controller and |
| | | any drives. |
| | | IDE device detection and install |
| | | Initialize any serial and parallel |
| | | ports (also game port). |
| 74 | Detect & | Initialize math coprocessor. |
| | Initialize Math | <u>.</u> |
| | Coprocessor | |
| 75 | HDD Check for | HDD check out |
| | Write protection | |
| 76 | Reserved | |
| | | |

| Code(hex) | Name | Description |
|-----------|-----------------------------|--|
| 77 | Display POST error | Check POST error and display them and ask for user intervention Ask password security (optional). |
| 78 | CMOS and Option ROM Init | Write all CMOS values back to RAM and clear screen. Enable parity checker Enable NMI, Enable cache before boot. Initialize any option ROMs present from C8000h to EFFFFh. NOTE: When FSCAN option is enabled, ROMs initialize from C8000h to F7FFFh. |
| 79 | Reserved | |
| 7A | Reserved | |
| 7B | Reserved | |
| 7C | Reserved | |
| 7D | Boot Medium detection | Read and store boot partition head and cylinders values in RAM |
| 7E | Final Init | Final init for last micro details before boot |
| 7F | Special KBC patch | Set system speed for boot Setup NumLock status according to Setup |
| 80 | Boot Attempt | Set low stack Boot via INT 19h. |
| FF | Boot | |

S4 POST Codes

| O 1 (1) | . . | B ' 4' |
|-----------------|--------------------|---------------------------------------|
| Code(hex) | Name | Description |
| 5A | Early Chipset Init | Early Initialized the super IO |
| | | Reset Video controller |
| | | Keyboard controller init |
| | | Test the Keyboard |
| | | Initilized the mouse |
| 5B | Cmos Check | Check Cmos Circuitry and reset CMOS |
| 5C | Chipset default | Program the chipset registers with |
| | Prog | CMOS values. Init onboard clock |
| | | generator |
| 5D | Identify the CPU | Check the CPU ID and init L1/L2 cache |
| 5E | Setup Interrupt | Initialize first 120 interrupt |
| | Vector Table | vectors with SPURIOUS INT HDLR and |
| | | INT 00h-1Fh according to INT TBL |
| | | First step initialize if single CPU |
| | | Onboard. Re-init KB |
| | | If support HPM, HPM get initialized |
| | | Here. |
| 5F | Test CMOS | Verifies CMOS is working correctly, |
| | Interface and | detects bad battery. If failed, load |
| | Battery status | CMOS defaults and load into chipset |
| 60 | KBC final Init | Final Initial KBC and setup BIOS data |
| | | area |

| Code(hex) | Name | Description |
|-----------|---------------------------------|--|
| 61 | Initialize Video Interface | Read CMOS location 14h to find out type of video in use. Detect and Initialize Video Adapter. |
| 62 | Video memory test | Test video memory, write sign-on message to screen. Setup shadow RAM - Enable shadow according to Setup. |
| 63 | Setup PS2 mouse and test DMA | Setup PS2 Mouse and reset KB Test DMA channel 0 |
| 64 | Test 8259 | Test 8259 channel 1 and mask IRQ 9 |
| 65 | Init Boot Device | Detect if mouse is present, initialize mouse, install interrupt vectors. Special treatment to PS2 Mouse port ACPI sub-system initializing Initialize cache controller. |
| 66 | Install Boot Devices | Enter setup check and auto- configuration check up Initialize floppy disk drive controller and any drives. Install FDD and setup BIOS data area Parameters Initialize hard drive controller and any drives. IDE device detection and install |
| 67 | Cache Init | Cache init and USB init |
| 68 | PM init | PM initialization |
| 69 | PM final Init and issue SMI | Final init Before resume |
| FF | Full on | |

BootBlock POST Codes

| Code(hex) | Name | Description |
|-----------|-------------------|--|
| 1 | Base memory test | Clear base memory area (0000:00009000:ffffh) |
| 5 | KB init | Initialized KBC |
| 12 | Install interrupt | Install int. vector (0-77), and |
| | vectors | initialized 00-1fh to their |
| | | proper place |
| 0D | Init Video | Video initializing |
| 41 | Init FDD | Scan floppy and media capacity for |
| | | onboard superIO |
| FF | Boot | Load boot sector |