

Triple-Channel Video Reconstruction Filter and Buffer for Composite and Y/C Outputs

General Description

The MAX7443/MAX7444 are low-cost, triple-channel video reconstruction filters for S-video and CVBS video signals. These devices are ideal for anti-aliasing and DAC smoothing video applications such as set-top boxes, DVD players, hard-disk recorders (HDRs), and personal video recorders. These filters operate from a single +5V supply. They are optimized for NTSC, PAL, and standard-definition digital TV (SDTV) video systems.

Each channel consists of a lowpass filter and an adjustable-gain output buffer capable of driving two standard 150Ω video loads. The output buffers can drive either an AC or a DC load such that the blanking level voltage after the backmatch resistor is less than 1V. The Y and C video inputs are summed to produce the composite video output. The MAX7444 offers highfrequency boost to improve picture sharpness. The MAX7443 has a maximum flat-frequency response.

The MAX7443/MAX7444 are available in a tiny 8-pin thin QFN and an 8-pin SO package with exposed pad, and are fully specified over the -40°C to +85°C extended temperature range.

Applications

Set-Top Boxes/HDR/DVD Security Cameras/Digital Cameras Game Consoles

Digital VCRs Every Composite, S-Video Output for NTSC, PAL, SDTV

Features

- ◆ Triple-Channel Filter and Buffer for CVBS and Y/C (S-Video)
- ♦ Filter Response Ideal for NTSC, PAL, and **Interlaced SDTV Signals**
- ◆ 41dB (typ) Stopband Attenuation at 27MHz
- ♦ -0.3dB (typ) Passband Response
- **♦ Low Blanking Level Voltage Allows DC-Coupled** Output
- ♦ Each Channel Drives Two 150Ω Video Loads
- ♦ +5V Single-Supply Voltage
- ◆ Tiny 8-Pin Thin QFN or 8-Pin SO Packages

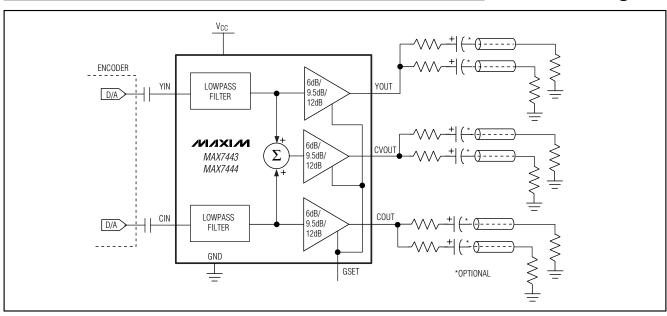
Ordering Information

| PART | TEMP RANGE | PIN-PACKAGE | TOP MARK |
|------------|----------------|----------------|-------------|
| MAX7443ETA | -40°C to +85°C | 8 Thin QFN-EP* | AFX |
| MAX7443ESA | -40°C to +85°C | 8 SO-EP* | _ |
| MAX7444ETA | -40°C to +85°C | 8 Thin QFN-EP* | AFY |
| MAX7444ESA | -40°C to +85°C | 8 SO-EP* | _ |

^{*}EP = Exposed pad.

Selector Guide appears at end of data sheet.

Functional Diagram



MIXIM

Maxim Integrated Products 1

ABSOLUTE MAXIMUM RATINGS

| V _{CC} to GND+6V |
|---|
| All Other Pins to GND0.3V to (V _{CC} + 0.3V) |
| Maximum Current into Any Pin Except VCC and GND ±50mA |
| Continuous Power Dissipation (T _A = +70°C) |
| 8-Pin Thin QFN (derate 24.4mW/°C above +70°C)1951mW |
| 8-Pin SO (derate 18.9mW/°C above +70°C)1509mW |

| Operating Temperature Range | 40°C to +85°C |
|-----------------------------------|----------------|
| Storage Temperature Range | 65°C to +150°C |
| Junction Temperature | +150°C |
| Lead Temperature (soldering, 10s) | +300°C |

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

 $(V_{CC}=+5V~\pm5\%,~C_L=0~to~20pF,~R_L=75\Omega~to~GND~for~DC-couple,~R_L=75\Omega~to~V_{CC}/2~for~AC-couple,~C_{YIN}=C_{CIN}=0.1\mu F,~T_A=T_{MIN}~to~T_{MAX},~GSET=GND~(6dB)~and~V_{CC}~(9.5dB),~unless~otherwise~noted.~Typical~values~are~at~T_A=+25^{\circ}C.)$

| PARAMETER | SYMBOL | CONDITIONS | | MIN | TYP | MAX | UNITS | |
|------------------------------|-------------------|---|----------------------------|------|------|-------|---------|--|
| Passband Response | | f = 100kHz to 4.2MHz, | MAX7443 | -0.8 | -0.3 | +0.1 | dB | |
| i assband nesponse | | relative to 100kHz | MAX7444 | -0.1 | +0.6 | +0.92 | uВ | |
| Stopband Attenuation | AsB | f = 27MHz | | 39 | 41 | | dB | |
| Boost Amplitude | | f = 4.2MHz, MAX7444 | | | 0.6 | | dB | |
| Differential Gain | dG | 5-step modulated staircase | Gain = 6dB, 9.5dB, 12dB | | 0.15 | 0.4 | % | |
| Differential Phase | dθ | 5-step modulated staircase | Gain = 6dB, 9.5dB, 12dB | | 0.15 | 0.4 | Degrees | |
| Signal-to-Noise Ratio | SNR | Peak signal (2V _{P-P}) to RMS noise, f = 100Hz to 50MHz Gain = 6dB, 9.5dB, 12dB | | 67 | 73 | | dB | |
| 0 0 0 0 0 0 | | Deviation from 100kHz to | MAX7443 | | 8 | 20 | | |
| Group Delay Deviation | Δtg | 4.1MHz | MAX7444 | | 12 | 30 | ns | |
| Line-Time Distortion | H _{DIST} | 18µs, 100 IRE bar | | | | 0.3 | % | |
| Field-Time Distortion | V _{DIST} | 130 lines, 18µs, 100 IRE bar | | | | 0.5 | % | |
| Clamp Settling Time | | To ±1% | | | | 100 | Lines | |
| | | YOUT | | 0.6 | 0.8 | 1.0 | | |
| Output DC Clamp Level | | CVOUT | | 0.6 | 0.8 | 1.0 | V | |
| | | COUT | | 1.44 | 1.6 | 1.97 | | |
| Low-Frequency Gain Accuracy | Ay | f = 100kHz, relative to 6dB | | -3.5 | | +2 | % | |
| Group Delay Matching | tg(MATCH) | Low-frequency channel-to-channel matching, f = 100kHz for YOUT and COUT | | | 2 | | ns | |
| Channel-to-Channel Crosstalk | XTALK | f = 100kHz to 3.58MHz, gain = 6dB | | | 61 | | dB | |
| Input Leakage Current | IIN | | | | | 10 | μΑ | |

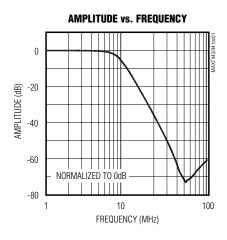
ELECTRICAL CHARACTERISTICS (continued)

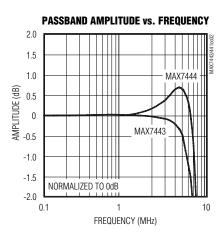
 $(V_{CC} = +5V \pm 5\%, \ C_L = 0 \ to \ 20pF, \ R_L = 75\Omega \ to \ GND \ for \ DC\text{-}couple, \ R_L = 75\Omega \ to \ V_{CC}/2 \ for \ AC\text{-}couple, \ C_{YIN} = C_{CIN} = 0.1 \mu F, \ T_A = T_{MIN} \ to \ T_{MAX}, \ GSET = GND \ (6dB) \ and \ V_{CC} \ (9.5dB), \ unless \ otherwise \ noted. \ Typical \ values \ are \ at \ T_A = +25^{\circ}C.)$

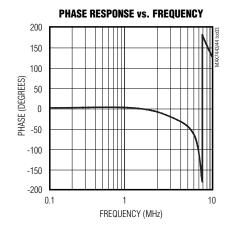
| PARAMETER | SYMBOL | CONDITIONS | MIN TYP MAX | | MAX | UNITS |
|------------------------------|--------------------|---|-------------|----|------|------------------|
| Janut Dynamia Swing | Y _{INP-P} | GSET = GND (6dB) | | | 1.2 | \/n n |
| Input Dynamic Swing | CINP-P | GSET = GND (6dB) | | | 0.9 | V _{P-P} |
| SUPPLY | | | | | | |
| Supply Voltage Range | Vcc | | 4.75 | | 5.25 | V |
| Supply Current | Icc | $V_{CC} = +5.25V$, no load | | 88 | 110 | mA |
| Power-Supply Rejection Ratio | PSRR | $V_{IN} = 100 \text{mVp-p}, f = 0 \text{ to } 3.5 \text{MHz}$ | | 33 | | dB |

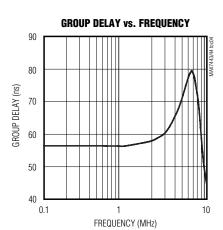
Typical Operating Characteristics

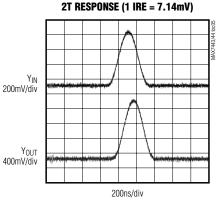
(V_{CC} = +5V, T_A = +25°C, unless otherwise noted.)

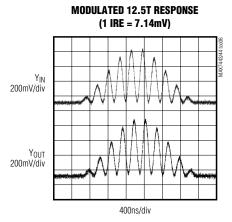






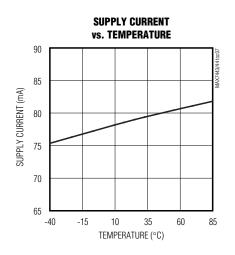


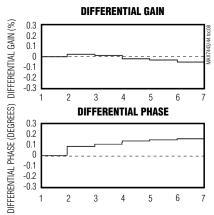


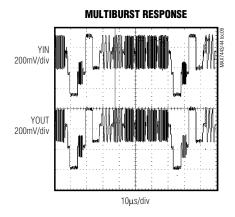


Typical Operating Characteristics (continued)

($V_{CC} = +5V$, $T_A = +25$ °C, unless otherwise noted.)







Pin Description

| PIN | NAME | FUNCTION |
|-----|-------|---|
| 1 | YIN | Luma (Y) Input |
| 2 | GSET | Gain-Setting Control Input. See Table 1 for values. |
| 3 | CIN | Chroma (C) Input |
| 4 | GND | Ground |
| 5 | Vcc | 5V Power Supply |
| 6 | COUT | Chroma (C) Output |
| 7 | CVOUT | Composite Video Output |
| 8 | YOUT | Luma (Y) Output |

Detailed Description

The primary function of the MAX7443/MAX7444 is to filter and buffer the encoder DAC outputs that contain digital video information in applications such as set-top boxes, hard-disk recorders, DVD players, and digital VCRs. These devices reconstruct and clean up analog video signals. Each MAX7443/MAX7444 consists of a lowpass filter and an adjustable-gain output video buffer that is capable of driving two standard 150Ω video loads. The luma (YIN) and chroma (CIN) video inputs are summed internally to provide the composite (CVOUT) video output.

These devices operate from a single +5V supply. The filters have a nominal cutoff frequency optimized for NTSC, PAL, and SDTV.

Filter

Filter Response

The reconstruction filter consists of two 2nd-order Sallen-Key stages. The Butterworth-type response features a maximally flat passband for NTSC and PAL bandwidths. The stopband offers 41dB (typ) of attenuation at sampling frequencies of 27MHz and above (see *Typical Operating Characteristics*).

High-Frequency Boost

The high-frequency boost available in the MAX7444 compensates for signal degradation and rolloff in the video encoder, which increases the image sharpness. The MAX7443 has a flat response over the video bandwidth.

Output Buffer

The output buffer drives two 150 Ω video loads with a 2VP-P signal. The adjustable gain of the output buffer provides a gain of 6dB, 9.5dB, and 12dB, which are selected by tying the GSET pin to GND, VCC, or floating, respectively. The MAX7443/MAX7444 can drive an AC load or drive the video load directly without using the large output capacitor. The output buffer drives a DC load with an output blanking level of less than 1V.

Table 1. Gain-Setting Control

| GSET | GAIN (dB) |
|------|-----------|
| GND | 6 |
| Vcc | 9.5 |
| Open | 12 |

12dB Gain Setting

The GSET pin is biased internally to $V_{CC}/2$ through two $100k\Omega$ resistors from V_{CC} to GND. The internal impedance at the node is $50k\Omega$. No additional connection is necessary since the pin offers a minimum noise margin immunity of $1V_{P-P}$.

Output Clamp Level

When sync pulses in the luma signal (Y) are detected, the DC restore loop is activated. The function of the loop is to set the sync tip of the video signal to the desired DC level of 0.8V for YOUT and COUT and the average DC voltage of COUT at 1.6V.

Applications Information

Input Considerations

Use a 0.1µF ceramic capacitor to AC-couple the input to the MAX7443/MAX7444. This input capacitor stores a DC level such that the outputs are clamped to the appropriate DC voltage level.

Output Considerations

The outputs of the MAX7443/MAX7444 are typically connected to a 75Ω series back-match resistor followed by the video cable. Because of the inherent divide by two of this configuration, the blanking level of the video cable is always less than 1V, which complies with industry-standard video requirements. The video buffer can also drive an AC-coupled video load. Good video performance is achieved with an output capacitor as low as 220µF.

Power-Supply Bypassing and Layout

The MAX7443/MAX7444 operate from a single +5V supply. Bypass V_{CC} to GND with a $0.1\mu F$ capacitor. Place all external components as close to the device as possible. Refer to the MAX7443 evaluation kit for a proven PC board layout example.

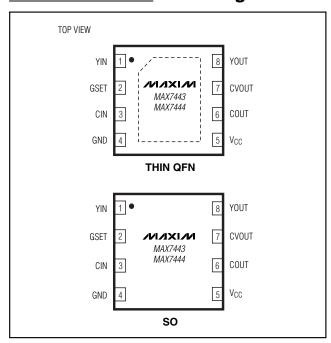
Exposed Pads

Both the SO and thin QFN packages of the MAX7443/MAX7444 have exposed pads on the bottom of the packages. These pads are electrically connected to GND and should be connected to the ground plane for improved thermal conductivity. Do not route signals under these packages.

Selector Guide

| PART | HIGH-FREQUENCY BOOST |
|------------|----------------------|
| MAX7443ETA | No |
| MAX7443ESA | No |
| MAX7444ETA | Yes |
| MAX7444ESA | Yes |

Pin Configurations

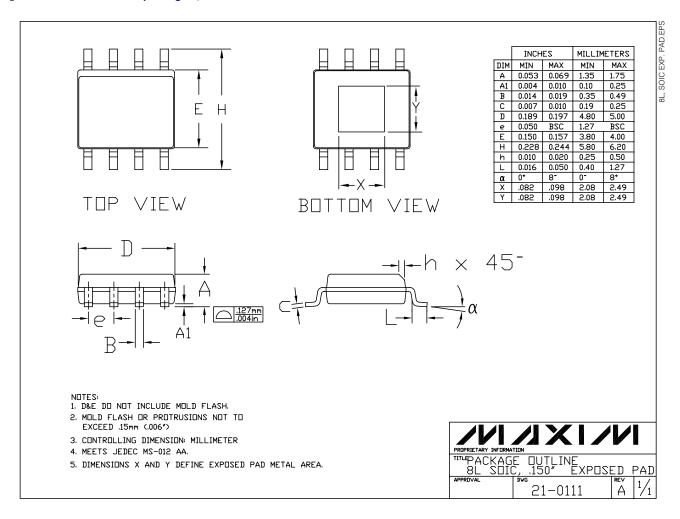


Chip Information

TRANSISTOR COUNT: 4252 PROCESS: BICMOS

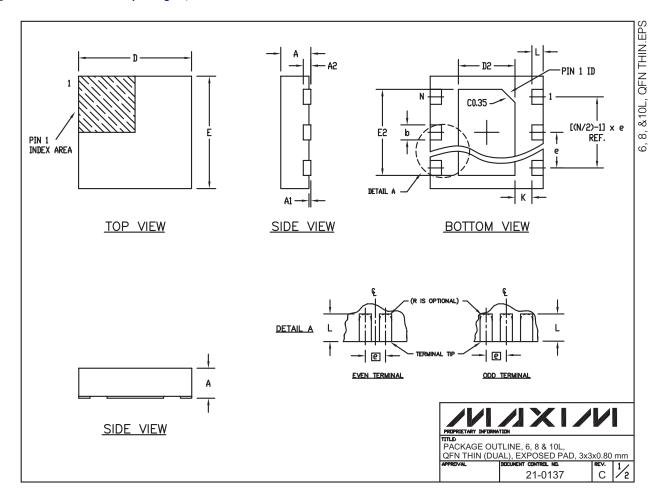
Package Information

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information, go to www.maxim-ic.com/packages.)



Package Information (continued)

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| COMMON DIMENSIONS | | | | | |
|-------------------|-----------|------|--|--|--|
| SYMBOL | MIN. | MAX. | | | |
| Α | 0.70 | 0.80 | | | |
| D | 2.90 | 3.10 | | | |
| Е | 2.90 | 3.10 | | | |
| A1 | 0.00 | 0.05 | | | |
| L | 0.20 | 0.40 | | | |
| k | 0.25 MIN | | | | |
| A2 | 0.20 REF. | | | | |

| PACKAGE VARIATIONS | | | | | | | | |
|--------------------|----|-----------|-----------|----------|----------------|-----------|---------------|--|
| PKG. CODE | N | D2 | E2 | е | JEDEC SPEC | b | [(N/2)-1] x e | |
| T633-1 | 6 | 1.50-0.10 | 2.30-0.10 | 0.95 BSC | MO229 / WEEA | 0.40-0.05 | 1.90 REF | |
| T833-1 | 8 | 1.50-0.10 | 2.30-0.10 | 0.65 BSC | MO229 / WEEC | 0.30-0.05 | 1.95 REF | |
| T1033-1 | 10 | 1.50-0.10 | 2.30-0.10 | 0.50 BSC | MO229 / WEED-3 | 0.25-0.05 | 2.00 REF | |

- 1. ALL DIMENSIONS ARE IN mm. ANGLES IN DEGREES.
 2. COPLANARITY SHALL NOT EXCEED 0.08 mm.
 3. WARPAGE SHALL NOT EXCEED 0.10 mm.

- 4. PACKAGE LENGTH/PACKAGE WIDTH ARE CONSIDERED AS SPECIAL CHARACTERISTIC(S).
 5. DRAWING CONFORMS TO JEDEC MO220.



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