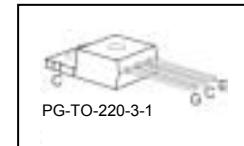
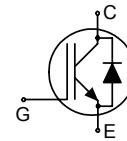


## Low Loss DuoPack : IGBT in TrenchStop® and Fieldstop technology with soft, fast recovery anti-parallel EmCon HE diode

- Very low  $V_{CE(sat)}$  1.5 V (typ.)
- Maximum Junction Temperature 175 °C
- Short circuit withstand time – 5μs
- Designed for :
  - Variable Speed Drive for washing machines, air conditioners and induction cooking
  - Uninterrupted Power Supply
- TrenchStop® and Fieldstop technology for 600 V applications offers :
  - very tight parameter distribution
  - high ruggedness, temperature stable behavior
- NPT technology offers easy parallel switching capability due to positive temperature coefficient in  $V_{CE(sat)}$
- Low EMI
- Low Gate Charge
- Very soft, fast recovery anti-parallel EmCon HE diode
- Qualified according to JEDEC<sup>1</sup> for target applications
- Pb-free lead plating; RoHS compliant
- Complete product spectrum and PSpice Models : <http://www.infineon.com/igbt/>



Type	$V_{CE}$	$I_c$	$V_{CE(sat)}, T_j=25^\circ\text{C}$	$T_{j,\text{max}}$	Marking Code	Package
IKP10N60T	600V	10A	1.5V	175°C	K10T60	PG-TO-220-3-1

### Maximum Ratings

Parameter	Symbol	Value	Unit
Collector-emitter voltage	$V_{CE}$	600	V
DC collector current, limited by $T_{j,\text{max}}$ $T_C = 25^\circ\text{C}$ $T_C = 100^\circ\text{C}$	$I_c$	20 10	A
Pulsed collector current, $t_p$ limited by $T_{j,\text{max}}$	$I_{C\text{puls}}$	30	
Turn off safe operating area $V_{CE} \leq 600\text{V}$ , $T_j \leq 175^\circ\text{C}$	-	30	
Diode forward current, limited by $T_{j,\text{max}}$ $T_C = 25^\circ\text{C}$ $T_C = 100^\circ\text{C}$	$I_F$	20 10	
Diode pulsed current, $t_p$ limited by $T_{j,\text{max}}$	$I_{F\text{puls}}$	30	
Gate-emitter voltage	$V_{GE}$	$\pm 20$	V
Short circuit withstand time <sup>2)</sup> $V_{GE} = 15\text{V}$ , $V_{CC} \leq 400\text{V}$ , $T_j \leq 150^\circ\text{C}$	$t_{sc}$	5	μs
Power dissipation $T_C = 25^\circ\text{C}$	$P_{\text{tot}}$	110	W
Operating junction temperature	$T_j$	-40...+175	°C
Storage temperature	$T_{stg}$	-55...+175	
Soldering temperature, wavesoldering, 1.6 mm (0.063 in.) from case for 10s		260	

<sup>1</sup> J-STD-020 and JESD-022

<sup>2)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

**Thermal Resistance**

Parameter	Symbol	Conditions	Max. Value		Unit
<b>Characteristic</b>					
IGBT thermal resistance, junction – case	$R_{thJC}$		1.35		K/W
Diode thermal resistance, junction – case	$R_{thJCD}$		1.9		
Thermal resistance, junction – ambient	$R_{thJA}$		62		

**Electrical Characteristic, at  $T_j = 25^\circ\text{C}$ , unless otherwise specified**

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	
<b>Static Characteristic</b>						
Collector-emitter breakdown voltage	$V_{(BR)CES}$	$V_{GE}=0\text{V}, I_C=0.2\text{mA}$	600	-	-	V
Collector-emitter saturation voltage	$V_{CE(\text{sat})}$	$V_{GE} = 15\text{V}, I_C=10\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	1.5	2.05	
Diode forward voltage	$V_F$	$V_{GE}=0\text{V}, I_F=10\text{A}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	1.6	2.0	
Gate-emitter threshold voltage	$V_{GE(\text{th})}$	$I_C=0.3\text{mA}, V_{CE}=V_{GE}$	4.1	4.6	5.7	
Zero gate voltage collector current	$I_{CES}$	$V_{CE}=600\text{V}, V_{GE}=0\text{V}$ $T_j=25^\circ\text{C}$ $T_j=175^\circ\text{C}$	-	-	40	$\mu\text{A}$
-			-	-	1000	
Gate-emitter leakage current	$I_{GES}$	$V_{CE}=0\text{V}, V_{GE}=20\text{V}$	-	-	100	nA
Transconductance	$g_{fs}$	$V_{CE}=20\text{V}, I_C=10\text{A}$	-	6	-	S
Integrated gate resistor	$R_{Gint}$		none			$\Omega$

**Dynamic Characteristic**

Input capacitance	$C_{iss}$	$V_{CE}=25\text{V}, V_{GE}=0\text{V}, f=1\text{MHz}$	-	551	-	pF
Output capacitance	$C_{oss}$		-	40	-	
Reverse transfer capacitance	$C_{rss}$		-	17	-	
Gate charge	$Q_{\text{Gate}}$	$V_{CC}=480\text{V}, I_C=\text{Fehler! Verweisquelle konnte nicht gefunden werden. A}$ $V_{GE}=15\text{V}$	-	62	-	nC

Internal emitter inductance measured 5mm (0.197 in.) from case	$L_E$		-	7	-	nH
Short circuit collector current <sup>1)</sup>	$I_{C(SC)}$	$V_{GE}=15V, t_{SC} \leq 5\mu s$ $V_{CC} = 400V,$ $T_j = 25^\circ C$	-	100	-	A

**Switching Characteristic, Inductive Load, at  $T_j=25^\circ C$** 

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	

**IGBT Characteristic**

Turn-on delay time	$t_{d(on)}$	$T_j=25^\circ C,$ $V_{CC}=400V, I_C=10A,$ $V_{GE}=0/15V,$ $R_G=23\Omega,$ $L_\sigma^{(2)}=60nH,$ $C_\sigma^{(2)}=40pF$ Energy losses include "tail" and diode reverse recovery.	-	12	-	ns
Rise time	$t_r$		-	8	-	
Turn-off delay time	$t_{d(off)}$		-	215	-	
Fall time	$t_f$		-	38	-	
Turn-on energy	$E_{on}$		-	0.16	-	mJ
Turn-off energy	$E_{off}$		-	0.27	-	
Total switching energy	$E_{ts}$		-	0.43	-	

**Anti-Parallel Diode Characteristic**

Diode reverse recovery time	$t_{rr}$	$T_j=25^\circ C,$ $V_R=400V, I_F=10A,$ $di_F/dt=880A/\mu s$	-	115	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	0.38	-	$\mu C$
Diode peak reverse recovery current	$I_{rrm}$		-	10	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	680	-	$A/\mu s$

**Switching Characteristic, Inductive Load, at  $T_j=175^\circ C$** 

Parameter	Symbol	Conditions	Value			Unit
			min.	typ.	max.	

**IGBT Characteristic**

Turn-on delay time	$t_{d(on)}$	$T_j=175^\circ C,$ $V_{CC}=400V, I_C=10A,$ $V_{GE}=0/15V,$ $R_G=23\Omega$ $L_\sigma^{(1)}=60nH,$ $C_\sigma^{(1)}=40pF$ Energy losses include "tail" and diode reverse recovery.	-	10	-	ns
Rise time	$t_r$		-	11	-	
Turn-off delay time	$t_{d(off)}$		-	233	-	
Fall time	$t_f$		-	63	-	
Turn-on energy	$E_{on}$		-	0.26	-	mJ
Turn-off energy	$E_{off}$		-	0.35	-	
Total switching energy	$E_{ts}$		-	0.61	-	

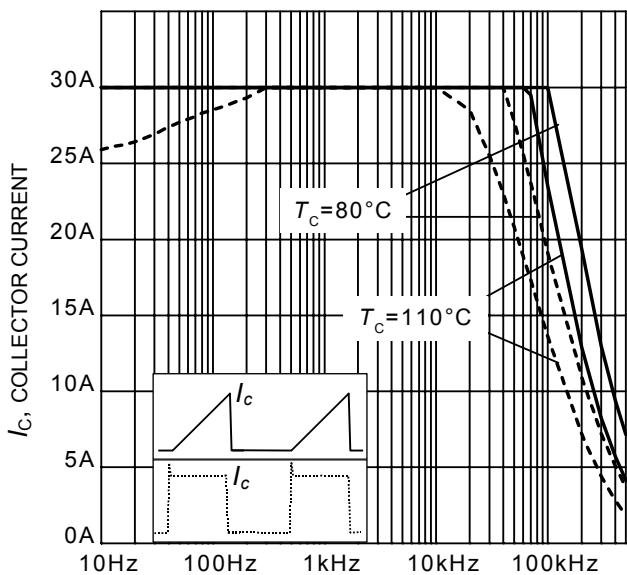
**Anti-Parallel Diode Characteristic**

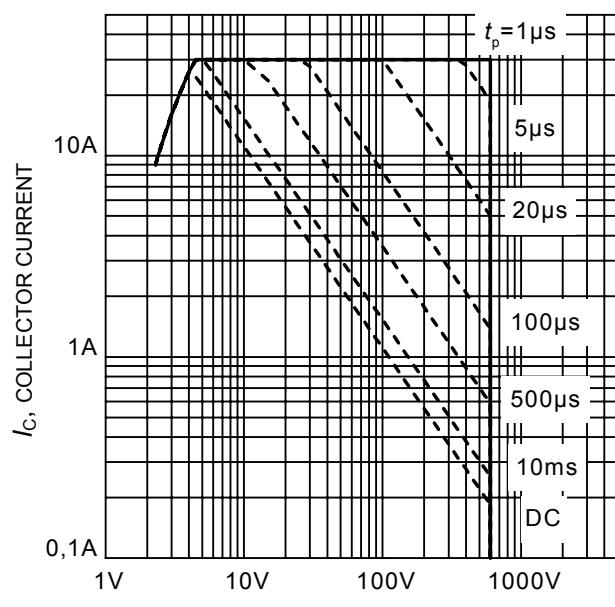
Diode reverse recovery time	$t_{rr}$	$T_j=175^\circ C$ $V_R=400V, I_F=10A,$ $di_F/dt=880A/\mu s$	-	200	-	ns
Diode reverse recovery charge	$Q_{rr}$		-	0.92	-	$\mu C$
Diode peak reverse recovery current	$I_{rrm}$		-	13	-	A
Diode peak rate of fall of reverse recovery current during $t_b$	$di_{rr}/dt$		-	390	-	$A/\mu s$

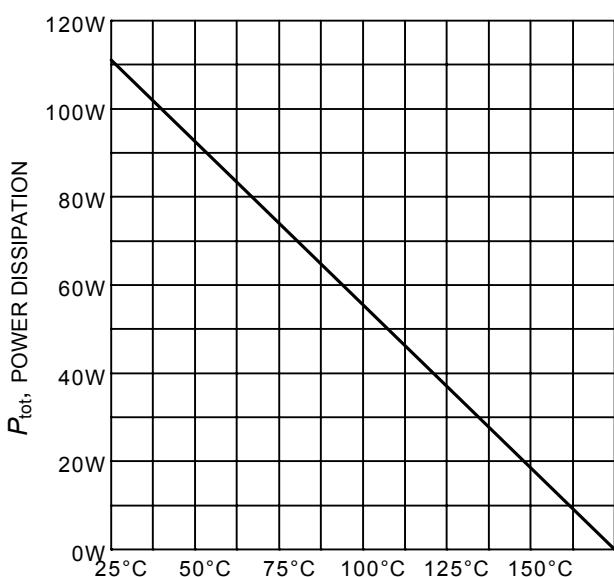
<sup>1)</sup> Allowed number of short circuits: <1000; time between short circuits: >1s.

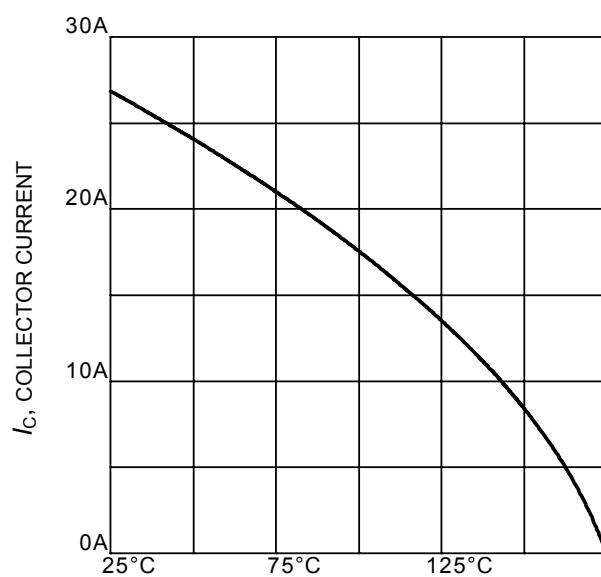
<sup>2)</sup> Leakage inductance  $L_\sigma$  and Stray capacity  $C_\sigma$  due to dynamic test circuit in Figure E.

<sup>1)</sup> Leakage inductance  $L_\sigma$  and Stray capacity  $C_\sigma$  due to dynamic test circuit in Figure E.

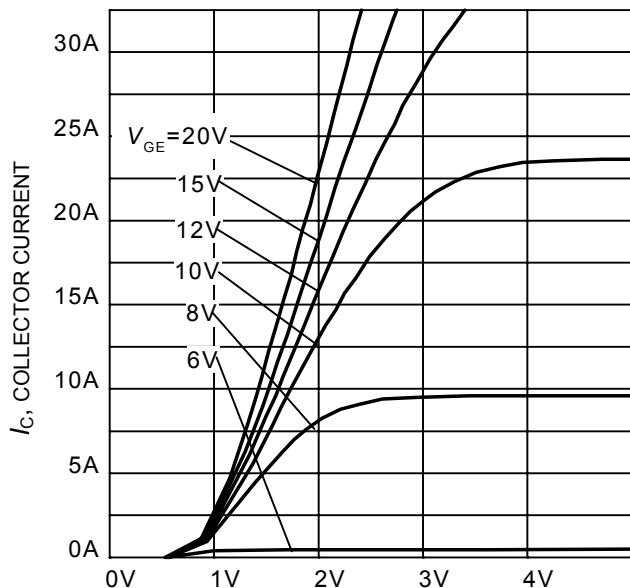

 $f$ , SWITCHING FREQUENCY

**Figure 1. Collector current as a function of switching frequency**
 $(T_j \leq 175^\circ\text{C}, D = 0.5, V_{CE} = 400\text{V}, V_{GE} = 0/+15\text{V}, R_G = 23\Omega)$ 

 $V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

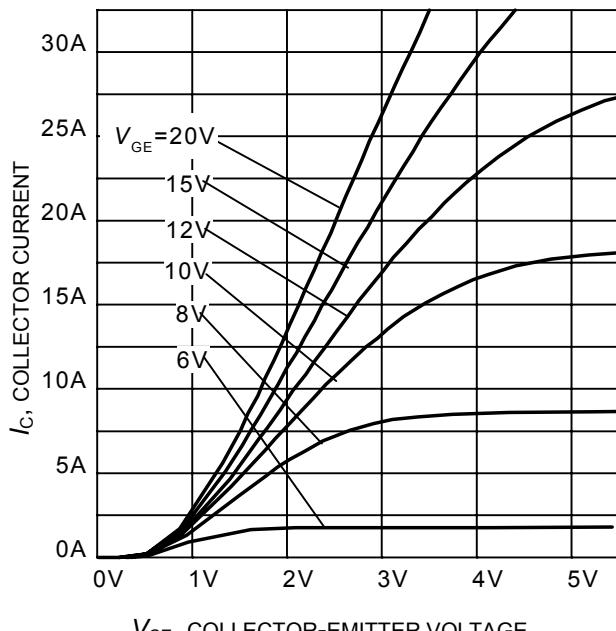
**Figure 2. Safe operating area**
 $(D = 0, T_c = 25^\circ\text{C}, T_j \leq 175^\circ\text{C}, V_{GE}=15\text{V})$ 

 $T_c$ , CASE TEMPERATURE

**Figure 3. Power dissipation as a function of case temperature**
 $(T_j \leq 175^\circ\text{C})$ 

 $T_c$ , CASE TEMPERATURE

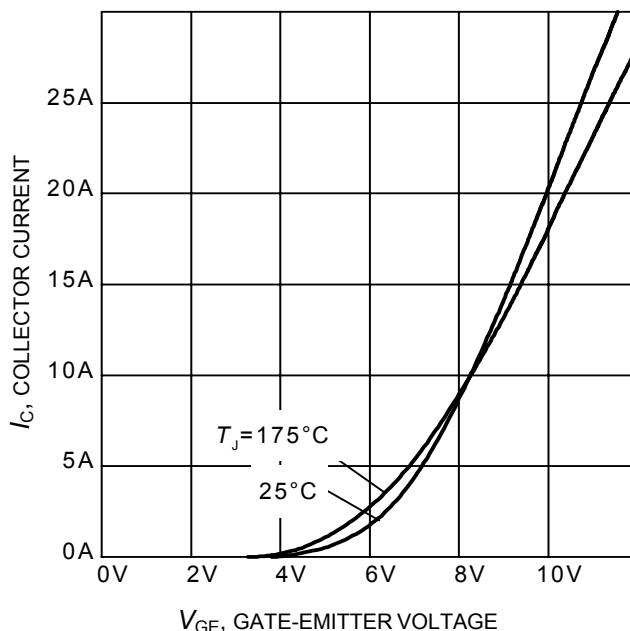
**Figure 4. Collector current as a function of case temperature**
 $(V_{GE} \geq 15\text{V}, T_j \leq 175^\circ\text{C})$



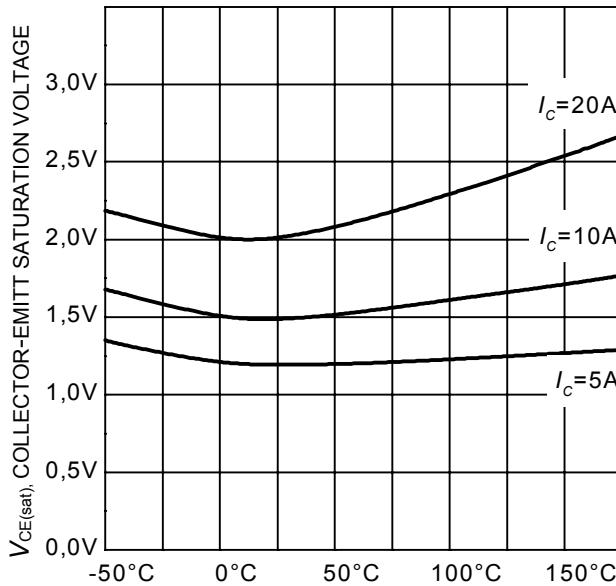
**Figure 5. Typical output characteristic**  
( $T_j = 25^\circ\text{C}$ )



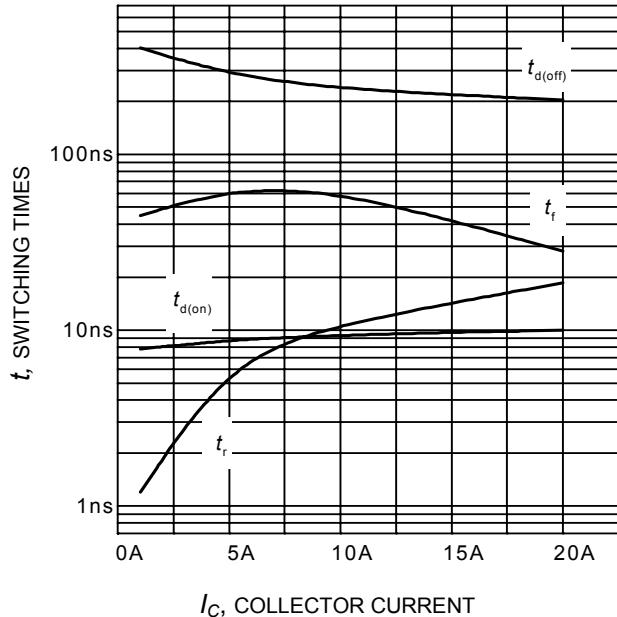
**Figure 6. Typical output characteristic**  
( $T_j = 175^\circ\text{C}$ )



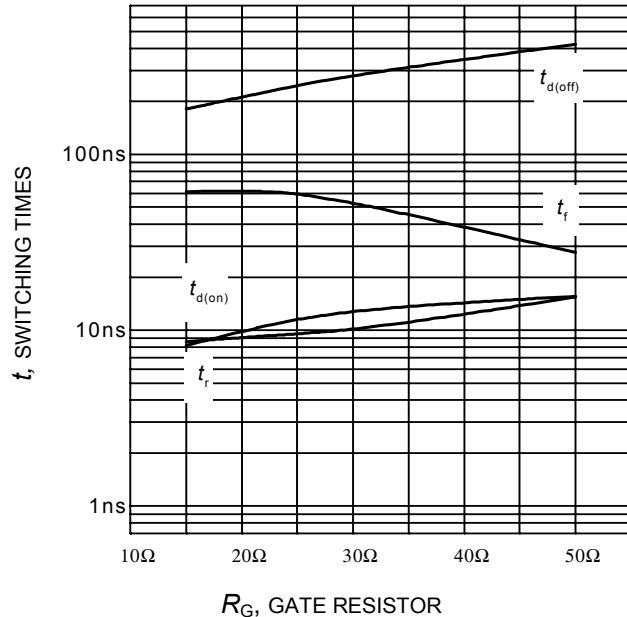
**Figure 7. Typical transfer characteristic**  
( $V_{CE}=20\text{V}$ )



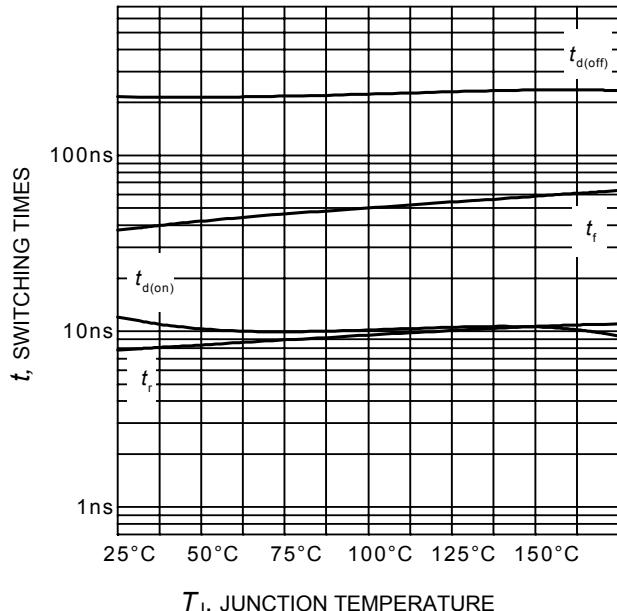
**Figure 8. Typical collector-emitter saturation voltage as a function of junction temperature**  
( $V_{GE} = 15\text{V}$ )



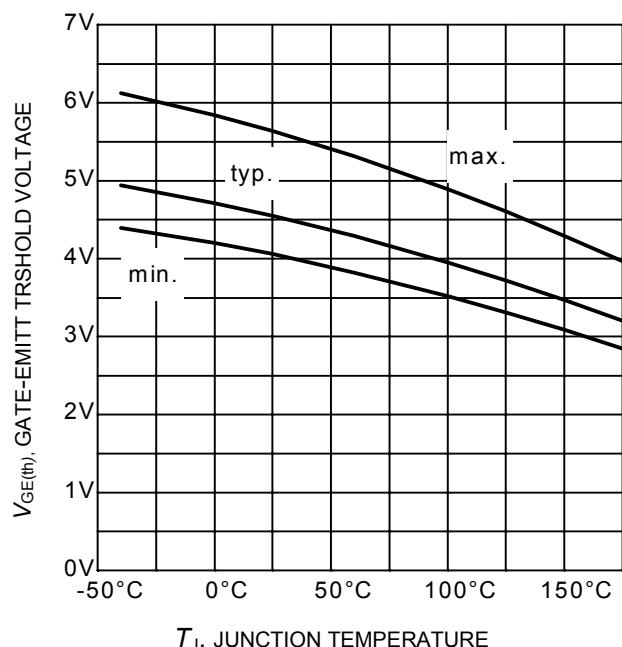
**Figure 9.** Typical switching times as a function of collector current  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $R_G = 23\Omega$ ,  
Dynamic test circuit in Figure E)



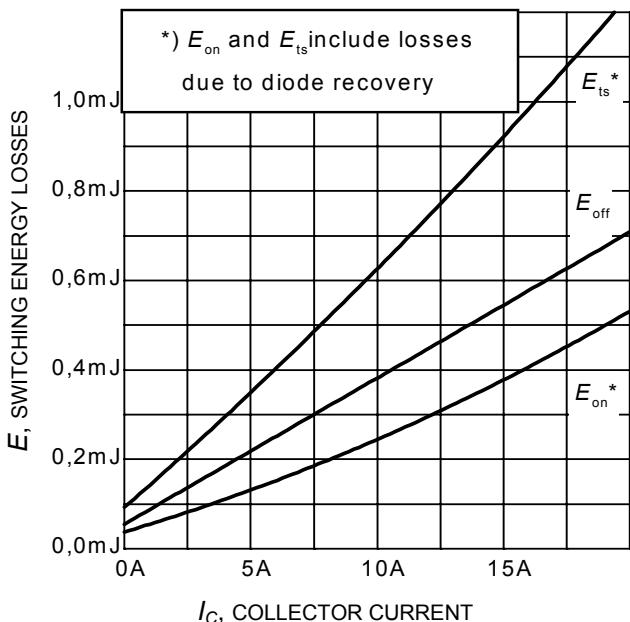
**Figure 10.** Typical switching times as a function of gate resistor  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $I_C = 10\text{A}$ ,  
Dynamic test circuit in Figure E)



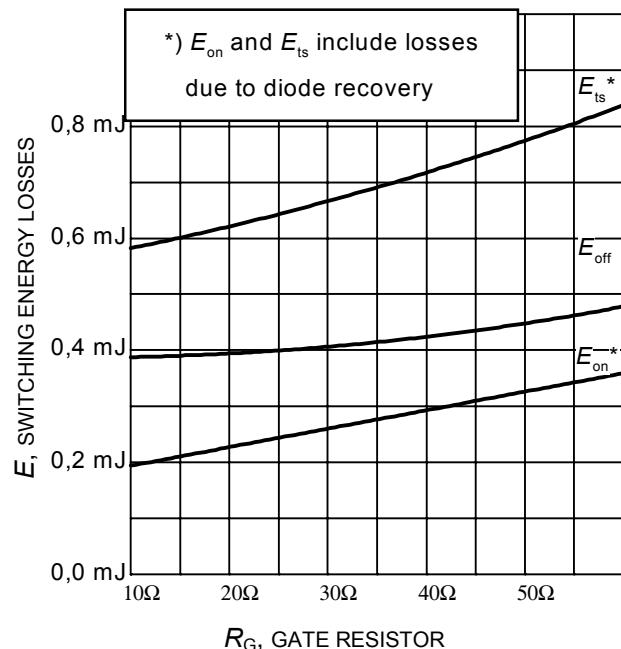
**Figure 11.** Typical switching times as a function of junction temperature  
(inductive load,  $V_{CE} = 400\text{V}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $I_C = 10\text{A}$ ,  $R_G = 23\Omega$ ,  
Dynamic test circuit in Figure E)



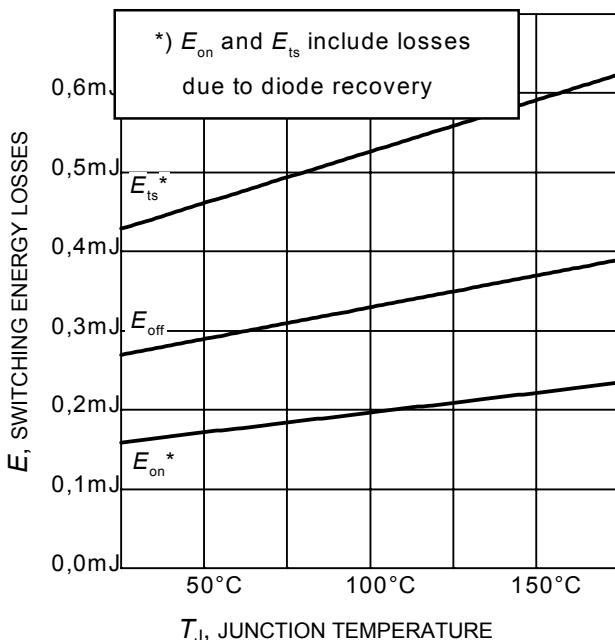
**Figure 12.** Gate-emitter threshold voltage as a function of junction temperature  
( $I_C = 0.3\text{mA}$ )



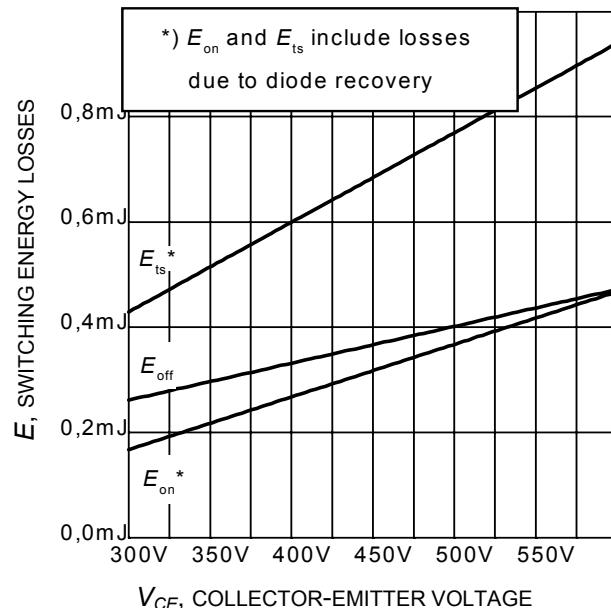
**Figure 13. Typical switching energy losses as a function of collector current**  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $R_G = 23\Omega$ ,  
Dynamic test circuit in Figure E)



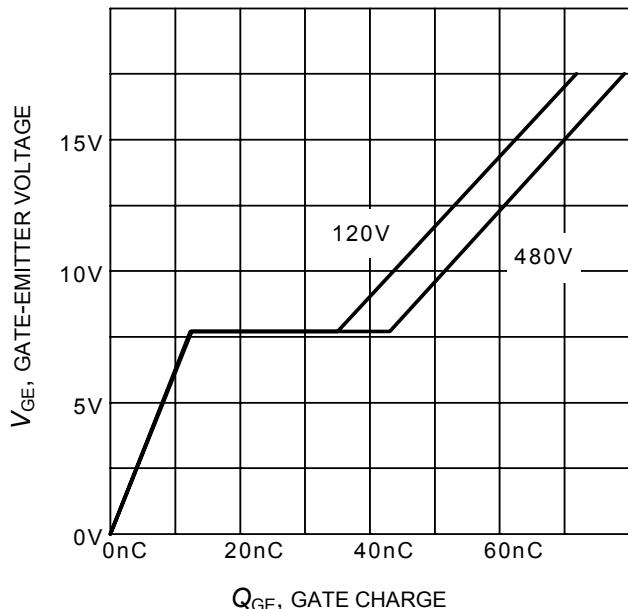
**Figure 14. Typical switching energy losses as a function of gate resistor**  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{CE} = 400\text{V}$ ,  $V_{GE} = 0/15\text{V}$ ,  $I_C = 10\text{A}$ ,  
Dynamic test circuit in Figure E)

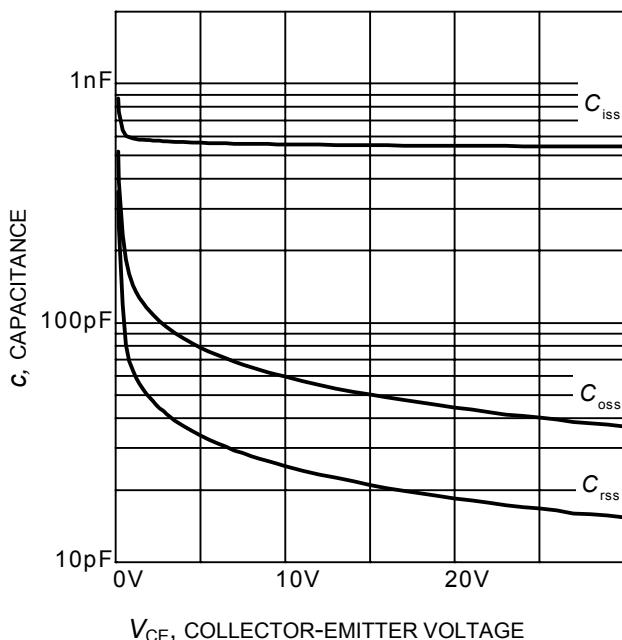


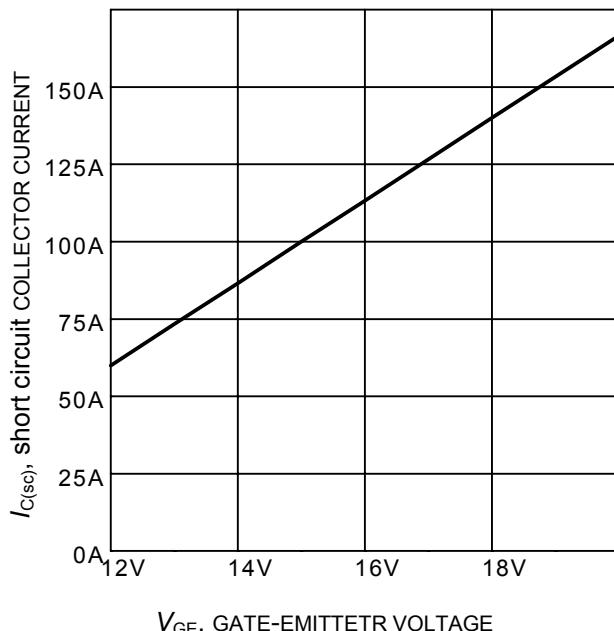
**Figure 15. Typical switching energy losses as a function of junction temperature**  
(inductive load,  $V_{CE} = 400\text{V}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $I_C = 10\text{A}$ ,  $R_G = 23\Omega$ ,  
Dynamic test circuit in Figure E)

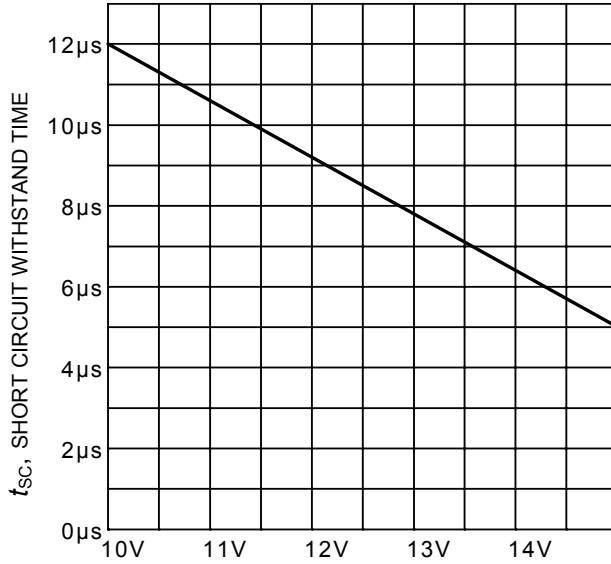


**Figure 16. Typical switching energy losses as a function of collector-emitter voltage**  
(inductive load,  $T_J = 175^\circ\text{C}$ ,  
 $V_{GE} = 0/15\text{V}$ ,  $I_C = 10\text{A}$ ,  $R_G = 23\Omega$ ,  
Dynamic test circuit in Figure E)

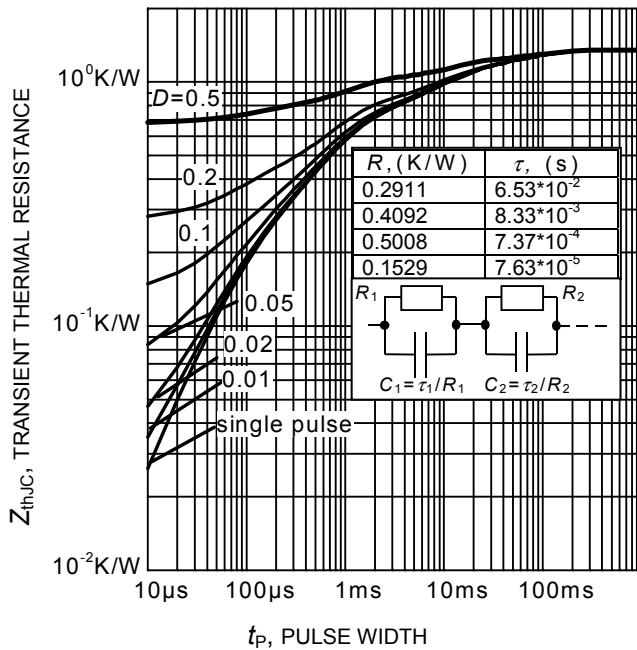

 $Q_{GE}$ , GATE CHARGE

**Figure 17. Typical gate charge**  
 $(I_C=10\text{ A})$ 

 $V_{CE}$ , COLLECTOR-EMITTER VOLTAGE

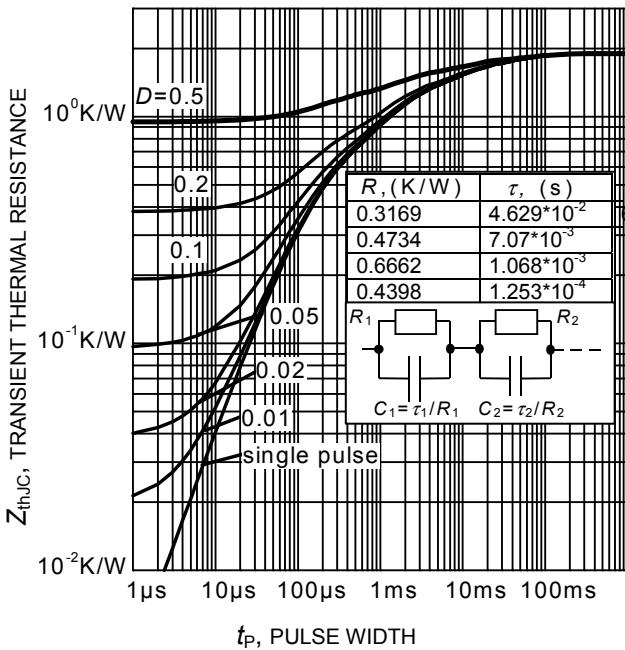
**Figure 18. Typical capacitance as a function of collector-emitter voltage**  
 $(V_{GE}=0\text{V}, f=1\text{ MHz})$ 

 $V_{GE}$ , GATE-EMITTER VOLTAGE

**Figure 19. Typical short circuit collector current as a function of gate-emitter voltage**  
 $(V_{CE} \leq 400\text{V}, T_j \leq 150^\circ\text{C})$ 

 $V_{GE}$ , GATE-EMITTER VOLTAGE

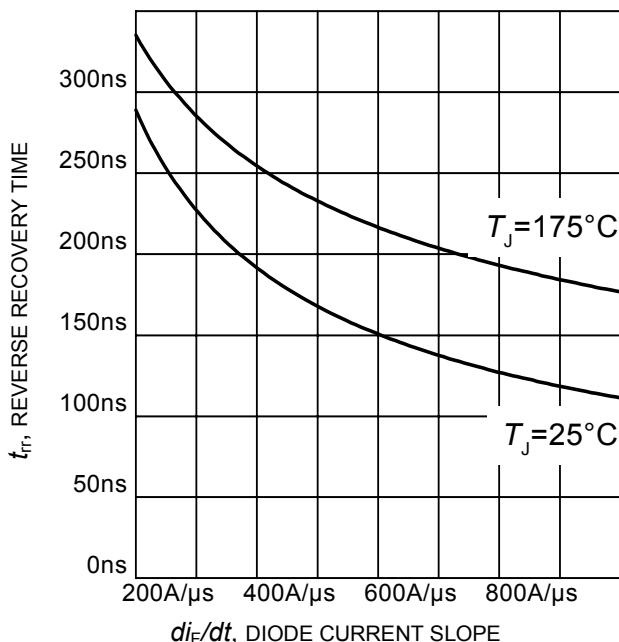
**Figure 20. Short circuit withstand time as a function of gate-emitter voltage**  
 $(V_{CE}=600\text{V}, \text{start at } T_j=25^\circ\text{C}, T_{jmax}<150^\circ\text{C})$



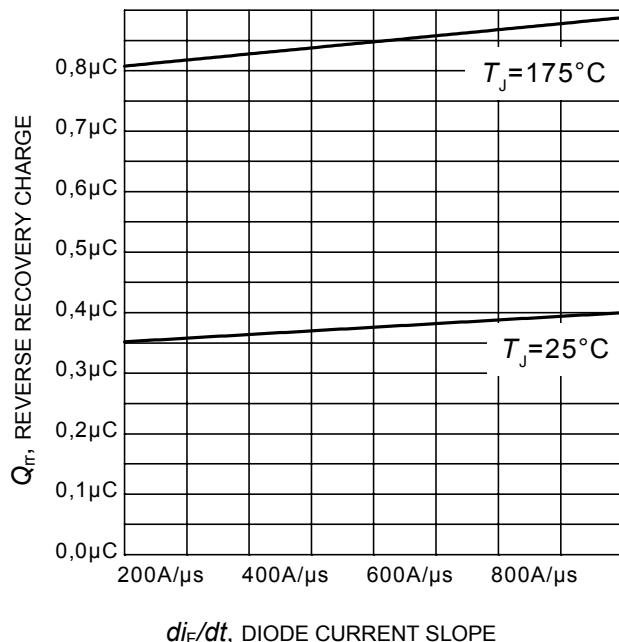
**Figure 21. IGBT transient thermal resistance**  
( $D = t_p / T$ )



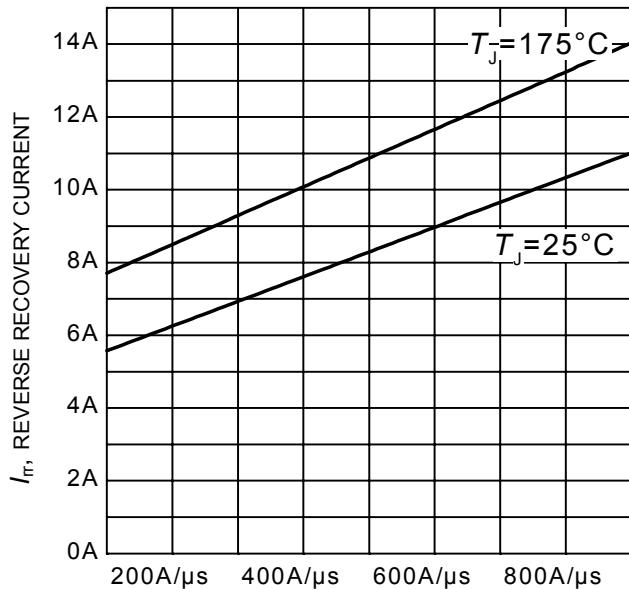
**Figure 22. Diode transient thermal impedance as a function of pulse width**  
( $D=t_p/T$ )



**Figure 23. Typical reverse recovery time as a function of diode current slope**  
( $V_R=400V$ ,  $I_F=10A$ , Dynamic test circuit in Figure E)



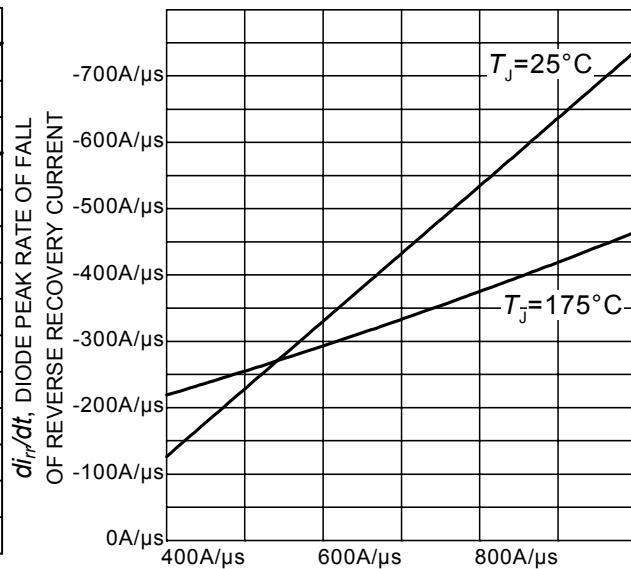
**Figure 24. Typical reverse recovery charge as a function of diode current slope**  
( $V_R = 400V$ ,  $I_F = 10A$ , Dynamic test circuit in Figure E)



$di_F/dt$ , DIODE CURRENT SLOPE

**Figure 25. Typical reverse recovery current as a function of diode current slope**

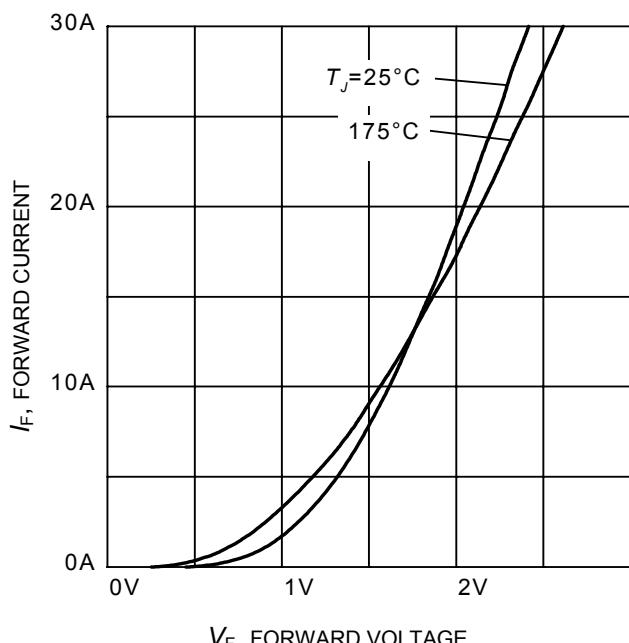
( $V_R = 400V$ ,  $I_F = 10A$ ,  
Dynamic test circuit in Figure E)



$di_F/dt$ , DIODE CURRENT SLOPE

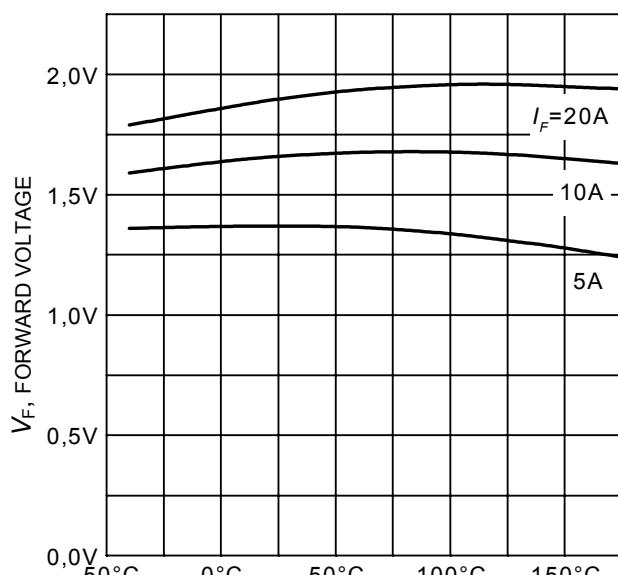
**Figure 26. Typical diode peak rate of fall of reverse recovery current as a function of diode current slope**

( $V_R=400V$ ,  $I_F=10A$ ,  
Dynamic test circuit in Figure E)



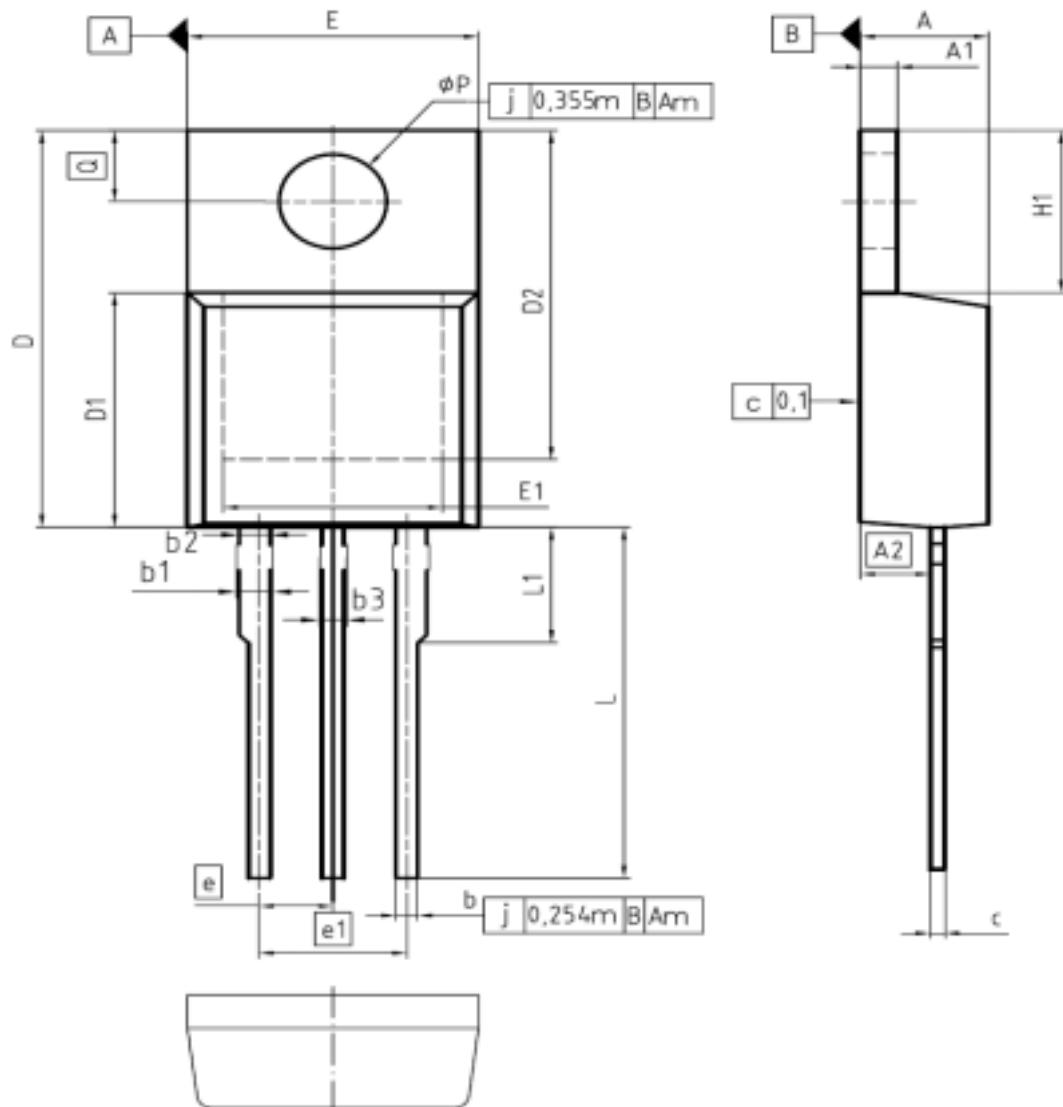
$V_F$ , FORWARD VOLTAGE

**Figure 27. Typical diode forward current as a function of forward voltage**



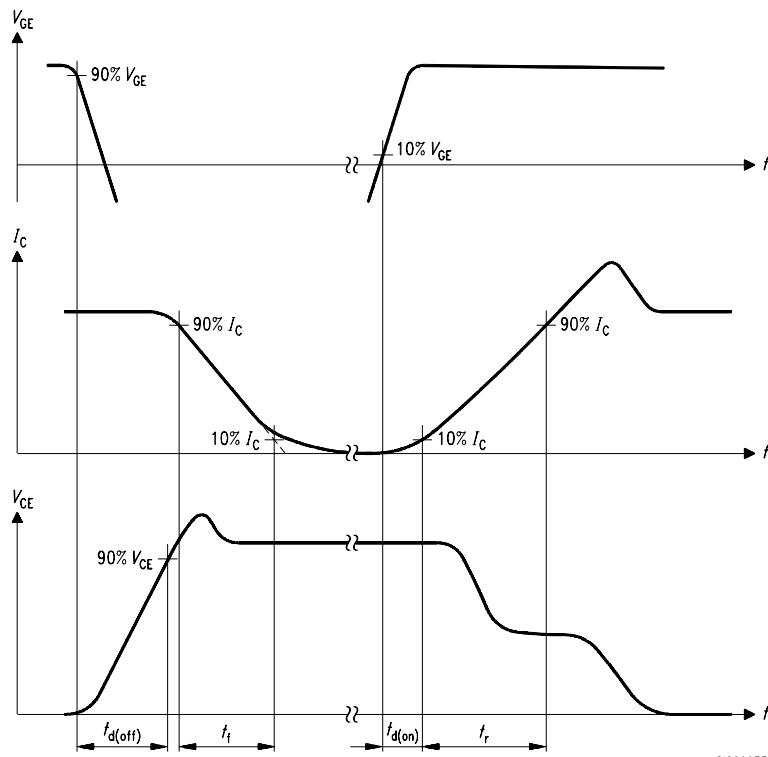
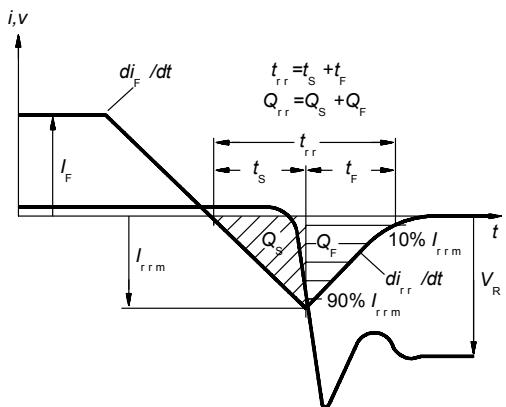
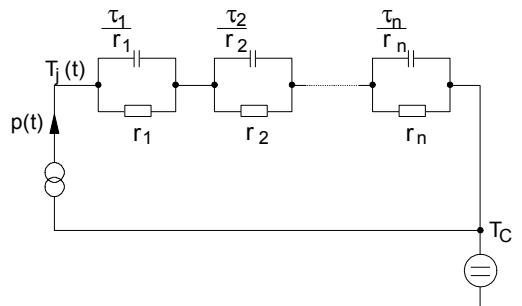
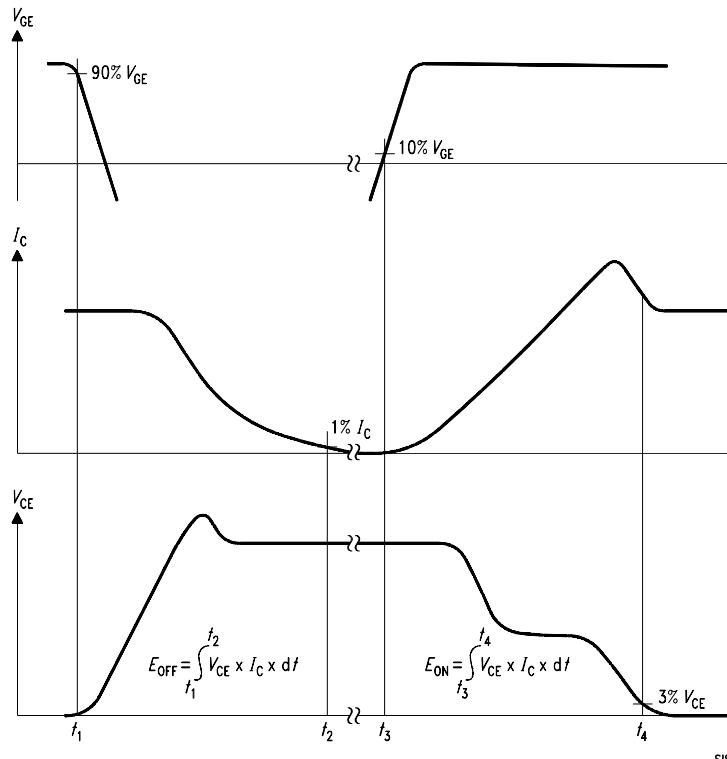
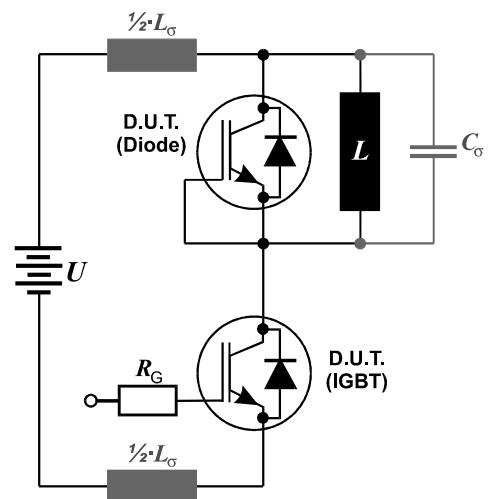
$T_J$ , JUNCTION TEMPERATURE

**Figure 28. Typical diode forward voltage as a function of junction temperature**

**PG-T0-220-3-1**


DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.30	4.57	0.169	0.180
A1	1.17	1.40	0.046	0.055
A2	2.15	2.72	0.085	0.107
b	0.65	0.86	0.026	0.034
b1	0.95	1.40	0.037	0.055
b2	0.95	1.15	0.037	0.045
b3	0.65	1.15	0.026	0.045
c	0.33	0.60	0.013	0.024
D	14.81	15.96	0.583	0.628
D1	8.51	9.45	0.335	0.372
D2	12.19	13.10	0.480	0.516
E	9.70	10.36	0.382	0.408
E1	6.50	8.80	0.256	0.339
e	2.54		0.100	
e1	5.08		0.200	
N	3		3	
H1	5.90	6.90	0.232	0.272
L	13.00	14.00	0.512	0.551
L1	-	4.80	-	0.189
φP	3.60	3.89	0.142	0.153
Q	2.60	3.00	0.102	0.118

DOCUMENT NO.	Z8B00003318
SCALE	0 2.5 0 2.5 5mm
EUROPEAN PROJECTION	
ISSUE DATE	23-08-2007
REVISION	05


**Figure A. Definition of switching times**

**Figure C. Definition of diodes switching characteristics**

**Figure D. Thermal equivalent circuit**

**Figure B. Definition of switching losses**

**Figure E. Dynamic test circuit**  
 Leakage inductance  $L_\sigma = 60\text{nH}$   
 and Stray capacity  $C_\sigma = 40\text{pF}$ .

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