

October 2001

# FDP6676S / FDB6676S

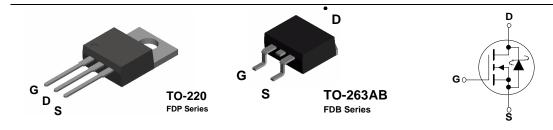
# 30V N-Channel PowerTrench® SyncFET<sup>™</sup>

### **General Description**

This MOSFET is designed to replace a single MOSFET and parallel Schottky diode in synchronous DC:DC power supplies. This 30V MOSFET is designed to maximize power conversion efficiency, providing a low  $R_{\rm DS(ON)}$  and low gate charge. The FDP/B6676S includes an integrated Schottky diode using Fairchild's monolithic SyncFET technology. The performance of the FDP/B6676S as the low-side switch in a synchronous rectifier is indistinguishable from the performance of the FDP/B6676 in parallel with a Schottky diode.

#### **Features**

- 38 A, 30 V.  $R_{DS(ON)} = 6.5 \ m\Omega \ @ \ V_{GS} = 10 \ V$   $R_{DS(ON)} = 8.0 \ m\Omega \ @ \ V_{GS} = 4.5 \ V$
- Includes SyncFET Schottky body diode
- Low gate charge (40nC typical)
- High performance trench technology for extremely low R<sub>DS(ON)</sub> and fast switching
- High power and current handling capability



Absolute Maximum Ratings T<sub>A</sub>=25°C unless otherwise noted

Symbol	Parameter		Ratings	Units
V <sub>DSS</sub>	Drain-Source Voltage		30	V
V <sub>GSS</sub>	Gate-Source Voltage		±16	V
I <sub>D</sub>	Drain Current - Continuous	(Note 1)	76	A
	- Pulsed	(Note 1)	150	
P <sub>D</sub>	Total Power Dissipation @ T <sub>C</sub> = 25°C		70	W
	Derate above 25°C		0.56	W/°C
T <sub>J</sub> , T <sub>STG</sub>	Operating and Storage Junction Temperature Range		-55 to +150	°C
TL	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		275	°C

### **Thermal Characteristics**

$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.8	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	55	°C/W

**Package Marking and Ordering Information** 

Device Marking	Device	Reel Size	Tape width	Quantity
FDB6676S	FDB6676S	13"	24mm	800
FDP6676S FDP6676S		Tube	n/a	45

Symbol	Parameter	Test Conditions	Min	Тур	Max	Units
Drain-Sc	ource Avalanche Ratings (Note	2)	I	ı	I	I
W <sub>DSS</sub>	Drain-Source Avalanche Energy	Single Pulse, $V_{DD} = 25 \text{ V}$ , $I_D=12\text{A}$			310	mJ
I <sub>AR</sub>	Drain-Source Avalanche Current				12	Α
Off Char	acteristics					
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	$V_{GS} = 0 \text{ V}, I_{D} = 1 \text{ mA}$	30			V
<u>ΔBV<sub>DSS</sub></u> ΔT <sub>J</sub>	Breakdown Voltage Temperature Coefficient	I <sub>D</sub> = 1 mA, Referenced to 25°C		25		mV/°C
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	$V_{DS} = 24 \text{ V}, \qquad V_{GS} = 0 \text{ V}$			500	μΑ
I <sub>GSSF</sub>	Gate-Body Leakage, Forward	$V_{GS} = 16 \text{ V}, \qquad V_{DS} = 0 \text{ V}$			100	nA
I <sub>GSSR</sub>	Gate-Body Leakage, Reverse	$V_{GS} = -16 \text{ V}$ $V_{DS} = 0 \text{ V}$			-100	nA
On Char	acteristics (Note 2)	·		•		•
V <sub>GS(th)</sub>	Gate Threshold Voltage	$V_{DS} = V_{GS}$ , $I_D = 1$ mA	1	1.3	3	V
$\frac{\Delta V_{GS(th)}}{\Delta T_J}$	Gate Threshold Voltage Temperature Coefficient	I <sub>D</sub> = 1 mA, Referenced to 25°C		-8.4		mV/°C
R <sub>DS(on)</sub>	Static Drain–Source On–Resistance	$V_{GS} = 10 \text{ V}, \qquad I_D = 38 \text{ A}$ $V_{GS} = 4.5 \text{ V}, \qquad I_D = 35 \text{ A}$ $V_{GS} = 10 \text{ V}, I_D = 38 \text{ A}, T_J = 125 ^{\circ}\text{C}$		4.7 5.2 7.3	6.5 8.0 11	mΩ
I <sub>D(on)</sub>	On-State Drain Current	$V_{GS} = 10 \text{ V}, \qquad V_{DS} = 10 \text{ V}$	60			Α
<b>G</b> FS	Forward Transconductance	$V_{DS} = 10 \text{ V}, \qquad I_{D} = 38 \text{ A}$		145		S
Dvnamio	Characteristics	•				
C <sub>iss</sub>	Input Capacitance	$V_{DS} = 15 \text{ V}, \qquad V_{GS} = 0 \text{ V},$		4853		pF
C <sub>oss</sub>	Output Capacitance	f = 1.0 MHz		850		pF
C <sub>rss</sub>	Reverse Transfer Capacitance			316		pF
Switchir	ng Characteristics (Note 2)					
t <sub>d(on)</sub>	Turn-On Delay Time	$V_{DS} = 15 \text{ V}, \qquad I_{D} = 1 \text{ A},$		14	25	ns
t <sub>r</sub>	Turn-On Rise Time	$V_{GS} = 10 \text{ V}, \qquad R_{GEN} = 6 \Omega$		11	20	ns
t <sub>d(off)</sub>	Turn-Off Delay Time			89	142	ns
t <sub>f</sub>	Turn-Off Fall Time	7		31	50	ns
Qg	Total Gate Charge	$V_{DS} = 15 \text{ V}, \qquad I_{D} = 38 \text{ A}, \\ V_{GS} = 5 \text{ V}$		40	56	nC
Q <sub>gs</sub>	Gate-Source Charge			10		nC
$Q_{gd}$	Gate-Drain Charge			11		nC
Drain-S	ource Diode Characteristics	and Maximum Ratings				
V <sub>SD</sub>	Drain–Source Diode Forward Voltage	$V_{GS} = 0 \text{ V},  I_S = 3.5 \text{ A}  \text{(Note 1)} $ $V_{GS} = 0 \text{ V},  I_S = 7 \text{ A}  \text{(Note 1)}$		0.4 0.5	0.7	V
t <sub>rr</sub>	Diode Reverse Recovery Time	I <sub>F</sub> = 3.5 A,		28.5		nS
Q <sub>rr</sub>	Diode Reverse Recovery Charge	$d_{iF}/d_t = 300 \text{ A/}\mu\text{s} \qquad \text{(Note 2)}$		57		nC

### Notes:

<sup>1.</sup> Pulse Test: Pulse Width <  $300\mu s$ , Duty Cycle < 2.0%

<sup>2.</sup> See "SyncFET Schottky body diode characteristics" below.

# **Typical Characteristics**

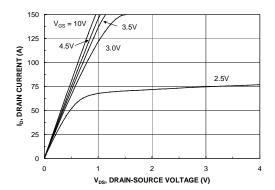


Figure 1. On-Region Characteristics.

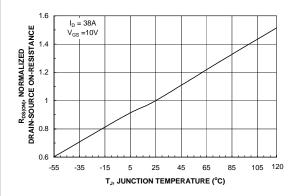


Figure 3. On-Resistance Variation with Temperature.

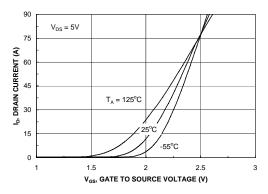


Figure 5. Transfer Characteristics.

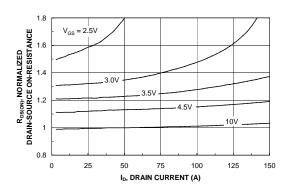


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

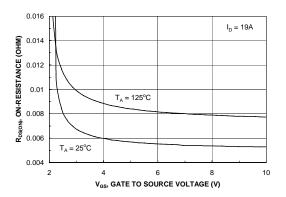


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

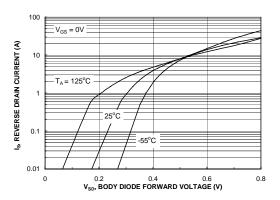
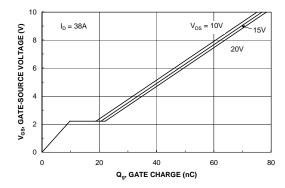


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

## Typical Characteristics (continued)



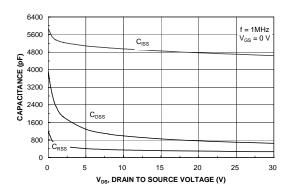


Figure 7. Gate Charge Characteristics.

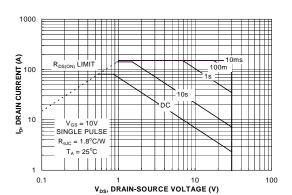


Figure 8. Capacitance Characteristics.

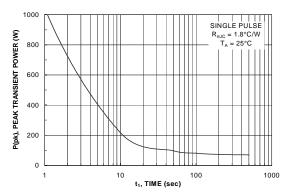


Figure 9. Maximum Safe Operating Area.



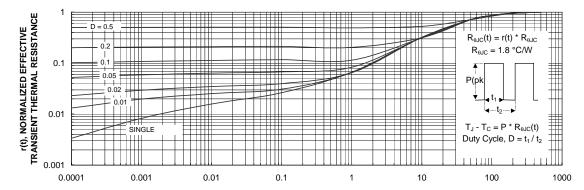


Figure 11. Transient Thermal Response Curve.

## Typical Characteristics (continued)

# SyncFET Schottky Body Diode Characteristics

Fairchild's SyncFET process embeds a Schottky diode in parallel with PowerTrench MOSFET. This diode exhibits similar characteristics to a discrete external Schottky diode in parallel with a MOSFET. Figure 12 FDP6676S.

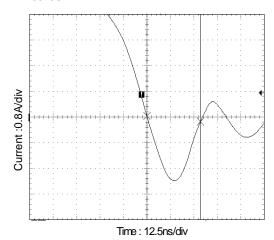


Figure 12. FDP6676S SyncFET body diode reverse recovery characteristic.

For comparison purposes, Figure 13 shows the reverse recovery characteristics of the body diode of an equivalent size MOSFET produced without SyncFET (FDP6676).

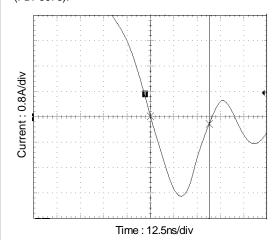


Figure 13. Non-SyncFET (FDP6676) body diode reverse recovery characteristic.

Schottky barrier diodes exhibit significant leakage at high temperature and high reverse voltage. This will increase the power in the device.

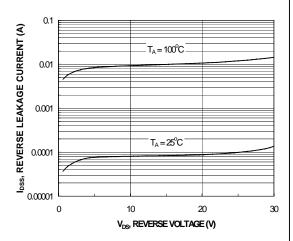


Figure 14. SyncFET diode reverse leakage versus drain-source voltage and temperature.

#### **TRADEMARKS**

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

SMART START™  $VCX^{TM}$ FAST ® OPTOLOGIC™ STAR\*POWER™ FASTr™ Bottomless™ OPTOPLANAR™ Stealth™ CoolFET™ FRFET™ PACMAN™ SuperSOT™-3 CROSSVOLT™ GlobalOptoisolator™ POP™ SuperSOT™-6 DenseTrench™ GTO™ Power247™  $HiSeC^{TM}$ SuperSOT™-8  $Power Trench^{\, @}$ DOME™ SyncFET™ EcoSPARK™ ISOPLANAR™ QFET™ TinyLogic™ E<sup>2</sup>CMOS<sup>TM</sup> LittleFET™  $OS^{TM}$ 

EnSigna™ MicroFET™ QT Optoelectronics™ TruTranslation™
FACT™ MicroPak™ Quiet Series™ UHC™
FACT Quiet Series™ MICROWIRE™ SILENT SWITCHER® UltraFET®

STAR\*POWER is used under license

#### DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS. NOR THE RIGHTS OF OTHERS.

#### LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the

2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

#### PRODUCT STATUS DEFINITIONS

### **Definition of Terms**

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

Rev. H4