

April 1988 Revised July 1999

## 74F139

# **Dual 1-of-4 Decoder/Demultiplexer**

#### **General Description**

The F139 is a high-speed, dual 1-of-4 decoder/demultiplexer. The device has two independent decoders, each accepting two inputs and providing four mutually exclusive active LOW outputs. Each decoder has an active LOW Enable input which can be used as a data input for a 4-output demultiplexer. Each half of the F139 can be used as a function generator providing all four minterms of two variables.

#### **Features**

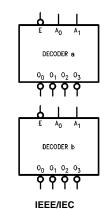
- Multifunction capability
- Two completely independent 1-of-4 decoders
- Active LOW mutually exclusive outputs

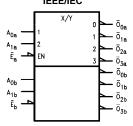
### **Ordering Code:**

Order Number Package Number		Package Description				
74F139SC	M16A	16-Lead Small Outline Integrated Circuit (SOIC), JEDEC MS-012, 0.150 Narrow				
74F139SJ	M16D	16-Lead Small Outline Package (SOP), EIAJ TYPE II, 5.3mm Wide				
74F139PC	N16E	16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide				

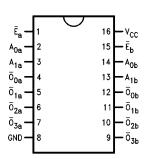
Devices also available in Tape and Reel. Specify by appending the suffix letter "X" to the ordering code.

# **Logic Symbols**





#### **Connection Diagram**



#### **Truth Table**

	Inputs		Outputs					
Ē	A <sub>0</sub>	A <sub>1</sub>	$\overline{O}_0$	$\overline{O}_1$	$\overline{O}_2$	$\overline{O}_3$		
Н	Х	Х	Н	Н	Н	Н		
L	L	L	L	Н	Н	Н		
L	Н	L	Н	L	Н	Н		
L	L	Н	Н	Н	L	Н		
L	Н	Н	Н	Н	Н	L		

H = HIGH Voltage Level

L = LOW Voltage Level

X = Immaterial

# **Unit Loading/Fan Out**

Din Names	December 1	U.L.	Input I <sub>IH</sub> /I <sub>IL</sub>	
Pin Names	Description	HIGH/LOW	Output I <sub>OH</sub> /I <sub>OL</sub>	
A <sub>0</sub> , A <sub>1</sub>	Address Inputs	1.0/1.0	20 μA/-0.6 mA	
Ē	Enable Inputs (Active LOW)	1.0/1.0	20 μA/-0.6 mA	
$\overline{O}_0 - \overline{O}_3$	Outputs (Active LOW)	50/33.3	-1 mA/20 mA	

# **Functional Description**

The F139 is a high-speed dual 1-of-4 decoder/demultiplexer. The device has two independent decoders, each of which accepts two binary weighted inputs  $(A_0-A_1)$  and provides four mutually exclusive active LOW Outputs  $(\overline{O}_0-\overline{O}_3)$ . Each decoder has an active LOW enable  $(\overline{E})$ . When  $\overline{E}$  is HIGH all outputs are forced HIGH. The enable can be used as the data input for a 4-output demultiplexer application. Each half of the F139 generates all four minterms of two variables. These four minterms are useful in some applications, replacing multiple gate functions as shown in Figure 1, and thereby reducing the number of packages required in a logic network.

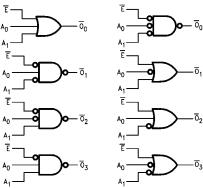
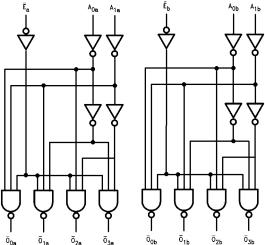


FIGURE 1. Gate Functions (each half)

### **Logic Diagram**



Please note that this diagram is provided only for the understanding of logic operations and should not be used to estimate propagation delays.

# Absolute Maximum Ratings(Note 1)

# Recommended Operating Conditions

 $\begin{array}{ll} \mbox{Storage Temperature} & -65\mbox{°C to } +150\mbox{°C} \\ \mbox{Ambient Temperature under Bias} & -55\mbox{°C to } +125\mbox{°C} \\ \end{array}$ 

Ambient Temperature under Bias  $-55^{\circ}$ C to  $+125^{\circ}$ C Junction Temperature under Bias  $-55^{\circ}$ C to  $+150^{\circ}$ C

 $\begin{array}{lll} \text{V}_{\text{CC}} \text{ Pin Potential to Ground Pin} & -0.5 \text{V to } +7.0 \text{V} \\ \text{Input Voltage (Note 2)} & -0.5 \text{V to } +7.0 \text{V} \\ \text{Input Current (Note 2)} & -30 \text{ mA to } +5.0 \text{ mA} \\ \end{array}$ 

Voltage Applied to Output in HIGH State (with  $V_{CC} = 0V$ )

 $\begin{array}{ll} \text{Standard Output} & -0.5 \text{V to V}_{\text{CC}} \\ \text{3 STATE Output} & -0.5 \text{V to } +5.5 \text{V} \end{array}$ 

Current Applied to Output

 $\label{eq:lower_lower} \begin{array}{ll} \text{in LOW State (Max)} & \text{twice the rated I}_{\text{OL}} \text{ (mA)} \\ \text{ESD Last Passing Voltage (Min)} & 4000 \text{V} \end{array}$ 

Free Air Ambient Temperature  $0^{\circ}$ C to +70°C Supply Voltage +4.5V to +5.5V

 $-0.5 \text{V to V}_{\text{CC}}$ Note 1: Absolute maximum ratings are values beyond which the device may be damaged or have its useful life impaired. Functional operation under these conditions is not implied.

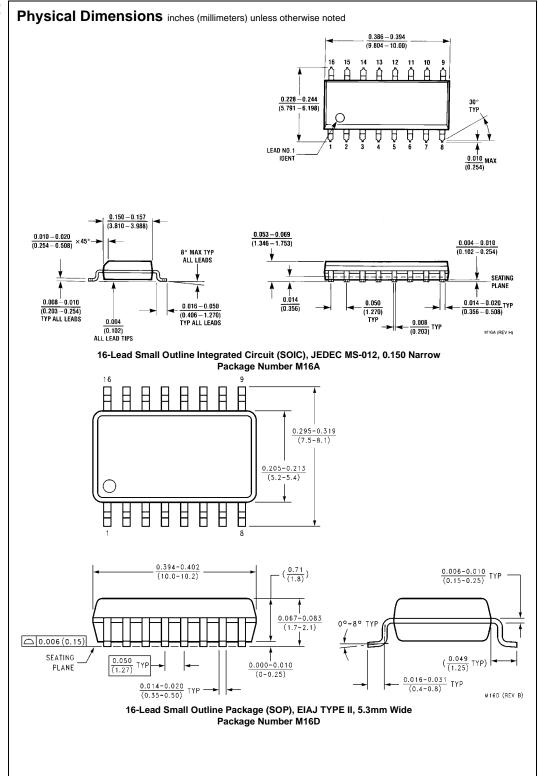
Note 2: Either voltage limit or current limit is sufficient to protect inputs.

### **DC Electrical Characteristics**

Symbol	Parameter	Min	Тур	Max	Units	v <sub>cc</sub>	Conditions
V <sub>IH</sub>	Input HIGH Voltage	2.0			V		Recognized as a HIGH Signal
V <sub>IL</sub>	Input LOW Voltage			0.8	V		Recognized as a LOW Signal
V <sub>CD</sub>	Input Clamp Diode Voltage			-1.2	V	Min	$I_{IN} = -18 \text{ mA}$
V <sub>OH</sub>	Output HIGH Voltage 10% V <sub>CC</sub>	2.5			V	Min	$I_{OH} = -1 \text{ mA}$
	5% V <sub>CC</sub>	2.7			v		$I_{OH} = -1 \text{ mA}$
V <sub>OL</sub>	Output LOW Voltage 10% V <sub>CC</sub>			0.5	V	Min	I <sub>OL</sub> = 20 mA
I <sub>IH</sub>	Input HIGH Current			5.0	μΑ	Max	$V_{IN} = 2.7V$
I <sub>BVI</sub>	Input HIGH Current Breakdown Test			7.0	μΑ	Max	$V_{IN} = 7.0V$
I <sub>CEX</sub>	Output HIGH Leakage Current			50	μΑ	Max	$V_{OUT} = V_{CC}$
V <sub>ID</sub>	Input Leakage Test	4.75			V	0.0	$I_{ID} = 1.9 \mu A$
					v		All Other Pins Grounded
I <sub>OD</sub>	Output Leakage Circuit Current			3.75	μА	0.0	V <sub>IOD</sub> = 150 mV
				3.73	μΑ	0.0	All Other Pins Grounded
I <sub>IL</sub>	Input LOW Current			-0.6	mA	Max	$V_{IN} = 0.5V$
Ios	Output Short-Circuit Current	-60		-150	mA	Max	V <sub>OUT</sub> = 0V
I <sub>CC</sub>	Power Supply Current		13	20	mA	Max	

#### **AC Electrical Characteristics**

Symbol	Parameter	$T_{A} = +25^{\circ}C$ $V_{CC} = +5.0V$ $C_{L} = 50 \text{ pF}$			$T_A = 0$ °C to $+70$ °C $V_{CC} = +5.0V$ $C_L = 50$ pF		Units
		Min	Тур	Max	Min	Max	
t <sub>PLH</sub>	Propagation Delay	3.5	5.3	7.5	3.0	8.5	ns
t <sub>PHL</sub>	$A_0$ or $A_1$ to $\overline{O}_n$	4.0	6.1	8.0	4.0	9.0	
t <sub>PLH</sub>	Propagation Delay	3.5	5.4	7.0	3.5	8.0	
t <sub>PHL</sub>	$\overline{E}_1$ to $\overline{O}_n$	3.0	4.7	6.5	3.0	7.5	ns



#### Physical Dimensions inches (millimeters) unless otherwise noted (Continued) 0.740 - 0.780 0.090 (18.80 - 19.81)(2.286) **16 15 14 13 12 11 10 9** 16 15 INDEX ARFA 0.250 ± 0.010 $\overline{(6.350 \pm 0.254)}$ PIN NO. 1 PIN NO. 1 1 2 3 4 5 6 7 8 1 2 \_ IDENT OPTION 01 OPTION 02 $\frac{0.065}{(1.651)}$ $\frac{0.130 \pm 0.005}{(3.302 \pm 0.127)}$ $\frac{0.060}{(1.524)}$ TYP 4º TYP OPTIONAL 0.300 - 0.320 (7.620 - 8.128)0.145 - 0.200 $\overline{(3.683 - 5.080)}$ 95° ± 5° $\frac{0.008 - 0.016}{(0.203 - 0.406)} \text{ TYP}$ 0.020 $\frac{0.280}{(7.112)}$ (0.508)0.125 - 0.150 (3.175 - 3.810) 0.030 ± 0.015 $(0.762 \pm 0.381)$ 0.014 - 0.023 0.100 ± 0.010 (0.325 +0.040 -0.015 (0.356 - 0.584)0.050 ± 0.010 $(2.540 \pm 0.254)$ N16E (REV F) $(1.270 \pm 0.254)$

16-Lead Plastic Dual-In-Line Package (PDIP), JEDEC MS-001, 0.300 Wide Package Number N16E

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