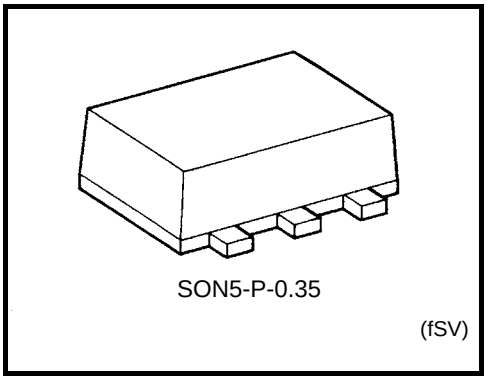
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7SH125FS

Bus Buffer

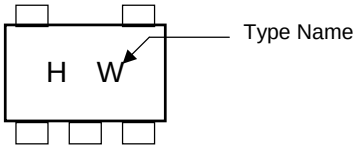


Weight : 0.001 g (Typ.)

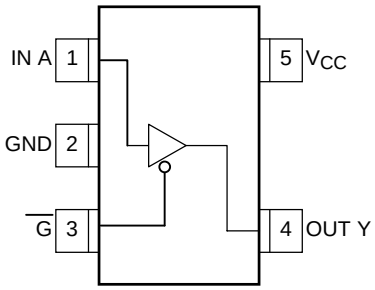
Features

High speed: t_{pd} = 3.8 ns (typ.) at V_{CC} = 5 V
Low power dissipation: I_{CC} = 2 μA (max) at Ta = 25°C
High noise immunity: $V_{NIH} = V_{NIL}$ = 28% V_{CC} (min)
5.5V tolerant input.
Wide operating voltage range: V_{CC} (opr) = 2~5.5 V

Marking



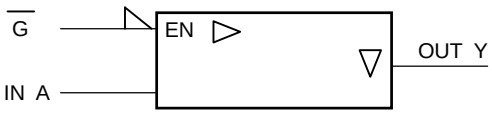
Pin Assignment (top view)



Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating	Unit
Supply voltage range	V_{CC}	−0.5~7.0	V
DC input voltage	V_{IN}	−0.5~7.0	V
DC output voltage	V_{OUT}	−0.5~V_{CC} + 0.5	V
Input diode current	I_{IK}	−20	mA
Output diode current	I_{OK}	±20	mA
DC output current	I_{OUT}	±25	mA
DC V_{CC}/ground current	I_{CC}	±50	mA
Power dissipation	P_D	50	mW
Storage temperature	T_{stg}	−65~150	°C

Logic Diagram



Truth Table

$\overline{G}$	A	Y
H	X	Z
L	L	L
L	H	H

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V_{CC}	2.0~5.5	V
Input voltage	V_{IN}	0~5.5	V
Output voltage	V_{OUT}	0~V_{CC}	V
Operating temperature	T_{opr}	−40~85	°C
Input rise and fall time	dt/dv	0~100 (V_{CC} = 3.3 ± 0.3 V)	ns/V
		0~20 (V_{CC} = 5 ± 0.5 V)	

Electrical Characteristics

DC Characteristics

Characteristics	Symbol	Test Circuit	Test Condition		V_{CC} (V)	Ta = 25°C Min	Ta = 25°C Typ.	Ta = 25°C Max	Ta = −40~85°C Min	Ta = −40~85°C Max	Unit
High-level input voltage	V_{IH}	—	—		2.0	1.5	—	—	1.5	—	V
					3.0~5.5	V_{CC} × 0.7	—	—	V_{CC} × 0.7	—	
Low-level input voltage	V_{IL}	—	—		2.0	—	—	0.50	—	0.50	V
					3.0~5.5	—	—	V_{CC} × 0.3	—	V_{CC} × 0.3	
High-level output voltage	V_{OH}	—	V_{IN} = V_{IH} or VIL	I_{OH} = −50 μA	2.0	1.9	2.0	—	1.9	—	V
					3.0	2.9	3.0	—	2.9	—	
					4.5	4.4	4.5	—	4.4	—	
				I_{OH} = −4 mA	3.0	2.58	—	—	2.48	—	
				I_{OH} = −8 mA	4.5	3.94	—	—	3.80	—	
Low-level output voltage	V_{OL}	—	V_{IN} = V_{IH}	I_{OL} = 50 μA	2.0	—	0	0.1	—	0.1	V
					3.0	—	0	0.1	—	0.1	
					4.5	—	0	0.1	—	0.1	
				I_{OL} = 4 mA	3.0	—	—	0.36	—	0.44	
				I_{OL} = 8 mA	4.5	—	—	0.36	—	0.44	
3-state output off-state current	I_{OZ}	—	V_{IN} = V_{IH} or V_{IL} V_{OUT} = V_{CC} or GND		5.5	—	—	±0.25	—	±2.5	μA
Input leakage current	I_{IN}	—	V_{IN} = 5.5 V or GND		0~5.5	—	—	±0.1	—	±1.0	μA
Quiescent supply current	I_{CC}	—	V_{IN} = V_{CC} or GND		5.5	—	—	2.0	—	20.0	μA

AC Characteristics (Input: $t_r = t_f = 3$ ns)

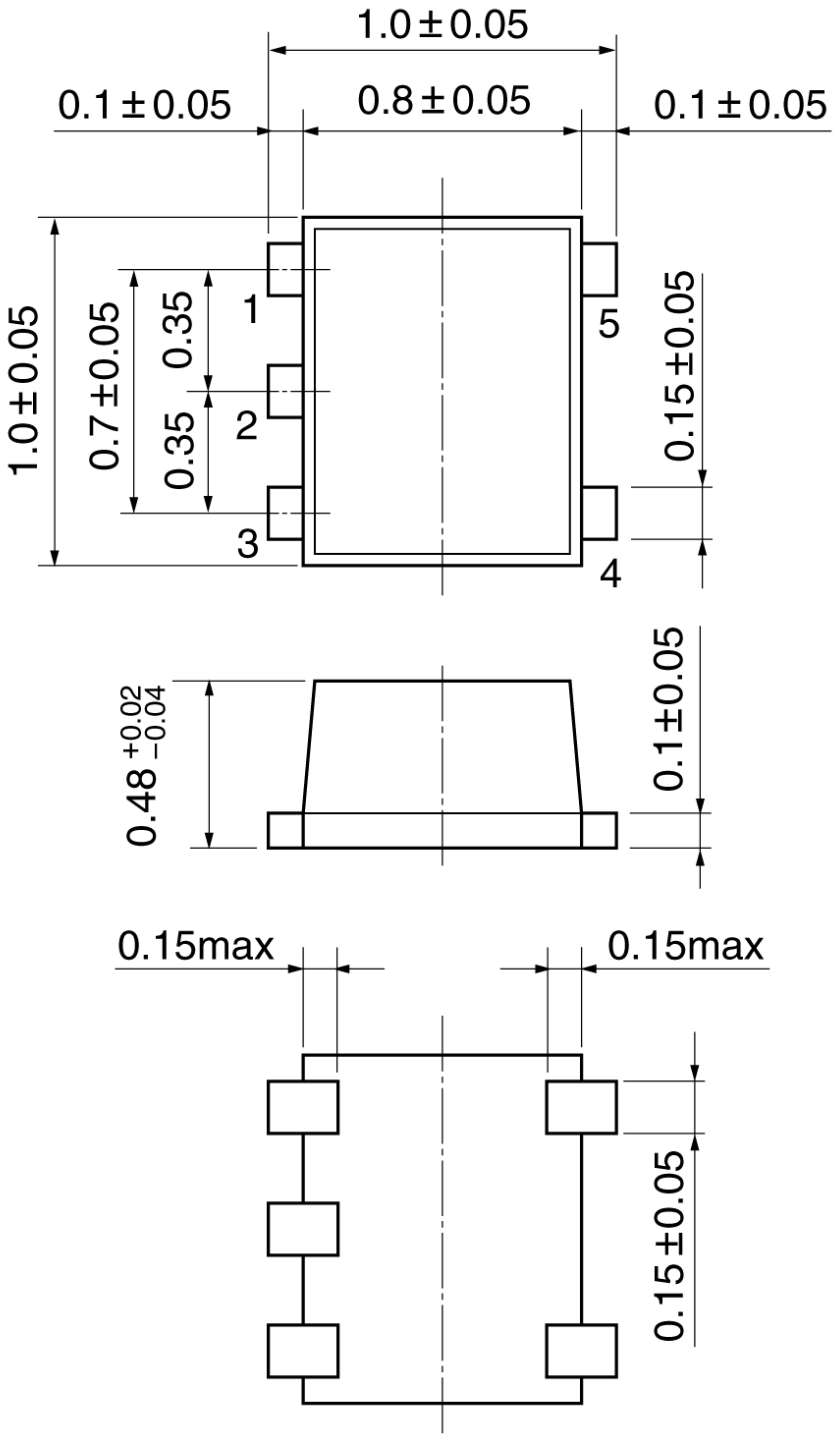
Characteristics	Symbol	Test Circuit	Test Condition		Ta = 25°C			Ta = −40~85°C		Unit
			V_{CC} (V)	C_L (pF)	Min	Typ.	Max	Min	Max	
Propagation delay time	t_{pLH} t_{pHL}	—	—	3.3 ± 0.3	15	—	5.6	8.0	1.0	9.5	ns
					50	—	8.1	11.5	1.0	13.0	
				5.0 ± 0.5	15	—	3.8	5.5	1.0	6.5	
					50	—	5.3	7.5	1.0	8.5	
3-state output enable time	t_{pZL} t_{pZH}	—	—	3.3 ± 0.3	15	—	5.4	8.0	1.0	9.5	ns
					50	—	7.9	11.5	1.0	13.0	
				5.0 ± 0.5	15	—	3.6	5.1	1.0	6.0	
					50	—	5.1	7.1	1.0	8.0	
3-state output disable time	t_{pLZ} t_{pHZ}	—	—	3.3 ± 0.3	50	—	9.5	13.2	1.0	15.0	ns
				5.0 ± 0.5	50	—	6.1	8.8	1.0	10.0	
Input capacitance	C_{IN}	—	—			—	4	10	—	10	pF
Output capacitance	C_{OUT}	—	—			—	6	—	—	—	pF
Power dissipation capacitance	C_{PD}	—	(Note)			—	14	—	—	—	pF

Note: C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation:

$$I_{CC\ (opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

Package Dimensions



Weight: 0.001 g (typ.)

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