### TOSHIBA Photocoupler GaAs IRED + Photo-Triac

# **TLP260J**

Triac Drivers
Programmable Controllers
AC-Output Modules
Solid-State Relays

The TOSHIBA mini-flat coupler TLP260J is a small-outline coupler suitable for surface mount assembly.

The TLP260J consists of a photo-triac optically coupled to a gallium arsenide infrared-emitting diode.

Peak off-state voltage : 600 V (min)
 Trigger LED current : 10 mA (max)
 On-state current : 70 mA (max)
 Isolation voltage : 3000 Vrms (min)

• UL-recognized : UL1577, file No. E67349

• Option (V4) type

VDE-approved :EIN EN 60747-5-2 satisfied

Maximum operating insulation voltage :565 VpK
Highest permissible overvoltage :6000 Vpk

Note: When an EN 60747-5-2 approved type is needed, be sure to specify "Option (V4)".

• Construction Mechanical Rating

Creepage distance : 4.0 mm (min)
Clearance : 4.0 mm (min)
Insulation thickness : 0.4 mm (min)

# Unit: mm 6 4 7.0 ± 0.4 1.3 3.6 ± 0.2 2.54 TOSHIBA 11–4C1

Weight: 0.09 g

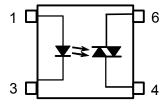
### **Trigger LED Current**

Classification*	Trigger LED C $V_T = 6 V, T$	Product Classification	
	Min	Max	Marking
Standard	_	10	Blank

Note: Be sure to use standard product type names when submitting type names for safety certification testing, i.e., TLP260J.

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### **Pin Configuration**



- 1. Anode
- 3. Cathode
- 4. Terminal 1
- 6. Terminal 2

### Absolute Maximum Ratings (Ta = 25°C)

Characteristic			Symbol	Rating	Unit
	Forward current	ΙF	50	mA	
	Forward current derating (T	ΔI <sub>F</sub> / °C	-0.7	mA / °C	
LED	Peak forward current (100 µ	us pulse, 100 pps)	IFP	1	Α
	Reverse voltage	V <sub>R</sub> 5		V	
	Junction temperature		Tj	125	°C
	Off-state output terminal vo	$V_{DRM}$	600	V	
	On-state RMS current	Ta = 25°C	I	70	mA
		Ta = 70°C	I <sub>T(RMS)</sub>	40	IIIA
Detector	On-state current derating (	ΔI <sub>T</sub> / °C	-0.67	mA / °C	
Dete	Peak on-state current (100 µs pulse, 120		I <sub>TP</sub>	2	Α
	Peak nonrepetitive surge ct (PW = 10 ms, DC = 10%)	I <sub>TSM</sub>	1.2	Α	
	Junction temperature		Tj	100	°C
Storage temperature range			T <sub>stg</sub>	-55~125	°C
Operating temperature range			T <sub>opr</sub>	-40~100	°C
Lead soldering temperature (10 s)			T <sub>sol</sub>	260	°C
Isolatio	Isolation voltage (AC, 1 min., R.H. ≤ 60%) (Note 1)			2500	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Note 1: Device considered as a two-terminal device: Pins 1 and 3 shorted together and pins 4 and 6 shorted together.

### **Recommended Operating Conditions**

Characteristic	Symbol	Min	Тур.	Max	Unit
Supply voltage	$V_{AC}$	_	_	240	Vac
Forward current	lF	15	20	25	mA
Peak on-state current	I <sub>TP</sub>	_	_	1	Α
Operating temperature	T <sub>opr</sub>	-25	_	85	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.

## Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
	Forward voltage	V <sub>F</sub>	I <sub>F</sub> = 10 mA	1.0	1.15	1.3	V
LED	Reverse current	I <sub>R</sub>	V <sub>R</sub> = 5 V	_	_	10	μΑ
	Capacitance	C <sub>T</sub>	V = 0, f = 1 MHz	_	30	_	pF
Detector	Peak off-state current	I <sub>DRM</sub>	V <sub>DRM</sub> = 600 V	_	10	1000	nA
	Peak on-state voltage	$V_{TM}$	I <sub>TM</sub> = 70 mA	_	1.7	2.8	٧
	Holding current	lн	_	_	1.0	_	mA
	Critical rate of rise of off–state voltage	dv / dt	V <sub>in</sub> = 240 Vrms, Ta = 85°C (Fig. 1)	_	500	_	V / µs
	Critical rate of rise of commutating voltage	dv / dt(c)	I <sub>T</sub> = 15 mA, V <sub>in</sub> = 60 Vrms (Fig. 1)	_	0.2	_	V / μs

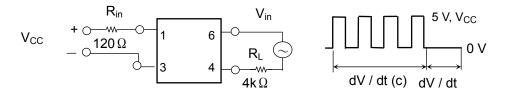
# **Coupled Electrical Characteristics (Ta = 25°C)**

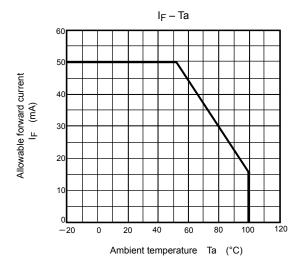
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Trigger LED current	I <sub>FT</sub>	V <sub>T</sub> = 6 V	_	5	10	mA
Turn-on time	t <sub>ON</sub>	$V_D = 6 \rightarrow 4 \text{ V}, R_L = 100\Omega$ $I_F = \text{rated } I_{FT} \times 1.5$	_	30	100	μs

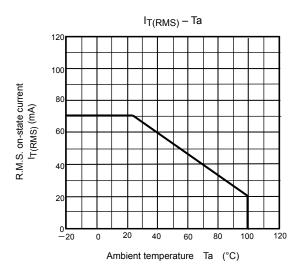
# Isolation Characteristics (Ta = 25°C)

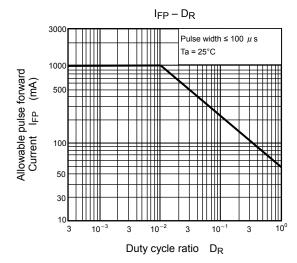
Characteristic	Symbol	Test Condition	Min	Тур.	Max	Unit
Capacitance input to output	CS	V <sub>S</sub> = 0, f = 1 MHz	_	0.8	_	pF
Isolation resistance	R <sub>S</sub>	V <sub>S</sub> = 500 V, R.H. ≤ 60%	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω
Isolation voltage	BVS	AC, 1 minute	3000	_	_	Vrms
		AC, 1 second, in oil	_	5000	_	VIIIIS
		DC, 1 minute, in oil	_	5000	_	Vdc

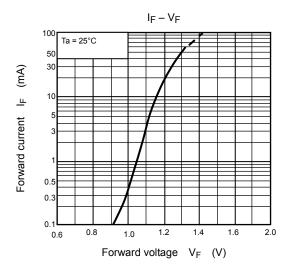
Fig. 1: dv / dt test circuit

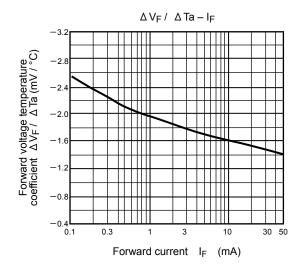


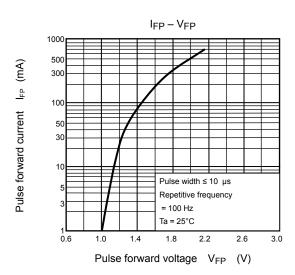




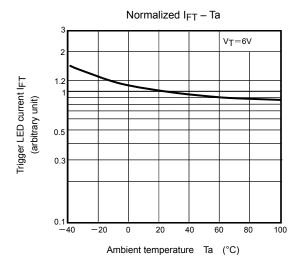


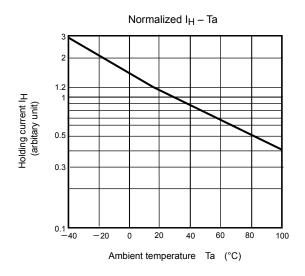


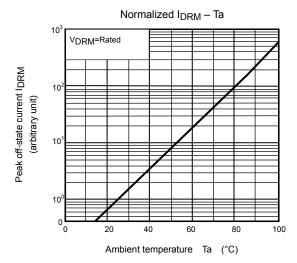


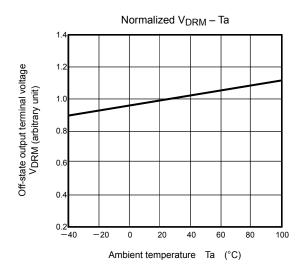


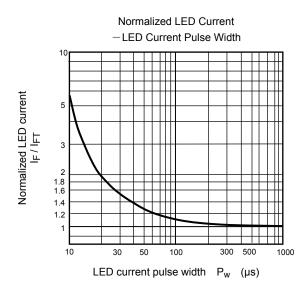
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  In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc.
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