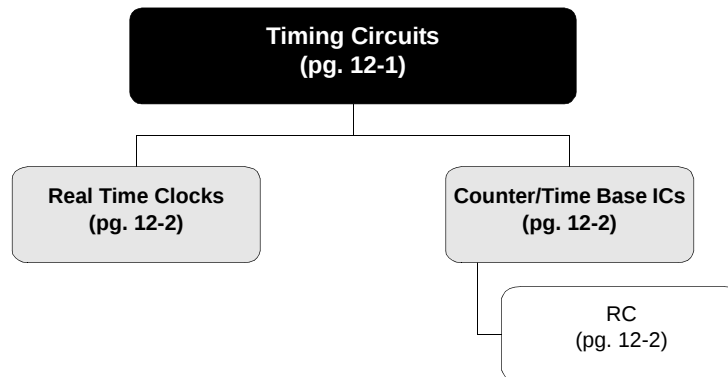




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Real Time Clocks

Device	V _{CC} (V)	V _{BAT} (V)	Alarms	CPU Supervisory		Battery		V _{TRIP} for POR	Other Fct	$\overline{\text{IRQ}}$ Output	Frequency Output	Oscillator Compensation (ppm)	Intg. Memory	Package
				Fct POR	Fct Watch dog Timer	Saver	Backup							
X1205	2.7 to 5.5	1.8 to 5.5	2				Y			$\overline{\text{IRQ}}/\text{PHZ}$	$\overline{\text{IRQ}}/\text{PHZ}$	-64 to +110	0	8 Ld SOIC, 8 Ld TSSOP
ISL1208	2.7 to 5.5	1.8 to 5.5	1			Y	Y			$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	-94 to +140	2 Bytes SRAM	8 Ld MSOP, 8 Ld SOIC
ISL1209	2.7 to 5.5	1.8 to 5.5	1			Y	Y		$\overline{\text{EVDET}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	-94 to +140	2 Bytes SRAM	10 Ld MSOP
ISL1218	2.7 to 5.5	1.8 to 5.5	1			Y	Y			$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	-94 to +140	8 Bytes SRAM	8 Ld MSOP, 8 Ld SOIC
ISL1219	2.7 to 5.5	1.8 to 5.5	1			Y	Y		$\overline{\text{EVDET}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	-94 to +140	2 Bytes SRAM	10 Ld MSOP
ISL1220	2.7 to 5.5	1.8 to 5.5	1			Y	Y			$\overline{\text{IRQ}}$	F _{OUT}	-94 to +140	8 Bytes SRAM	10 Ld MSOP
ISL1221	2.7 to 5.5	1.8 to 5.5	1			Y	Y		$\overline{\text{IRQ}}/\text{EVDET}$	$\overline{\text{IRQ}}/\text{EVDET}$	F _{OUT}	-94 to +140	2 Bytes SRAM	10 Ld MSOP
X1226	2.7 to 5.5	1.8 to 5.5	2				Y			$\overline{\text{IRQ}}/\text{PHZ}$	$\overline{\text{IRQ}}/\text{PHZ}$	-64 to +110	512 Bytes E ²	8 Ld SOIC, 8 Ld TSSOP
ISL12024	2.7 to 5.5	1.8 to 5.5	2				Y		64-bit Unique Id	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	-64 to +110	512 Bytes E ²	8 Ld SOIC, 8 Ld TSSOP
ISL12025	2.7 to 5.5	1.8 to 5.5	2	Y	Y		Y	5 Select V _{TRIP} Levels from 2.63V to 4.64V	64-bit Unique Id	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	-64 to +110	512 Bytes E ²	8 Ld SOIC, 8 Ld TSSOP
ISL12026	2.7 to 5.5	1.8 to 5.5	2				Y			$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	-64 to +110	512 Bytes E ²	8 Ld SOIC, 8 Ld TSSOP
ISL12027	2.7 to 5.5	1.8 to 5.5	2	Y	Y		Y	5 Select V _{TRIP} Levels from 2.63V to 4.64V				-64 to +110	512 Bytes E ²	8 Ld SOIC, 8 Ld TSSOP
ISL12028	2.7 to 5.5	1.8 to 5.5	2	Y	Y		Y	5 Select V _{TRIP} Levels from 2.63V to 4.64V		$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	-64 to +110	512 Bytes E ²	14 Ld SOIC, 14 Ld TSSOP
ISL12029	2.7 to 5.5	1.8 to 5.5	2	Y	Y		Y	5 Select V _{TRIP} Levels from 2.63V to 4.64V		$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	$\overline{\text{IRQ}}/\text{F}_{\text{OUT}}$	-64 to +110	512 Bytes E ²	14 Ld SOIC, 14 Ld TSSOP
X1286	2.7 to 5.5	1.8 to 5.5	2				Y	4 Factory Programmable V _{TRIP} Levels from 2.65V to 4.63V		$\overline{\text{IRQ}}/\text{PHZ}$	$\overline{\text{IRQ}}/\text{PHZ}$	-64 to +110	32k Bytes E ²	14 Ld TSSOP, 8 Ld EIAJ SOIC
X1288	2.7 to 5.5	1.8 to 5.5	2	Y	Y		Y	4 Factory Programmable V _{TRIP} Levels from 2.65V to 4.63V		$\overline{\text{IRQ}}/\text{PHZ}$	$\overline{\text{IRQ}}/\text{PHZ}$	-64 to +110	32k Bytes E ²	14 Ld TSSOP, 16 Ld SOIC

Counter/Time Base ICs RC

Device	Oscillator Type	Freq. Range (Hz)	ICC (μA)	VCC Range ($\pm\text{V}$)	Package
ICM7242	RC	0.1 to 13M	220	2 to 16	8 Ld PDIP
ICM7555	RC	0.1 to 1M	40	2 to 18	8 Ld PDIP, 8 Ld SOIC
ICM7556	RC	0.1 to 1M	80	2 to 18	14 Ld CerDIP, 14 Ld PDIP