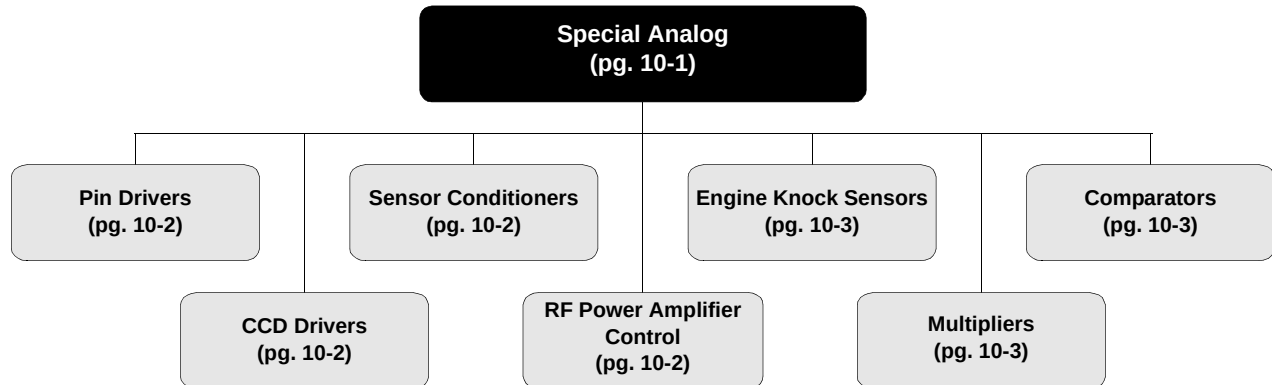




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Pin Drivers

Device	# of Channels	Max Operating Frequency (MHz)	Peak Output I_{PK} (A)	Rise Time t_R (ns)	Fall Time t_F (ns)	Turn Off Delay t_D (ns)	I_Q (mA)	On (Ω)	Input Signal Range (V)	Input Supply Range (V_P)	Max Input Signal (V)	Output Signal Range (V)	Max Output Signal (V)	Package
EL7154	1	10	4	20	20	20	2.5	1.5	0 to V_P	+4.5 to +16	16	-3 to 15.5	15.5	8 Ld PDIP, 8 Ld SOIC
EL7155	1	40	3.5	14.5	15	9.5	3	2	0 to V_P	+4.5 to +16	16	-5 to 16	16	8 Ld PDIP, 8 Ld SOIC
EL7156	1	40	3.5	14.5	15	9.5	3	2	0 to V_P	+4.5 to +16	16	-5 to +16	16	8 Ld PDIP, 8 Ld SOIC
EL7158	1	40	12	8	8	22	3	0.5	0 to V_P	+4.5 to +18	18	-5 to +12	12	8 Ld SOIC
ISL55100A	4	65	1	2.5	2.5	18	70	5	$-V_P$ to $+V_P$	-18 to +18	18	$-V_P$ to $+V_P$	18	72 Ld QFN
ISL55100B	4	50	1	3	3	18	70	9	$-V_P$ to $+V_P$	-18 to +18	18	$-V_P$ to $+V_P$	18	72 Ld QFN
ISL55110	2	100	3.5	1.5	1.5	20-QFN Only	3.5	3	0 to V_P	+2.7 to +5.5	5.5	0 to +13.2	13.2	16 Ld QFN, 8 Ld TSSOP
ISL55111	2	100	3.5	1.5	1.5	20-QFN Only	3.5	3	0 to V_P	+2.7 to +5.5	5.5	0 to +13.2	13.2	16 Ld QFN, 8 Ld TSSOP

CCD Drivers

Device	# of Drivers	Max Operating Frequency (MHz)	Peak Output I_{PK} (A)	Rise Time t_R (ns)	Fall Time t_F (ns)	Turn Off Delay t_D (ns)	I_Q (mA)	On (Ω)	Input Signal Range (V)	Input Supply Range (V_P)	Max Input Signal (V)	Output Signal Range (V)	Max Output Signal (V)	Package
EL7243	2	20	2	20	20	20	1	4	0 to V_P	+5 to +16.5	16.5	0 to +16.5	16.5	10 Ld SOIC
EL7457	1	40	2	11	1	12	0.8	4.2	$-V_P$ to $-V_P$	+4.5 to +16	16	4.5 to +16	16	16 Ld QFN, 16 Ld QSOP, 16 Ld SOIC
ISL55110	2	100	3.5	1.5	1.5	20-QFN Only	3.5	3	0 to V_P	+2.7 to +5.5	+5.5	0 to +13.2	13.2	16 Ld QFN, 8 Ld TSSOP
ISL55111	2	100	3.5	1.5	1.5	20-QFN Only	3.5	3	0 to V_P	+2.7 to +5.5	+5.5	0 to +13.2	13.2	16 Ld QFN, 8 Ld TSSOP

Sensor Conditioners

Device	Number of DCPs	Number of Taps	Memory Type	Bus Interface Type	V_{CC} Range (V)	DCP Differential Terminal Voltage (V)	Single Supply (V_{CC} , GND)	Resistance Taper	Wiper Current (I _W) (mA)	Standby Current I _{sb} (μ A)	Package
X96010	Dual	256	Non-Volatile	2-Wire	3.0 to 5.5	0 to +5.5	Y	Linear	± 4	200	14 Ld TSSOP
X96011	Single	256	Non-Volatile	2-Wire	3.0 to 5.5	0 to +5.5	Y	Linear	± 4	200	14 Ld TSSOP
X96012	Dual	256	Non-Volatile	2-Wire	3.0 to 5.5	0 to +5.5	Y	Linear	± 4	200	14 Ld TSSOP

RF Power Amplifier Control

Device	26V (Typical) Instrumentation Anti-Dither (Drain Sense) Amplifier	IDQ Sense and Tracking	8-Bit (256-Tap) Gate Voltage Adjust Output	Gate Voltage Output Buffer	V_{REF} Input	Non-volatile Storage of Gate Voltage Setting	Input for Temperature Compensation Circuits	Optional Automatic/Periodic Control for Drain Current Sense to Gate Voltage Adjustment	Power On Recall Circuits	Bus Interface	Package
X9470	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	Yes	2-Wire	24 Ld TSSOP

Engine Knock Sensors

Device	Description	Package
HIP9011	Engine Knock Signal Processor	20 Ld SOIC

Multipliers

Device	BW (MHz)	SR (V/ μ s)	Key Spec.	I_{CC} (mA)	V_{CC} Range ($\pm$ V)	Package
HA-2556	57	450	Mult Error = 1.5%	18	5 to 15	16 Ld CerDIP, 16 Ld SOIC

Comparators

Device	# of Channels	Response Time (ns)	I_{CC} (mA)	Input Supply Range (Vp)	Max Input Signal (V)	Comparator Receiver Range (V)	Output Signal Range (V)	Max Output Signal (V)	I_{BIAS} (nA)	V_{OFFSET} (mV)	I_{OFFSET} (nA)	Package
ISL55141	1	10	7	-18 to +18	18	$-V_P$ to $+V_P - 5$	$-V_P$ to $+V_P$	18	10	50	10	16 Ld QFN, 14 Ld TSSOP
ISL55142	2	10	7	-18 to +18	18	$-V_P$ to $+V_P - 5$	$-V_P$ to $+V_P$	18	10	50	10	20 Ld QFN, 20 Ld TSSOP
ISL55143	4	10	7	-18 to +18	18	$-V_P$ to $+V_P - 5$	$-V_P$ to $+V_P$	18	10	50	10	36 Ld QFN

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