Changes for the Better



Meets User Needs for Driving Power MOSFET/IGBT HVIC series



HVIC series

Mitsubishi has utilized its excellent advanced high-voltage process technology and drive protection circuit technology to accurately transmit the microcomputer control signal to the power MOSFET/IGBT with high speed and high reliability without a photo-coupler.

Application fields





High



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S.S. Integrate		

HVIC technology

HVIC technology

Technology of high voltage devices integration

HVIC include junction isolated 600V devices and 5/15V devices

- MFFP (Multiple Floating Field Plate) structure : It is composed of one poly-silicon layer and one aluminum layer , and it is a new electric field relaxation technique.
- Double buried layer structure : High voltage isolation structure with N⁺/N⁻ double buried layer stabilizes breakdown voltage because avalanche position shifts surface of the n- epitaxial layer to the substrate.
- COMS transistor with buried layer latch-up toughness is improved.



Control circuit technology for analog/digital signal

HVIC is formed high/low side driver, 600V level-shift and under voltage protection. More over the oscillator is embedded in the HVIC. MCU is able to control MOSFET or IGBT by using HVIC (half bridge driver or full bridge driver) without photo

coupler.

Half bridge driver





Example half-bridge driver circuit configuration



3-phase bridge driver



Example 3-phase bridge driver circuit configuration



How to use

Floating power supply method

The source voltage of high side MOSFET shifts ground level to HV level.

Therefore in order to drive high side MOSFET, the power supply of high side driver needs one Vcc up to source voltage of high side MOSFET.

One solution is floating power supply method.

Typical connection of floating power supply method is shown as follow.

■ High side driver flooting power supply method



2 Boot strap circuit method & basic operation

Another solution is bootstrap circuit method. This method is the use of low side power supply, bootstrap diode D1, resister R1 and bootstrap capacitor C1.

C1 is charged through R1, D1 from Vcc. Circuit diagram are shown as follow.



3 Current path of charging and discharging during HVIC stationary operation.

Current path of charging and discharging during HVIC stationary operation are shown as follow.

■ Current path of charging and discharging



Setting example of boot strap capacitor value

(Initial charged voltage of boot strap capacitor)

At first low side MOSFET is switched ON mode. Boot strap capacitor is charged by this.

Charging current ID is given by

ID= $(Vcc/R1)e^{-t/(R1 \cdot C1)}$ Initial condition t=0

ID=Vcc/R1

When charged voltage VC1 of boot strap capacitor C1 can be expressed as (1) VC1=Vcc-VF-VDS ··· (1)

VF : Forward voltage of diode D1 VDS: drain-source voltage of low side MOSFET

Simplified calculation of boot strap capacitor value

Boot strap capacitor value can be expressed as (2) C1=IBS×T1/ Δ V+ (Margin : 2 \sim 3 times of IBS×T1/ Δ V) \cdots (2)

T1 : maximum time of high side MOSFET is ON (or maximum time of high side MOSFET and low side MOSFET are OFF) IBS: High side Consumption current (consider Temperature characteristic and Frequency characteristic) △V : maximum voltage when C1 discharges electricity

C1 is calculated by (1) (2), expression This setting example is only calculation, so you should design with investigation of your actual set.



Application examples

AAAAAA

Application examples





Example PDP drive configuration



Quick referrence

Type name	Rating voltage (V)	Output current (A)	Drive type	Input signal	Dead-time control	Remarks	Pakage	
(pb free)	600	+0.12/ -0.25	Half bridge drive	2		with Interlock	*** 8P2S ①	
M81707FP (pb free)		0.1	Dual high side drive	1×2	Input	—		
M81708FP (pb free)		600	+0.12/ -0.25	Half bridge	2	signal		16P2N ②
M81709FP (pb free)		2	drive			WITH INTERIOCK		
M81711FP (pb free)	24	0.5	Dual low			Low active		
M81716FP (pb free)		0.5	side drive	1×2		High active		
M81713FP (pb free)	600	0.5	Holf bridge	1	Internal	_	8P2S ①	
M81719FP * (pb free)		+0.12/ -0.25	drive	2		with Input filter		
M81712FP ★ (pb free)		+0.2/ -0.35	3ø Bridge drive	6			28X9R ③	
M81019FP (pb free)	1200 600	4			Input Signal	with Interlock		
M81721FP (pb free)		I	Half bridge drive	2		for DIP-CIB	24P2Q ④	
M81722FP ★ (pb free)		3					*** 8P2S ①	
M81723FP ★ (pb free)	300	0.1	Dual high side drive	1×2		_	16P2N ②	
M81725FP ★ (pb free)	600	3	High side drive	1	_		% 8P2S ①	
M63958P/FP ★ (pb free)		+0.5/ -0.25	Half bridge drive	_	Internal	for Fluorescent lamp	16P4(5)/16P2N(2)	

★: Under development

Pakage outline

Quick referrence

2 TYPE 16P2N 16pin 300mil SOP



(4) TYPE 24P2Q 24pin 300mil SSOP



