

Package : VCSP50L2

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1. Structure and materials

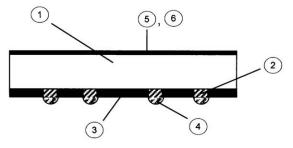


Fig. 1 Structure

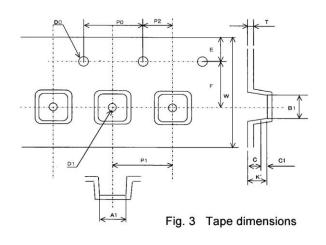
2. Tape and Reel information

2. 1. Packing specification

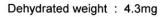
and the second state of th	
Таре	Embossed carrier tape
Quantity	3,000pcs/Reel
Direction of feed	E2 (See Fig. 2)

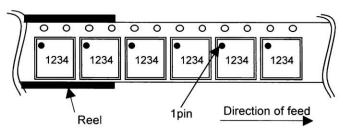
2. 2. Tape and Reel specification

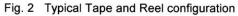
2. 2. 1. Tape and reel dimensions (See the table on page 2/4)

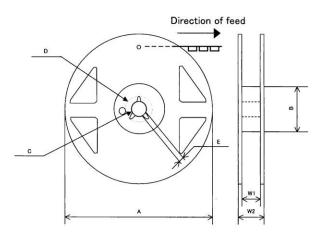


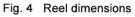
No.	ltem	Materials
1	Die	Silicon
2	Cu Post	Cu
3	Mold Compound	Epoxy Resin
4	Ext. terminal	Sn-3Ag-0.5Cu Solder
5	Mold Compound	Polyamide-imide Resin
6	Marking	Laser Marking











A1	B1	С	C1	D0	D1	E	F	Κ'	P0	P1	P2	Т	W
2.14	2.14	(0.6)	(0.25)	φ1.5	ф0.5	1.75	3.5	0.85	4.0	4.0	2.0	0.3	8.0
±0.1	±0.1			+0.1 -0	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.1	±0.05	±0.3

No component pockets are 100 pockets(400mm) or more.

2. 3. 2. Trailer

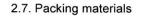
No component pockets are 40 pockets(160mm) or more. Tape is free from reel.

Out going inspection stamp (Only unit box) 2. 4. Label for Reel and Box Product No. BD88410GUL-E2 Quantity 3,000pcs. 0124 A5110F Marking lot number Lot number-K.YAMADA Internal product code-BD88410GUL-E2 MNo. 124 02 pcs. Pb Free Mark MNo .124 024 Free pcs ROHM SEMICONDUCTOR MADE IN JAPAN Fig. 5 Label example 2. 5. Packing style Label 4 reels or less per inner 182

Label

2. 6. Shipping style

Reel



Inner box

183

Material

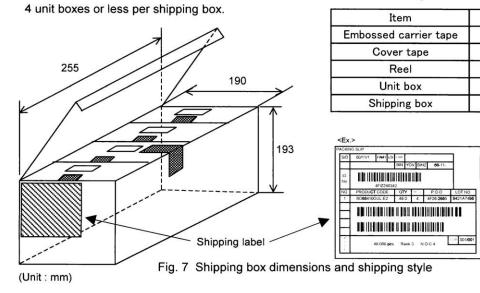
PS

PET + PE

PS

Cardboard

Cardboard



Joint tape

Fig. 6 Packing style

<Shipping label> 1. Product code

- 2. Q' TY
- 3. N.O.C
- 4. LOT No.

57

(Unit : mm)

Antistatic

Yes

Yes

Yes

None

None

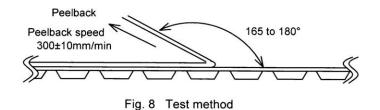
aal dimensions)

Α	В	С	D	Е	W1	W2
φ180 +0 -1.5	60 MIN	φ13.0 ±0.2	φ20.2 MIN	1.5 MIN	9.0 +1.0 -0	11.4 ±1.0

2.8. Others

2. 8. 1. Peelback strength

Cover tape peelback strength is 0.2 to 0.7N.



2. 8. 2. Missing ICs

(1) No consecutive dropouts.

(2) A maximum 0.1% of specified number of products in each packing may be missing.

3. Storage conditions

3. 1. Storage environment

Recommended storage conditions are as follows :

-Temperature : 5 to 30°C

: 40 to 70% RH

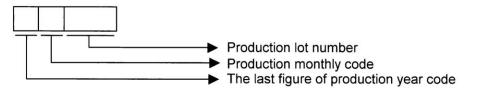
-Humidity 3. 2. Storage period

-Specified storage period : 1 year

3. 3. Specified storage period until soldering

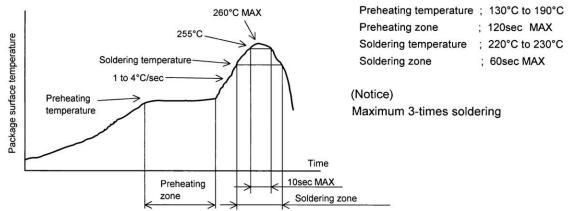
This package dose not require additional drying treatment as long as the moisture condition at the mounting process is within our recommended mounting condition.

4. Marking lot number



5. Soldering conditions

5. 1. Recommended temperature profile for reflow

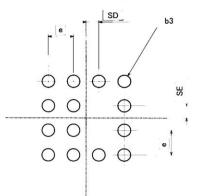


5. 2. About mounting with Sn-Pb solder paste.

Mounting with Sn-Pb solder paste is not recommended because it has a possibility of reducing reliability to connect with Sn-3.0Ag-0.5Cu solder balls.

- 5. 3. The wave soldering method is not supported.
- 5. 4. Partial heat supply method (by soldering iron) is not supported.

6. Footprint dimensions (Optimize footprint dimensions to the board design and soldering condition)

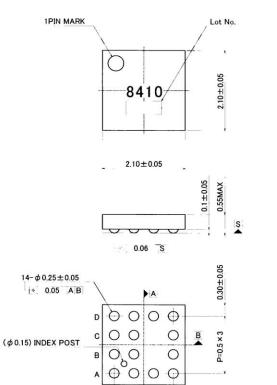


Symbol	Reference Value
е	0.50
b3	Ф0.25
SE	0.25
SE	0.25

7. External dimensions

(Unit : mm)

(Unit : mm)



8. Precautions

8. 1. Caution for handling

Silicon substrate surface is exposing to the side of this package.

Therefore, please pay careful attention to chip and crack, and handle without touching the side of package.

P=0.5X3

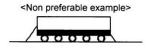
8. 2. Regarding the underfill material

In some case, the underfill material is applied in order to reinforce the solder junction of package. Since there is a case that solder joint reliability may deteriorate according to the resin material or coating condition, please evaluate it sufficiently for its application. In term of the coating condition, it is proforable that there is an anough material bound the case four sides of peokage.

it is preferable that there is an enough material beyond the each four sides of package.

 0.30 ± 0.05

<Preferable example> 0000



(There is a Underfill resin evenly at each four sides.)

(There is little Underfill resin at one or two sides.)

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