

500mA / 12V Low V_{CE} (sat) Digital transistors (with built-in resistors)

DTD523YE / DTD523YM

Applications

Inverter, Interface, Driver

Feature

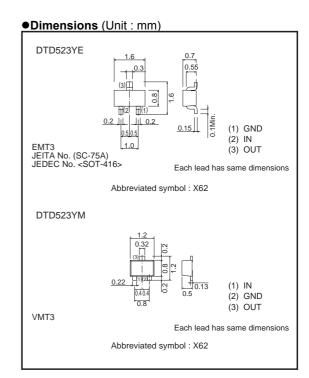
- 1) VCE (sat) is lower than conventional products.
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 4) Only the on / off conditions need to be set for operation, making the device design easy.

Structure

NPN epitaxial plannar silicon transistor (Resistor built-in type)

Packaging specifications

	Package	EMT3	VMT3
	Packaging type	Taping	Taping
	Code	TL	T2L
Part No.	Basic ordering unit (pieces)	3000	8000
DTD523YE		0	_
DTD523YM		-	0

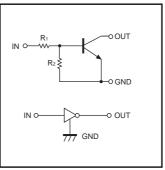


●Absolute maximum ratings (Ta=25°C

Parameter	Symbol	Limits	Unit	
	Symbol	DTD523YE DTD523YM	Offic	
Supply voltage	Vcc	12	V	
Input voltage	Vin	−5 to +12	V	
Collector current *1	IC (max)	500	mA	
Power dissipation *2	Po	150	mW	
Junction temperature	Tj	150	ొ	
Storage temperature	Tstg	-55 to +150	ဗ	

^{*1} Characteristics of built-in transistor.

●Inner circuit



 $R_1=2.2k\Omega / R_2=10k\Omega$

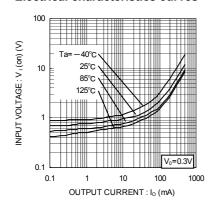
^{*2} Each terminal mounted on a recommended land

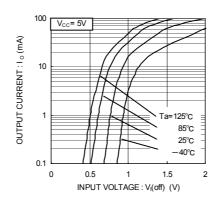
●Electrical characteristics (Ta=25°C)

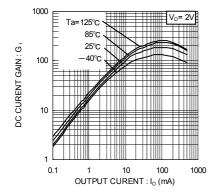
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Input voltage	VI(off)	_	_	0.3	V	Vcc=5V, Io=100μA
	VI(on)	2.5	_	-		Vo=0.3V, Io=20mA
Output voltage	Vo(on)	_	60	300	mV	lo/l:=100mA / 5mA
Input current	lı	_	_	3.0	mA	V _I = 5V
Output current	IO(off)	_	_	500	nA	Vcc=12V, Vi=0V
DC current gain	Gı	140	_	_	_	Vo=2V, Io=100mA
Transition frequency *	f⊤	_	260	_	MHz	Vc=10V, I=-5mA, f=100MHz
Input resistance	R ₁	1.54	2.2	2.86	kΩ	_
Resistance ratio	R2/R1	3.6	4.5	5.5	_	_

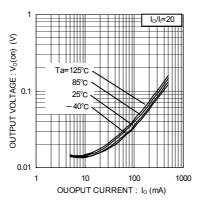
^{*} Characteristics of built-in transistor.

•Electrical characteristics curves









Notes

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