

DTA024E series

PNP -100mA -50V Digital Transistors (Bias Resistor Built-in Transistors) Datasheet

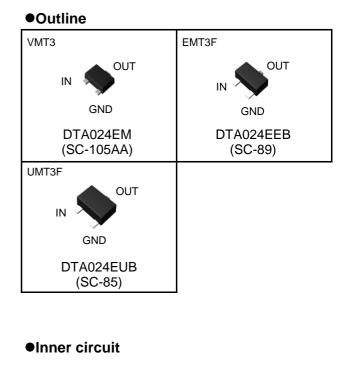
Parameter	Value
V _{CC}	-50V
I _{C(MAX.)}	-100mA
R ₁	22k Ω
R ₂	22k Ω

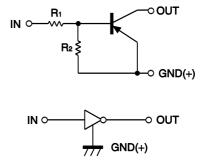
Features

- 1) Built-In Biasing Resistors, $R_1 = R_2 = 22k\Omega$.
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see inner circuit).
- The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of completely eliminating parasitic effects.
- 4) Only the on/off conditions need to be set for operation, making the circuit design easy.
- 5) Complementary NPN Types :DTC024E series
- 6) Lead Free/RoHS Compliant.

Application

Switching circuit, Inverter circuit, Interface circuit, Driver circuit





Part No.	Package	Package size (mm)	Taping code	Reel size (mm)	Tape width (mm)	Basic ordering unit (pcs)	Marking
DTA024EM	VMT3	1212	T2L	180	8	8,000	38
DTA024EEB	EMT3F	1616	TL	180	8	3,000	38
DTA024EUB	UMT3F	2021	TL	180	8	3,000	38

•Packaging specifications

●Absolute maximum ratings (Ta = 25°C)

Para	neter	Symbol	Values	Unit
Supply voltage		V _{CC}	-50	V
Input voltage		V _{IN}	-40 to +10	V
Output current		Ι _Ο	-30	mA
Collector current		I _{C(MAX.)} *1	-100	mA
Power dissipation	DTA024EM DTA024EEB	P _D ^{*2}	150	mW
DTA024EUB			200	mW
Junction temperature		Tj	150	°C
Range of storage tempera	ture	T _{stg}	-55 to +150	°C

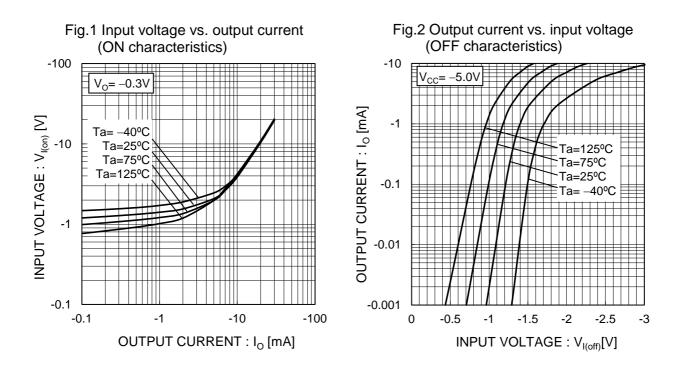
•Electrical characteristics(Ta = 25°C)

Parameter	Symbol	Conditions	Min.	Тур.	Max.	Unit
Input voltage	V _{I(off)}	$V_{CC} = -5V, I_{O} = -0.1mA$	-	-	-0.5	V
Input voltage	V _{I(on)}	$V_0 = -0.3V, I_0 = -5mA$	-3.0	-	-	V
Output voltage	V _{O(on)}	I _O / I _I = -5mA / -0.5mA	-	-0.07	-0.15	V
Input current	I _I	$V_1 = -5V$	-	-	-0.36	mA
Output current	I _{O(off)}	$V_{CC} = -50V, \ V_I = 0V$	-	-	-0.5	μA
DC current gain	G _I	$V_0 = -10V, I_0 = -5mA$	60	-	-	-
Input resistance	R ₁	-	15.4	22	28.6	kΩ
Resistance ratio	R_2/R_1	-	0.8	1	1.2	-
Transition frequency	f _T *1	V _{CE} = -10V, I _E = 5mA, f = 100MHz	-	250	-	MHz

*1 Characteristics of built-in transistor

*2 Each terminal mounted on a reference footprint

•Electrical characteristic curves(Ta = 25°C)



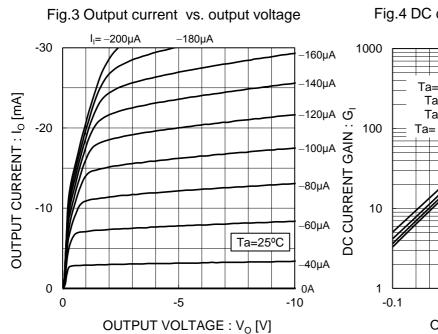
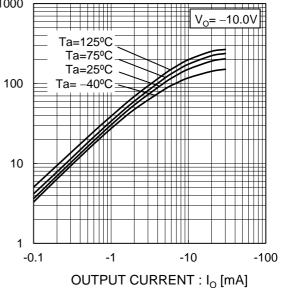


Fig.4 DC current gain vs. output current



•Electrical characteristic curves(Ta = 25°C)

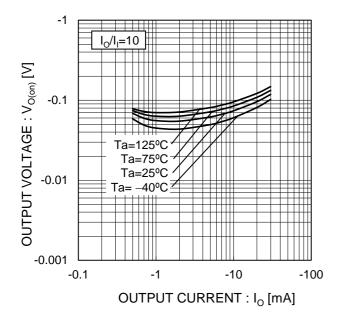
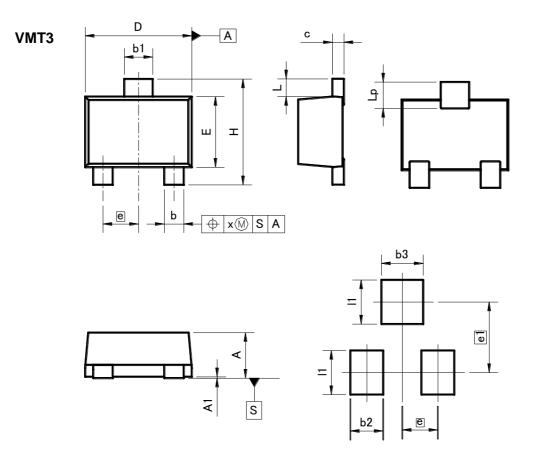


Fig.5 Output voltage vs. output current

•Dimensions (Unit : mm)



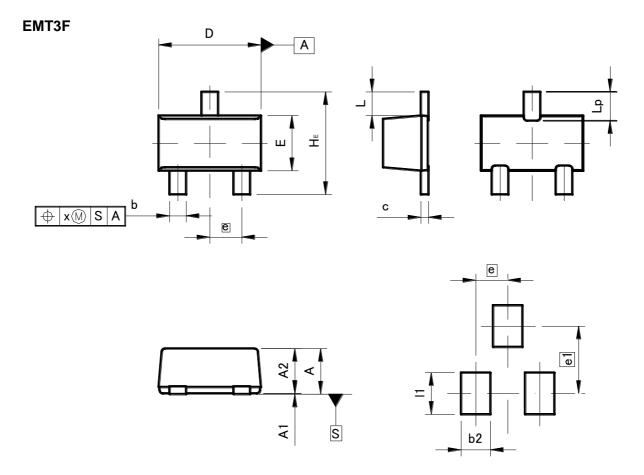
Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIN	MIN	MAX	MIN	MAX
А	0.45	0.55	0.018	0.022
A1	0.00	0.10	0	0.004
b	0.17	0.27	0.007	0.011
b1	0.27	0.37	0.011	0.015
с	0.08	0.18	0.003	0.007
D	1.10	1.30	0.043	0.051
E	0.70	0.90	0.028	0.035
е	0.4	40	0.02	
HE	1.10	1.30	0.043	0.051
L	0.10	0.30	0.004	_
Lp	0.20	0.40	0.008	-
х	_	0.10		0.004

DIM	MILIMETERS		INC	HES	
DIN	MIN	MAX	MIN	MAX	
e1	0.80		0.03		
b2	-	0.37	-	0.015	
b3	-	0.47	-	0.019	
1	_	0.50	-	0.02	

Dimension in mm/inches

•Dimensions (Unit : mm)



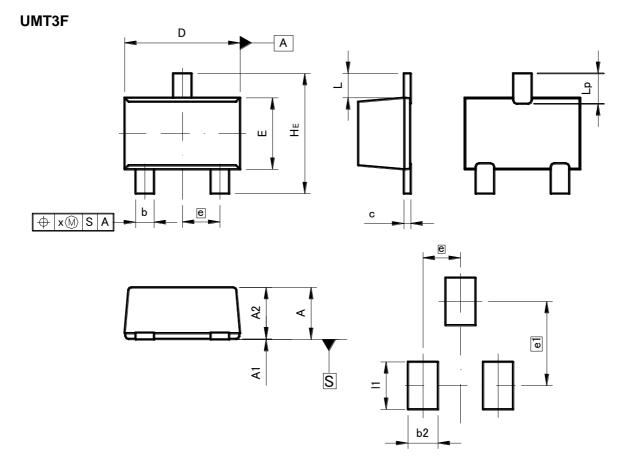
Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.65	0.85		
A1	0.00	0.10	0	0.004
A2	0.60	0.80	0.024	0.031
b	0.21	0.36	0.008	0.014
с	0.08	0.18	0.003	0.007
D	1.50	1.70	0.059	0.067
E	0.76	0.96	0.03	0.038
е	0.	50	0.0	02
HE	1.50	1.70	0.059	0.067
L	0.37		0.0	15
Lp	0.35	0.55	0.014	0.022
x	_	0.10	_	0.004

DIM	MILIMETERS		INC	HES
DIM	MIN	MAX	MIN	MAX
e1	-	1.05	-	0.041
b2	-	0.46	-	0.018
1	-	0.65	-	0.026

Dimension in mm/inches

•Dimensions (Unit : mm)



Patterm of terminal position areas

DIM	MILIM	ETERS	INC	HES
DIM	MIN	MAX	MIN	MAX
Α	0.85	1.05	0.033	0.041
A1	0.00	0.10	0	0.004
A2	0.80	1.00	0.031	0.039
b	0.27	0.42	0.011	0.017
с	0.08	0.18	0.003	0.007
D	1.90	2.10	0.075	0.083
E	1.15	1.35	0.045	0.053
е	0.0	65	0.0	03
HE	2.00	2.20	0.079	0.087
L	0.4	0.425		02
Lp	0.43	0.63	0.017	0.025
x	_	0.10	_	0.004

DIM	MILIMETERS		INC	HES	
DIM	MIN	MAX	MIN	MAX	
e1	1.47		0.058		
b2	-	0.52	-	0.02	
1	_	0.83	_	0.033	

Dimension in mm/inches

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