INTEGRATED CIRCUITS

DATA SHEET

TDA1518BQ24 W BTL or 2 x 12 watt stereo car radio power amplifier

Product specification
File under Integrated Circuits, IC01

July 1994





24 W BTL or 2 x 12 watt stereo car radio power amplifier

TDA1518BQ

GENERAL DESCRIPTION

The TDA1518BQ is an integrated class-B output amplifier in a 13-lead single-in-line (SIL) plastic power package. The device is primarily developed for car radio applications.

FEATURES

- · Requires very few external components
- Flexibility in use stereo as well as mono BTL
- High output power (without bootstrap)
- Low offset voltage at output (important for BTL)
- · Fixed gain
- · Good ripple rejection
- · Mute/stand-by switch
- · Load dump protection
- A.C. and d.c. short-circuit-safe to ground and V_P

- · Thermally protected
- · Reverse polarity safe
- Capability to handle high energy on outputs $(V_P = 0 V)$
- No switch-on/switch-off plop
- Flexible leads
- · Low thermal resistance
- Identical inputs (inverting and non-inverting)
- Compatible with TDA1516BQ (except gain).

QUICK REFERENCE DATA

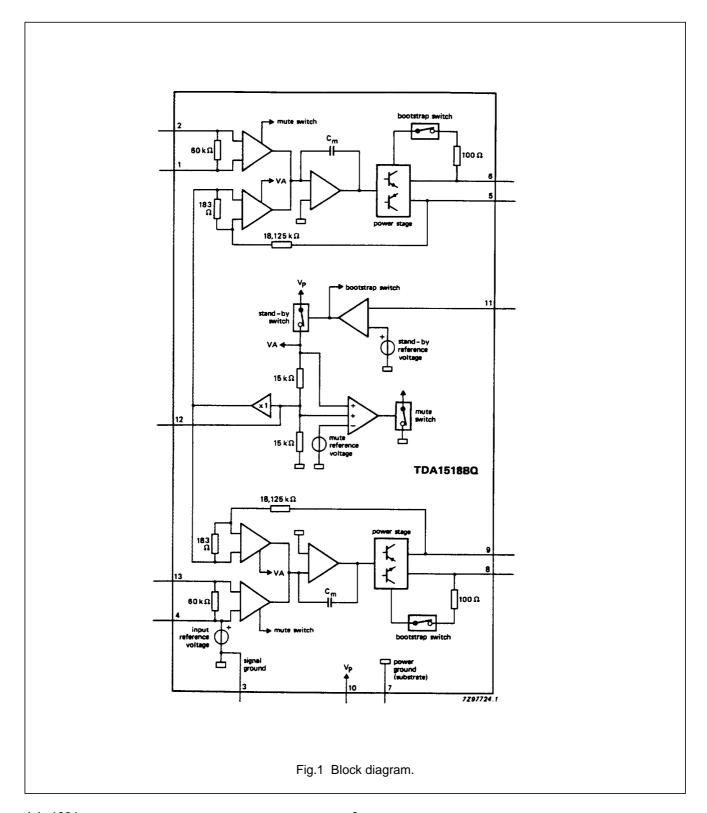
PARAMETER	CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Supply voltage range							
operating		V _P	6,0	14,4	18,0	V	
non-operating		V _P	_	_	30,0	V	
load dump		V _P	_	_	45,0	V	
Repetitive peak output							
current		I _{ORM}	_	_	4	Α	
Total quiescent current		I _{tot}	_	30	_	mA	
Stand-by current		I _{sb}	_	0,1	100	μΑ	
Switch-on current		I _{sw}	_	_	40	μΑ	
Input impedance							
BTL		$ Z_{I} $	25	_	_	kΩ	
stereo		Z _I	50	_	_	kΩ	
Stereo application							
Output power	THD = 10%; 4 Ω	Po	_	6	_	W	
	THD = 10%; 2 Ω	Po	_	11	_	W	
Channel separation		α	40	_	_	dB	
Noise output voltage		V _{no(rms)}	_	150	_	μV	
BTL application							
Output power	THD = 10%, 4 Ω	Po	_	22	_	W	
Supply voltage	$R_S = 0 \Omega;$						
ripple rejection	f = 100 Hz to 10 kHz	RR	48	_	_	dB	
D.C. output offset voltage		ΔV _O	_	_	250	mV	

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PACKAGE OUTLINE

13-lead SIL-bent-to-DIL; plastic power (SOT141C); SOT141-6; 1996 July 19.



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PINNING

1	–INV1	non-inverting input 1	8	BS2	bootstrap 2
2	INV	inverting input	9	OUT2	output 2
3	GND1	ground (signal)	10	V_{P}	supply voltage
4	V_{ref}	reference voltage	11	M/SS	mute/stand-by switch
5	OUT1	output 1	12	RR	supply voltage ripple rejection
6	BS1	bootstrap 1	13	-INV2	non-inverting input 2
7	GND2	ground (substrate)			

FUNCTIONAL DESCRIPTION

The TDA1518BQ contains two identical amplifiers with differential input stages. This device can be used for stereo or bridge applications. The gain of each amplifier is fixed at 40 dB. A special feature of this device is the mute/stand-by switch which has the following features:

- low stand-by current (< 100 μA)
- low mute/stand-by switching current (low cost supply switch)
- mute condition.

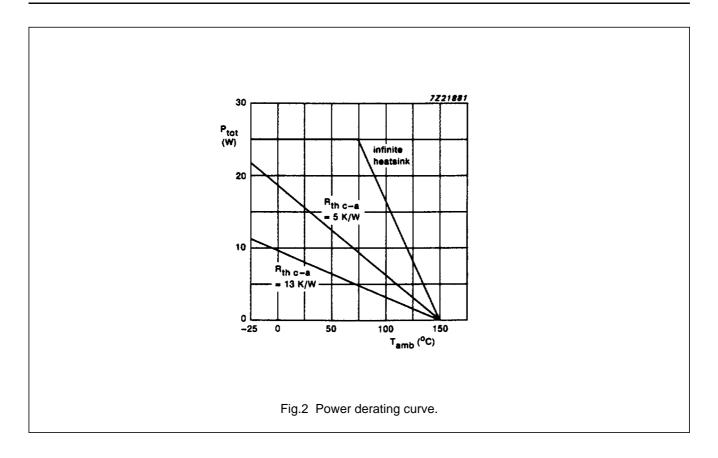
RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 134)

PARAMETER	CONDITIONS	SYMBOL	MIN.	MAX.	UNIT
Supply voltage					
operating		V_{P}	_	18	V
non-operating		V_{P}	_	30	V
load dump	during 50 ms;				
	t _r ≥ 2,5 ms	V_{P}	_	45	V
A.C. and d.c. short-circuit-					
safe voltage		V _{PSC}	_	18	V
Reverse polarity		V_{PR}	_	6	V
Energy handling capability					
at outputs	$V_P = 0 V$		_	200	mJ
Non-repetitive peak output					
current		I _{OSM}	_	6	Α
Repetitive peak output					
current		I _{ORM}	_	4	Α
Total power dissipation	see Fig.2	P _{tot}	_	25	W
Crystal temperature		T _c	_	150	°C
Storage temperature range		T _{stg}	-55	+150	°C

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D.C. CHARACTERISTICS (note 1)

 $V_P\!=$ 14.4 V; $T_{amb}\!=$ 25 °C; unless otherwise specified

PARAMETER	CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply	'		<u>'</u>	•		1
Supply voltage range	note 2	V _P	6,0	14,4	18,0	V
Quiescent current		I _P	-	30	note 3	mA
D.C. output voltage at						
approximately V _P /2	note 4	V _O	_	6,8	_	V
D.C. output offset						
voltage		ΔV ₅₋₉	_	_	200	mV
Mute/stand-by switch			•		•	•
Switch-on voltage level		V _{ON}	8,5	_	_	V
Mute condition	'		•	•		•
		V _{mute}	3,0	_	6,4	V
Output signal in mute	$V_{I} = 1 V (max.);$					
position	f = 20 Hz to					
	15 kHz	Vo	_	note 3	20	mV
D.C. output offset						
voltage		ΔV ₅₋₉	_	_	250	mV
Stand-by condition	1	-	_			'
		V _{sb}	0	_	2	V
D.C. current in						
stand-by condition		I _{sb}	-	_	100	μΑ
Switch-on current		I _{sw}	_	12	40	μΑ

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A.C. CHARACTERISTICS

 V_P = 14,4 V; R_L = 4 $\Omega;$ f = 1 kHz; T_{amb} = 25 $^{\circ}C;$ unless otherwise specified

PARAMETER	CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT			
Stereo application	note 1	note 1							
Output power	note 5;								
	THD = 0,5%	Po	4	5	_	W			
	THD = 10%	Po	5,5	6,0	_	W			
	notes 5 and 6;								
	THD = 10%	Po	6	7	_	W			
Output power at	note 5;								
$R_L = 2 \Omega$	THD = 0,5%	Po	7,75	8,5	_	W			
	THD = 10%	Po	10	11	_	W			
	notes 5 and 6;								
	THD = 10%	P _o	10,5	12,0	_	W			
Low frequency roll-off	note 7;								
	-3 dB	fL	_	45	_	Hz			
High frequency roll-off	-1 dB	f _H	20	_	_	kHz			
Closed loop voltage gain		G _v	39	40	41	dB			
Supply voltage ripple									
rejection:	note 8								
ON		RR	48	_	_	dB			
mute		RR	48	_	_	dB			
stand-by		RR	80	_	_	dB			
Input impedance		$ Z_{I} $	50	60	75	kΩ			
Noise output voltage:	note 9;								
ON	$R_S = 0 \Omega$	V _{no(rms)}	_	150	_	μV			
ON	$R_S = 10 \text{ k}\Omega$	V _{no(rms)}	_	250	500	μV			
mute	note 10	V _{no(rms)}	_	120	_	μV			
Channel separation	$R_S = 10 \text{ k}\Omega$	α	40	_	_	dB			
Channel balance		G _v	_	0.1	1	dB			
BTL application	note 11	- 1 - 2							
Output power	THD = 0,5%	P _o	15,5	17,0	_	W			
	THD = 10%	Po	20	22	_	W			
	note 6; THD = 10%	Po	21	24	_	W			
Output power at V _P	, , , , , , , , , , , , , , , , , , , ,								
= 13,2 V	THD = 0,5%	Po	_	13.5	_	W			
· - , - ·	THD = 10%	P _o	_	17	_	W			
	note 6; THD = 10%	P _o	_	19	_	W			
Power bandwidth	THD = 0,5%	. 0							
. Sit Si Ballamatil	$P_0 = 15 \text{ W}$	B _w	_	20 to	_	Hz			
	0 - 10 **] w		15 000		' '			

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PARAMETER	CONDITIONS	SYMBOL	MIN.	TYP.	MAX.	UNIT
Low frequency roll-off	note 7;					
	-3 dB	fL	_	45	_	Hz
High frequency roll-off	-1 dB	f _H	20	_	_	kHz
Closed loop voltage gain		G _v	45	46	47	dB
Supply voltage ripple						
rejection:	note 8					
ON		RR	48	_	_	dB
mute		RR	48	_	_	dB
stand-by		RR	80	_	_	dB
Input impedance		Z _I	25	30	38	kΩ
Noise output voltage:	note 9;					
ON	$R_S = 0 \Omega$	V _{no(rms)}	_	200	_	μV
ON	$R_S = 10 \text{ k}\Omega$	V _{no(rms)}	_	350	700	μV
mute	note 10	V _{no(rms)}	_	120	_	μV
Switch-on/switch-off						
behaviour		dV/dt	_	_	note 3	V/ms

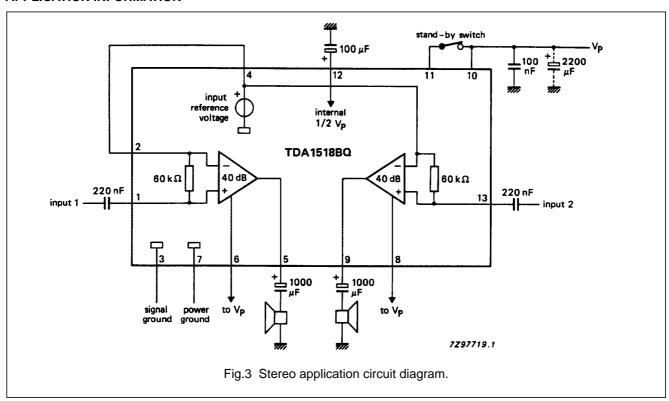
Notes to the characteristics

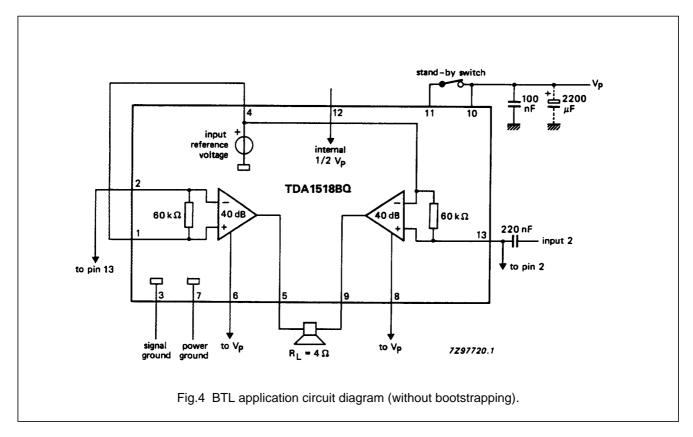
- 1. All characteristics, for stereo application are measured using the circuit shown in Fig.3.
- 2. The circuit is d.c. adjusted at $V_P = 6 \text{ V}$ to 18 V and a.c. operating at $V_P = 8,1 \text{ V}$ to 18 V.
- 3. Value to be fixed.
- 4. At 18 V < V_P < 30 V the d.c. output voltage $\leq V_P/2$.
- 5. Output power is measured directly at the output pins of the IC.
- 6. With bootstrap and a 100 k Ω resistor from pin 12 to the positive supply voltage (V_P), value of bootstrap capacitor is 47 μ F.
- 7. Frequency response externally fixed.
- 8. Ripple rejection measured at the output with a source impedance of 0 Ω (maximum ripple amplitude of 2 V) and a frequency between 1 kHz and 10 kHz.
- 9. Noise voltage measured in a bandwidth of 20 Hz to 20 kHz.
- 10. Noise output voltage independent of R_S ($V_I = 0 V$).
- 11. All characteristics, for BTL application are measured using the circuit shown in Fig.4.

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APPLICATION INFORMATION





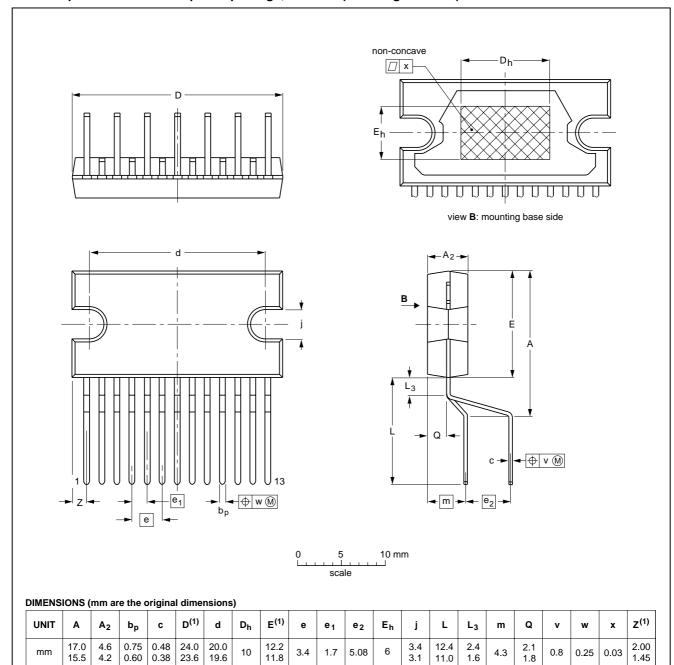
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PACKAGE OUTLINE

DBS13P: plastic DIL-bent-SIL power package; 13 leads (lead length 12 mm)

SOT141-6



Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

19.6

0.60

OUTL	INE		REFERENCES			EUROPEAN	ISSUE DATE
VERS	SION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT1	141-6						95-03-11 97-12-16

3.1

11.0

July 1994 10

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SOLDERING

Introduction

There is no soldering method that is ideal for all IC packages. Wave soldering is often preferred when through-hole and surface mounted components are mixed on one printed-circuit board. However, wave soldering is not always suitable for surface mounted ICs, or for printed-circuits with high population densities. In these situations reflow soldering is often used.

This text gives a very brief insight to a complex technology. A more in-depth account of soldering ICs can be found in our "IC Package Databook" (order code 9398 652 90011).

Soldering by dipping or by wave

The maximum permissible temperature of the solder is 260 °C; solder at this temperature must not be in contact with the joint for more than 5 seconds. The total contact time of successive solder waves must not exceed 5 seconds.

The device may be mounted up to the seating plane, but the temperature of the plastic body must not exceed the specified maximum storage temperature (T_{stg max}). If the printed-circuit board has been pre-heated, forced cooling may be necessary immediately after soldering to keep the temperature within the permissible limit.

Repairing soldered joints

Apply a low voltage soldering iron (less than 24 V) to the lead(s) of the package, below the seating plane or not more than 2 mm above it. If the temperature of the soldering iron bit is less than 300 $^{\circ}$ C it may remain in contact for up to 10 seconds. If the bit temperature is between 300 and 400 $^{\circ}$ C, contact may be up to 5 seconds.

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

Application information

Where application information is given, it is advisory and does not form part of the specification.

LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.