Preferred Devices

SC-74 Quad Monolithic Common Anode Transient Voltage Suppressors for ESD Protection

This quad monolithic silicon voltage suppressor is designed for applications requiring transient overvoltage protection capability. It is intended for use in voltage and ESD sensitive equipment such as computers, printers, business machines, communication systems, medical equipment, and other applications. Its quad junction common anode design protects four separate lines using only one package. These devices are ideal for situations where board space is at a premium.

Specification Features:

- SC-74 Package Allows Four Separate Unidirectional Configurations
- Peak Power Min. 24 W @ 1.0 ms (Unidirectional), per Figure 5 Waveform
- Peak Power Min. 150 W @ 20 µs (Unidirectional), per Figure 6 Waveform
- Maximum Clamping Voltage @ Peak Pulse Current
- Low Leakage < 2.0 μA
- ESD Rating of Class N (exceeding 16 kV) per the Human Body Model
- Pb–Free Packages are Available

THERMAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

Characteristic	Symbol	Value	Unit
Peak Power Dissipation @ 1.0 ms (Note 1) @ $T_A \leq 25^\circ C$	P _{pk}	24	W
Peak Power Dissipation @ 20 μs (Note 2) @ $T_A \leq 25^\circ C$	P _{pk}	150	W
Total Power Dissipation on FR-5 Board (Note 3) @ $T_A = 25^{\circ}C$	P _D	225 1.8	mW mW/°C
Thermal Resistance from Junction-to-Ambi- ent	R_{\thetaJA}	556	°C/W
Total Power Dissipation on Alumina Sub- strate (Note 4) @ $T_A = 25^{\circ}C$ Derate above 25°C	P _D	300 2.4	mW mW/°C
Thermal Resistance from Junction-to-Ambi- ent	R_{\thetaJA}	417	°C/W
Junction and Storage Temperature Range	T _J , T _{stg}	– 55 to +150	°C
Lead Solder Temperature – Maximum (10 Second Duration)	ΤL	260	°C



ON Semiconductor®

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SC-74 QUAD TRANSIENT VOLTAGE SUPPRESSOR 24 WATTS PEAK POWER 5.6 – 33 VOLTS

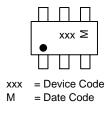
PIN ASSIGNMENT



PLASTIC CASE 318F

3	H	. ← ⊢	◄	4
PIN	1.	CA	гноі	DE
	2.	AN	ODE	
	3.	CA	гноі	DE
	4.	CA	ГНОІ	DE
			ODE	
	6.	CAT	LHOI	DE

MARKING DIAGRAM



ORDERING INFORMATION

See detailed ordering and shipping information in the table on page 5 of this data sheet.

DEVICE MARKING INFORMATION

See specific marking information in the device marking table on page 5 of this data sheet.

Preferred devices are recommended choices for future use and best overall value.

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted) UNIDIRECTIONAL

(Circuit tied to pins 1, 2, and 5; Pins 2, 3, and 5; Pins 2, 4, and 5; or Pins 2, 5, and 6) (V_F = 0.9 V Max @ I_F = 10 mA)

	E	Breakdown	Voltag	je	Ma Reve Leak Curr	rse age	Мах		Max Reverse Voltage @ ax I _{RSM}	Maximum	Capacitance @ 0 Volt Bias, 1 MHz	
		V _{ZT} (Note 5) (V)		@ I _{ZT}	I _R	V _R	Max Zener Impedance (Note 7)	Reverse Surge (C	(Note 6) (Clamping Voltage)	Temperature Coefficient of V _Z	(p	F)
Device	Min	Nom	Max	(mA)	(nA)	(V)	Z _{ZT} @ I _{ZT} (Ω) (mA)	I _{RSM} (A)	V _{RSM} (V)	(mV/°C)	Min	Max
MMQA5V6T1,T3	5.32	5.6	5.88	1.0	2000	3.0	400	3.0	8.0	1.26	-	-
MMQA6V2T1,T3	5.89	6.2	6.51	1.0	700	4.0	300	2.66	9.0	10.6	-	-
MMQA6V8T1,T3	6.46	6.8	7.14	1.0	500	4.3	300	2.45	9.8	10.9	100	250
MMQA12VT1,T3	11.4	12	12.6	1.0	75	9.1	80	1.39	17.3	14	-	-
MMQA13VT1	12.4	13	13.7	1.0	75	9.8	80	1.29	18.6	15	-	-
MMQA15VT1,T3	14.3	15	15.8	1.0	75	11	80	1.1	21.7	16	-	-
MMQA18VT1,T3	17.1	18	18.9	1.0	75	14	80	0.923	26	19	-	-
MMQA20VT1,T3	19	20	21	1.0	75	15	80	0.84	28.6	20.1	-	-
MMQA21VT1,T3	20	21	22.1	1.0	75	16	80	0.792	30.3	21	-	-
MMQA22VT1,T3	20.9	22	23.1	1.0	75	17	80	0.758	31.7	22	-	-
MMQA24VT1,T3	22.8	24	25.2	1.0	75	18	100	0.694	34.6	25	-	-
MMQA27VT1,T3	25.7	27	28.4	1.0	75	21	125	0.615	39	28	-	-
MMQA30VT1,T3	28.5	30	31.5	1.0	75	23	150	0.554	43.3	32	-	-
MMQA33VT1,T3	31.4	33	34.7	1.0	75	25	200	0.504	48.6	37	_	-

1. Non-repetitive current pulse per Figure 5 and derate above $T_A = 25^{\circ}C$ per Figure 4. 2. Non-repetitive current pulse per Figure 6 and derate above $T_A = 25^{\circ}C$ per Figure 4.

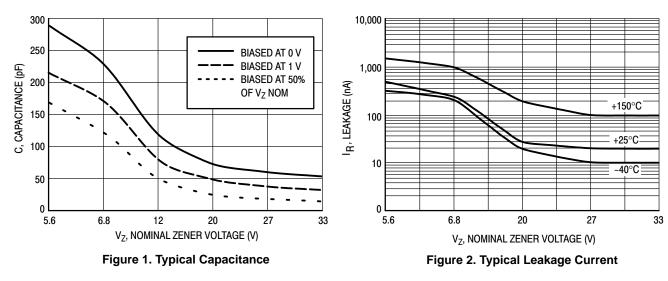
3. FR-5 = 1.0 x 0.75 x 0.62 in.

4. Alumina = 0.4 x 0.3 x 0.024 in., 99.5% alumina

5. V_Z measured at pulse test current I_T at an ambient temperature of 25°C.

6. Surge current waveform per Figure 5 and derate per Figure 4.

 Z_{ZT} is measured by dividing the AC voltage drop across the device by the AC current supplied. The specified limits are $I_{Z(AC)} = 0.1 I_{Z(DC)}$, 7. with AC frequency = 1 kHz.



TYPICAL CHARACTERISTICS

TYPICAL CHARACTERISTICS

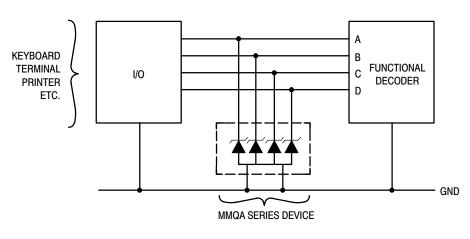
300 100 PEAK PULSE DERATING IN % OF PEAK POWER 90 PD , POWER DISSIPATION (mW) 250 80 ALUMINA SUBSTRATE OR CURRENT @ $T_A = 25^{\circ}C$ 70 200 60 150 50 40 100 30 FR-5 BOARD 20 50 10 0° 0 0 25 50 75 100 125 150 175 25 50 75 100 125 150 175 200 T_A, AMBIENT TEMPERATURE (°C) T_A, AMBIENT TEMPERATURE (°C) Figure 3. Steady State Power Derating Curve Figure 4. Pulse Derating Curve 100 PULSE WIDTH (tp) IS DEFINED PEAK VALUE $I_{RSM} \ensuremath{@}\xspace 8\ensuremath{\,\mu s}\xspace$ AS THAT POINT WHERE THE 90 t, PEAK CURRENT DECAYS TO 50% PULSE WIDTH (tp) IS DEFINED **OF PEAK PULSE CURRENT** 80 AS THAT POINT WHERE THE OF IRSM. PEAK VALUE - IRSM 100 70 t_r ≤ 10 μs PEAK CURRENT DECAY = 8 µs 60 VALUE (%) - HALF VALUE I_{RSM}/2 @ 20 μs I_{RSM} 50 HALF VALUE 2 40 50 30 tc 20 tp % 10 0 0L 0 2 3 1 4 20 40 60 80 0 t, TIME (ms) t, TIME (µs) Figure 5. 10 \times 1000 μ s Pulse Waveform Figure 6. 8 \times 20 μ s Pulse Waveform 200 100 1111 1 1 1 1 1 1 1 1 RECTANGULAR 180 WAVEFORM. TA = 25°C Ś 160 Ppk PEAK SURGE POWER (W) 8 × 20 WAVEFORM AS PER FIGURE 6 PEAK SURGE POWER 140 120 10 100 80 UNIDIRECTIONAL 10×100 WAVEFORM AS PER FIGURE 5 60 Ррқ, 40 20 1.0∟ 0.1 0 1.0 10 100 1000 5.6 6.8 12 20 27 33 PW, PULSE WIDTH (ms) NOMINAL V_Z Figure 7. Maximum Non–Repetitive Surge Figure 8. Typical Maximum Non-Repetitive Power, Ppk versus PW Surge Power, Ppk versus V_{BR}

Power is defined as $V_{RSM} \times I_Z(pk)$ where V_{RSM} is the clamping voltage at I₇(pk).

TYPICAL COMMON ANODE APPLICATIONS

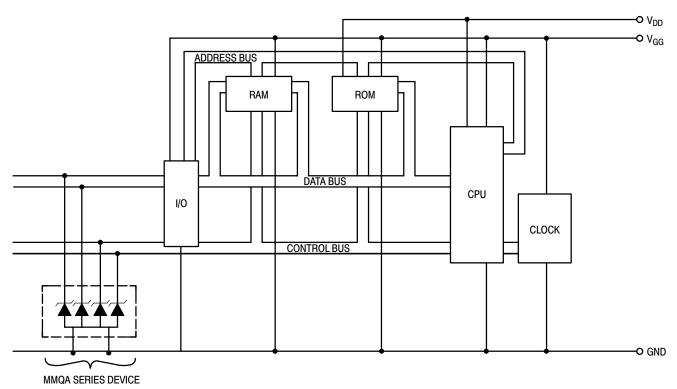
A quad junction common anode design in a SC-74 package protects four separate lines using only one package. This adds flexibility and creativity to PCB design especially

when board space is at a premium. A simplified example of MMQA Series Device applications is illustrated below.



Computer Interface Protection





DEVICE MARKING AND ORDERING INFORMATION

Device	Device Marking	Package	Shipping [†]		
MMQA5V6T1	5A6	SC-74	3,000/Tape & Reel		
MMQA5V6T3	5A6	SC-74	10,000/Tape & Reel		
MMQA6V2T1	6A2	SC-74	3,000/Tape & Reel		
MMQA6V2T1G	6A2	SC–74 (Pb–Free)	3,000/Tape & Reel		
MMQA6V2T3	6A2	SC-74	10,000/Tape & Reel		
MMQA6V8T1	6A8	SC-74	3,000/Tape & Reel		
MMQA6V8T3	6A8	SC-74	10,000/Tape & Reel		
MMQA12VT1	12A	SC-74	3,000/Tape & Reel		
MMQA12VT1G	12A	SC–74 (Pb–Free)	3,000/Tape & Reel		
MMQA12VT3	12A	SC-74	10,000/Tape & Reel		
MMQA13VT1	13A	SC-74	3,000/Tape & Reel		
MMQA15VT1	15A	SC-74	3,000/Tape & Reel		
MMQA15VT3	15A	SC-74	10,000/Tape & Reel		
MMQA18VT1	18A	SC-74	3,000/Tape & Reel		
MMQA18VT3	18A	SC-74	10,000/Tape & Reel		
MMQA20VT1	20A	SC-74	3,000/Tape & Reel		
MMQA20VT3	20A	SC-74	10,000/Tape & Reel		
MMQA20VT3G	20A	SC–74 (Pb–Free)	10,000/Tape & Reel		
MMQA21VT1	21A	SC-74	3,000/Tape & Reel		
MMQA21VT3	21A	SC-74	10,000/Tape & Reel		
MMQA22VT1	22A	SC-74	3,000/Tape & Reel		
MMQA22VT3	22A	SC-74	10,000/Tape & Reel		
MMQA24VT1	24A	SC-74	3,000/Tape & Reel		
MMQA24VT3	24A	SC-74	10,000/Tape & Reel		
MMQA27VT1	27A	SC-74	3,000/Tape & Reel		
MMQA27VT3	27A	SC-74	10,000/Tape & Reel		
MMQA30VT1	30A	SC-74	3,000/Tape & Reel		
MMQA30VT3	30A	SC-74	10,000/Tape & Reel		
MMQA33VT1	33A	SC-74	3,000/Tape & Reel		
MMQA33VT3	33A	SC-74	10,000/Tape & Reel		

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, BRD8011/D.

Mechanical Characteristics:

CASE: Void-free, transfer-molded, thermosetting plastic case.

FINISH: Corrosion resistant finish, easily solderable.

Package designed for optimal automated board assembly.

Small package size for high density applications.

Available in 8 mm Tape and Reel.

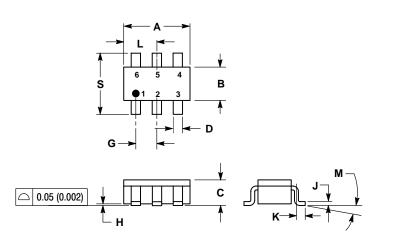
Use the Device Number to order the 7 inch/3,000 unit reel.

Replace the "T1" with "T3" in the Device Number to order the

13 inch/10,000 unit reel.

PACKAGE DIMENSIONS

SC-74 CASE 318F-05 ISSUE K



NOTES

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. CONTROLLING DIMENSION: INCH.

2

MAXIMUM LEAD THICKNESS INCLUDES 3. LEAD FINISH THICKNESS, MINIMUM LEAD FINISH THICKNESS. MINIMUM LEAD THICKNESS IS THE MINIMUM THICKNESS OF BASE MATERIAL. 318F-01, -02, -03 OBSOLETE. NEW STANDARD 318F-04.

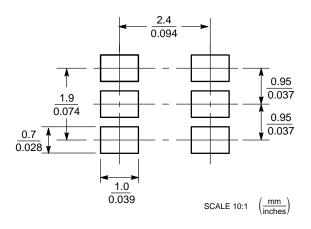
4

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.1142	0.1220	2.90	3.10	
В	0.0512	0.0669	1.30	1.70	
С	0.0354	0.0433	0.90	1.10	
D	0.0098	0.0197	0.25	0.50	
G	0.0335	0.0413	0.85	1.05	
н	0.0005	0.0040	0.013	0.100	
J	0.0040	0.0102	0.10	0.26	
Κ	0.0079	0.0236	0.20	0.60	
Ĺ	0.0493	0.0649	1.25	1.65	
М	0 °	10 °	0 °	10 °	
S	0.0985	0.1181	2.50	3.00	

STYLE 1: PIN 1. CATHODE

2. ANODE
3. CATHODE
4. CATHODE
5. ANODE
6. CATHODE

SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual. SOLDERRM/D.

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