

# MAC228A Series

Preferred Device

## Sensitive Gate Triacs

### Silicon Bidirectional Thyristors

Designed primarily for industrial and consumer applications for full wave control of ac loads such as appliance controls, heater controls, motor controls, and other power switching applications.

- Sensitive Gate Triggering in 3 Modes for AC Triggering on Sinking Current Sources
- Four Mode Triggering for Drive Circuits that Source Current
- All Diffused and Glass-Passivated Junctions for Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance and High Heat Dissipation
- Center Gate Geometry for Uniform Current Spreading
- Device Marking: Logo, Device Type, e.g., MAC228A4, Date Code

#### MAXIMUM RATINGS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Peak Repetitive Off-State Voltage <sup>(1)</sup> ( $T_J = -40$ to $110^\circ\text{C}$ , Sine Wave, 50 to 60 Hz, Gate Open) MAC228A4 MAC228A6 MAC228A8 MAC228A10	$V_{\text{DRM}}$ , $V_{\text{RRM}}$	200 400 600 800	Volts
On-State RMS Current ( $T_C = 80^\circ\text{C}$ ) Full Cycle Sine Wave 50 to 60 Hz	$I_{\text{T(RMS)}}$	8.0	Amps
Peak Non-Repetitive Surge Current (One Full Cycle Sine Wave, 60 Hz, $T_J = 110^\circ\text{C}$ )	$I_{\text{TSM}}$	80	Amps
Circuit Fusing Considerations ( $t = 8.3$ ms)	$I^2t$	26	$\text{A}^2\text{s}$
Peak Gate Current ( $t \leq 2 \mu\text{s}$ , $T_C = 80^\circ\text{C}$ )	$I_{\text{GM}}$	$\pm 2.0$	Amps
Peak Gate Voltage ( $t \leq 2 \mu\text{s}$ , $T_C = 80^\circ\text{C}$ )	$V_{\text{GM}}$	$\pm 10$	Volts
Peak Gate Power ( $t \leq 2 \mu\text{s}$ , $T_C = 80^\circ\text{C}$ )	$P_{\text{GM}}$	20	Watts
Average Gate Power ( $t \leq 8.3$ ms, $T_C = 80^\circ\text{C}$ )	$P_{\text{G(AV)}}$	0.5	Watt
Operating Junction Temperature Range	$T_J$	$-40$ to $110$	$^\circ\text{C}$
Storage Temperature Range	$T_{\text{stg}}$	$-40$ to $150$	$^\circ\text{C}$
Mounting Torque	—	8.0	in. lb.

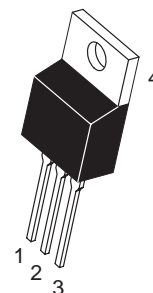
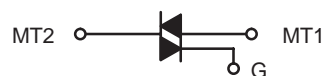
(1)  $V_{\text{DRM}}$  and  $V_{\text{RRM}}$  for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.



ON Semiconductor

<http://onsemi.com>

**TRIACS**  
**8 AMPERES RMS**  
**200 thru 800 VOLTS**



**TO-220AB**  
**CASE 221A**  
**STYLE 4**

#### PIN ASSIGNMENT

1	Main Terminal 1
2	Main Terminal 2
3	Gate
4	Main Terminal 2

#### ORDERING INFORMATION

Device	Package	Shipping
MAC228A4	TO220AB	500/Box
MAC228A6	TO220AB	500/Box
MAC228A8	TO220AB	500/Box
MAC228A10	TO220AB	500/Box

Preferred devices are recommended choices for future use and best overall value.

## MAC228A Series

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Value	Unit
Thermal Resistance — Junction to Case — Junction to Ambient	$R_{\theta JC}$ $R_{\theta JA}$	2.0 62.5	$^{\circ}\text{C}/\text{W}$
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 10 Seconds	$T_L$	260	$^{\circ}\text{C}$

### ELECTRICAL CHARACTERISTICS ( $T_C = 25^{\circ}\text{C}$ unless otherwise noted; Electricals apply in both directions)

Characteristic	Symbol	Min	Typ	Max	Unit
----------------	--------	-----	-----	-----	------

### OFF CHARACTERISTICS

Peak Repetitive Blocking Current ( $V_D = \text{Rated } V_{DRM}, V_{RRM}$ ; Gate Open)	$I_{DRM}, I_{RRM}$	— —	— —	10 2.0	$\mu\text{A}$ mA
$T_J = 25^{\circ}\text{C}$ $T_J = 110^{\circ}\text{C}$					

### ON CHARACTERISTICS

Peak On-State Voltage ( $I_{TM} = \pm 11 \text{ A Peak}$ , Pulse Width $\leq 2 \text{ ms}$ , Duty Cycle $\leq 2\%$ )	$V_{TM}$	—	—	1.8	Volts
Gate Trigger Current (Continuous dc) ( $V_D = 12 \text{ V}$ , $R_L = 100 \Omega$ ) MT2(+), G(+); MT2(+), G(-); MT2(-), G(-) MT2(-), G(+)	$I_{GT}$	— —	— —	5.0 10	mA
Gate Trigger Voltage (Continuous dc) ( $V_D = 12 \text{ V}$ , $R_L = 100 \Omega$ ) MT2(+), G(+); MT2(+), G(-); MT2(-), G(-) MT2(-), G(+)	$V_{GT}$	— —	— —	2.0 2.5	Volts
Gate Non-Trigger Voltage (Continuous dc) ( $V_D = 12 \text{ V}$ , $T_C = 110^{\circ}\text{C}$ , $R_L = 100 \Omega$ ) All Four Quadrants	$V_{GD}$	0.2	—	—	Volts
Holding Current ( $V_D = 12 \text{ Vdc}$ , Initiating Current = $\pm 200 \text{ mA}$ , Gate Open)	$I_H$	—	—	15	mA
Gate-Controlled Turn-On Time ( $V_D = \text{Rated } V_{DRM}$ , $I_{TM} = 16 \text{ A Peak}$ , $I_G = 30 \text{ mA}$ )	$t_{gt}$	—	1.5	—	$\mu\text{s}$

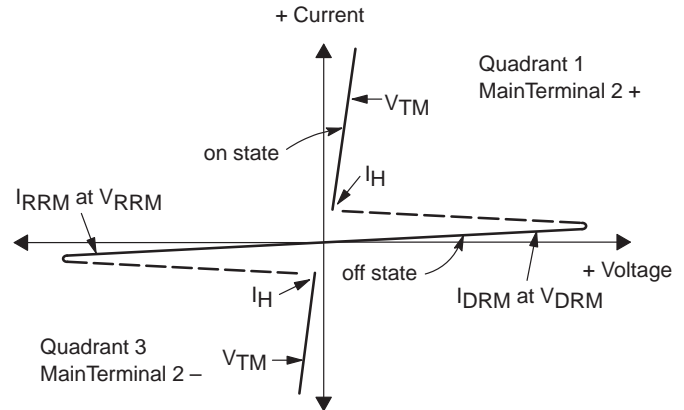
### DYNAMIC CHARACTERISTICS

Critical Rate of Rise of Off-State Voltage ( $V_D = \text{Rated } V_{DRM}$ , Exponential Waveform, $T_C = 110^{\circ}\text{C}$ )	$dv/dt$	—	25	—	$\text{V}/\mu\text{s}$
Critical Rate of Rise of Commutation Voltage ( $V_D = \text{Rated } V_{DRM}$ , $I_{TM} = 11.3 \text{ A}$ , Commutating $di/dt = 4.1 \text{ A/ms}$ , Gate Unenergized, $T_C = 80^{\circ}\text{C}$ )	$dv/dt(c)$	—	5.0	—	$\text{V}/\mu\text{s}$

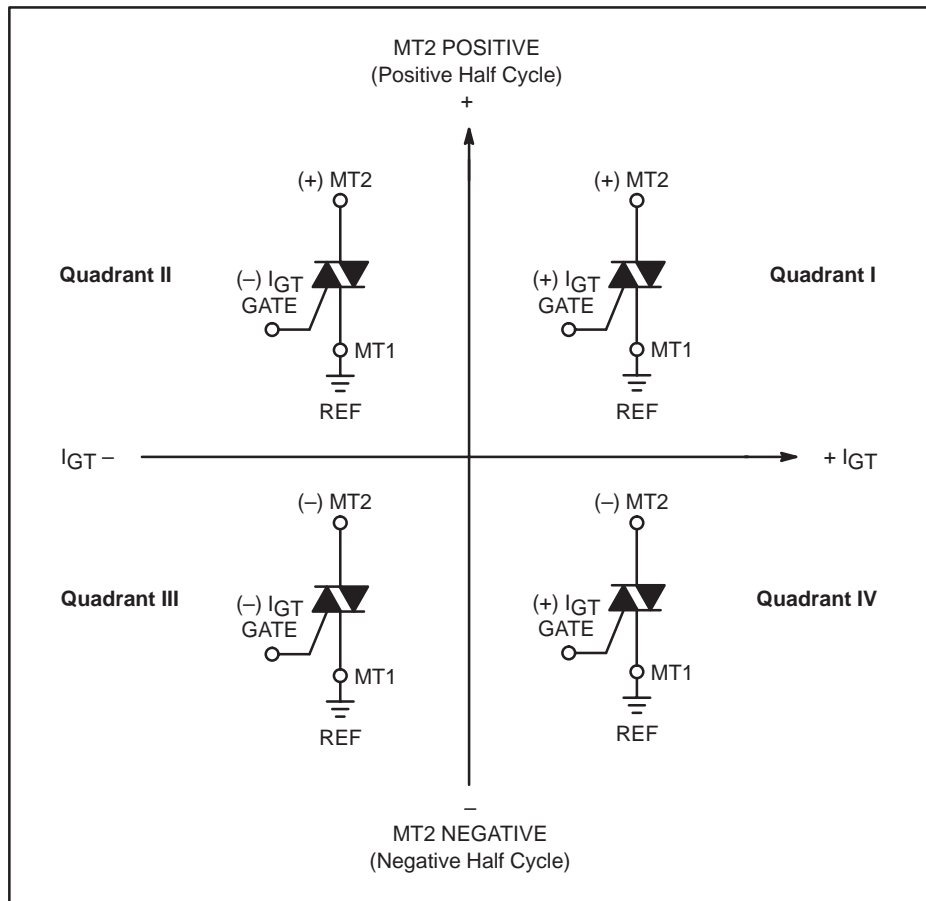
## MAC228A Series

### Voltage Current Characteristic of Triacs (Bidirectional Device)

Symbol	Parameter
$V_{DRM}$	Peak Repetitive Forward Off State Voltage
$I_{DRM}$	Peak Forward Blocking Current
$V_{RRM}$	Peak Repetitive Reverse Off State Voltage
$I_{RRM}$	Peak Reverse Blocking Current
$V_{TM}$	Maximum On State Voltage
$I_H$	Holding Current



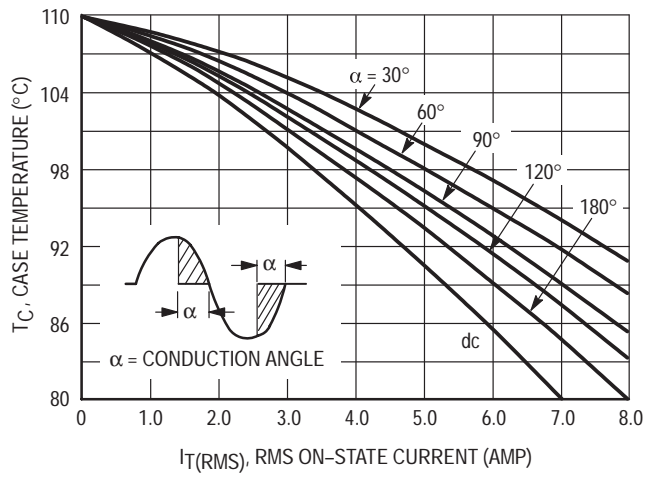
### Quadrant Definitions for a Triac



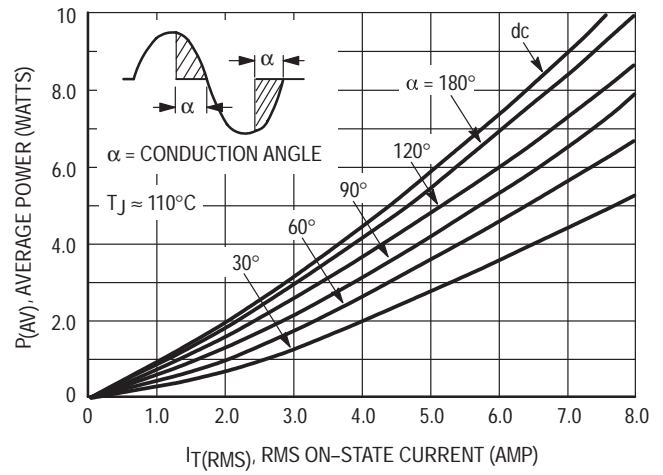
All polarities are referenced to MT1.

With in-phase signals (using standard AC lines) quadrants I and III are used.

## MAC228A Series



**Figure 1. RMS Current Derating**

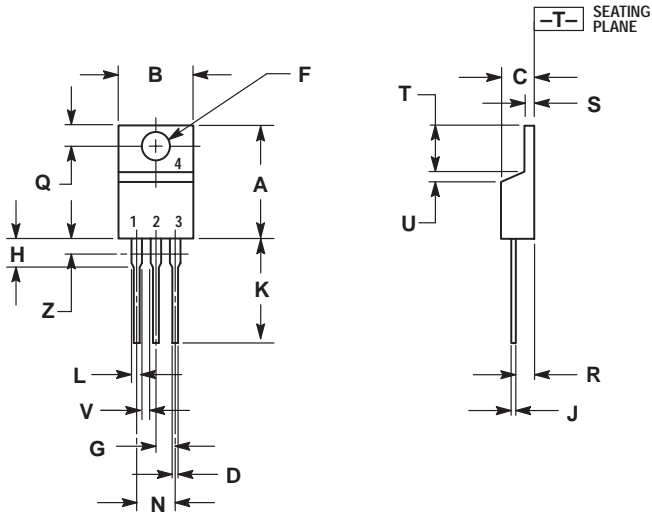


**Figure 2. On-State Power Dissipation**

# MAC228A Series

## PACKAGE DIMENSIONS

### TO-220AB CASE 221A-07 ISSUE Z



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.570	0.620	14.48	15.75
B	0.380	0.405	9.66	10.28
C	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
H	0.110	0.155	2.80	3.93
J	0.014	0.022	0.36	0.55
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	---	1.15	---
Z	---	0.080	---	2.04

- STYLE 4:
- PIN 1. MAIN TERMINAL 1
  - MAIN TERMINAL 2
  - GATE
  - MAIN TERMINAL 2

## **Notes**

## **Notes**

## MAC228A Series

**ON Semiconductor** and  are trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

### PUBLICATION ORDERING INFORMATION

#### **NORTH AMERICA Literature Fulfillment:**

Literature Distribution Center for ON Semiconductor  
P.O. Box 5163, Denver, Colorado 80217 USA  
**Phone:** 303-675-2175 or 800-344-3860 Toll Free USA/Canada  
**Fax:** 303-675-2176 or 800-344-3867 Toll Free USA/Canada  
**Email:** ONlit@hibbertco.com  
Fax Response Line: 303-675-2167 or 800-344-3810 Toll Free USA/Canada

**N. American Technical Support:** 800-282-9855 Toll Free USA/Canada

**EUROPE:** LDC for ON Semiconductor – European Support

**German Phone:** (+1) 303-308-7140 (M–F 1:00pm to 5:00pm Munich Time)  
**Email:** ONlit-german@hibbertco.com  
**French Phone:** (+1) 303-308-7141 (M–F 1:00pm to 5:00pm Toulouse Time)  
**Email:** ONlit-french@hibbertco.com  
**English Phone:** (+1) 303-308-7142 (M–F 12:00pm to 5:00pm UK Time)  
**Email:** ONlit@hibbertco.com

**EUROPEAN TOLL-FREE ACCESS\*: 00-800-4422-3781**

\*Available from Germany, France, Italy, England, Ireland

#### **CENTRAL/SOUTH AMERICA:**

**Spanish Phone:** 303-308-7143 (Mon–Fri 8:00am to 5:00pm MST)  
**Email:** ONlit-spanish@hibbertco.com

**ASIA/PACIFIC:** LDC for ON Semiconductor – Asia Support

**Phone:** 303-675-2121 (Tue–Fri 9:00am to 1:00pm, Hong Kong Time)  
Toll Free from Hong Kong & Singapore:  
**001-800-4422-3781**  
**Email:** ONlit-asia@hibbertco.com

**JAPAN:** ON Semiconductor, Japan Customer Focus Center  
4-32-1 Nishi-Gotanda, Shinagawa-ku, Tokyo, Japan 141-8549  
**Phone:** 81-3-5740-2745  
**Email:** r14525@onsemi.com

**ON Semiconductor Website:** <http://onsemi.com>

For additional information, please contact your local Sales Representative.