November 1994

LM102/LM302 Voltage Followers

General Description

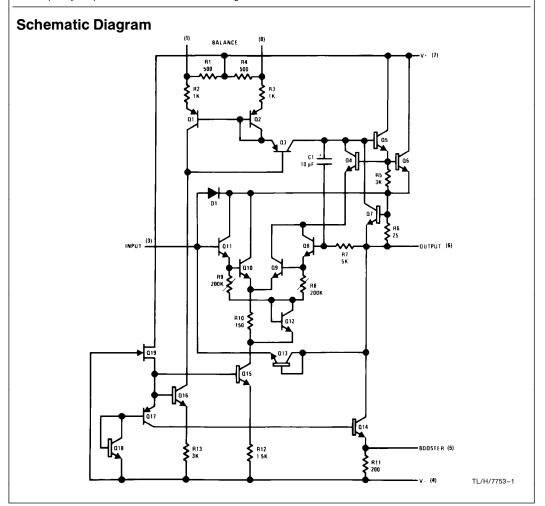
The LM102 series are high-gain operational amplifiers designed specifically for unity-gain voltage follower applications. Built on a single silicon chip, the devices incorporate advanced processing techniques to obtain very low input current and high input impedance. Further, the input transistors are operated at zero collector-base voltage to virtually eliminate high temperature leakage currents. It can therefore be operated in a temperature stabilized component oven to get extremely low input currents and low offset voltage drift.

The LM102, which is designed to operate with supply voltages between $\pm 12 V$ and $\pm 15 V$, also features low input capacitance as well as excellent small signal and large signal frequency response—all of which minimize high fre-

quency gain error. Because of the low wiring capacitances inherent in monolithic construction, this fast operation can be realized without increasing power consumption.

Features

- Fast slewing 10V/µs
- Low input current 10 nA (max)
- High input resistance 10,000 M Ω
- No external frequency compensation required
- \blacksquare Simple offset balancing with optional 1 k Ω potentiometer
- Plug-in replacement for both the LM101 and LM709 in voltage follower applications



Absolute Maximum Ratings

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications. (Note 6)

 Supply Voltage
 ± 18V

 Power Dissipation (Note 1)
 500 mW

 Input Voltage (Note 2)
 ± 15V

 Output Short Circuit Duration (Note 3)
 Indefinite

Operating Free Air Temperature Range
LM102
LM302

Storage Temperature Range
Lead Temperature (Soldering, 10 sec.)

ESD rating to be determined.

-55°C to +125°C
-65°C to +70°C
-65°C to +150°C
300°C
300°C

Electrical Characteristics (Note 4)

Parameter	Conditions	LM102			LM302			Units
		Min	Тур	Max	Min	Туре	Max	UiillS
Input Offset Voltage	$T_A = 25^{\circ}C$		2	5		5	15	mV
Input Bias Current	$T_A = 25^{\circ}C$		3	10		10	30	nA
Input Resistance	$T_A = 25^{\circ}C$	10 ¹⁰	10 ¹²		10 ⁹	10 ¹²		Ω
Input Capacitance				3.0		3.0		pF
Large Signal Voltage Gain	$T_A = 25$ °C, $V_S \pm 15V$, $V_{OUT} = \pm 10V$, $R_L = 8 \text{ k}\Omega$	0.999	0.9996		0.9985	0.9995	1.0	V/V
Output Resistance	$T_A = 25^{\circ}C$		0.8	2.5		0.8	2.5	Ω
Supply Current	$T_A = 25^{\circ}C$		3.5	5.5		3.5	5.5	mA
Input Offset Voltage				7.5			20	mV
Offset Voltage Temperature Drift			6			20		μV/°C
Input Bias Current	$T_A = T_A MAX$ $T_A = T_A MIN$		3 30	10 100		3.0 20	15 50	nA nA
Large Signal Voltage Gain	$V_S = \pm 15V, V_{OUT} = \pm 10V,$ $R_L = 10 \text{ k}\Omega$	0.999						
Output Voltage Swing	$V_S=\pm 15 V, R_L=10 \text{ k}\Omega$ (Note 5)	±10			±10			V
Supply Current	T _A = 125°C		2.6	4.0				mA
Supply Voltage Rejection Ratio	$\pm 12V \le V_{\mbox{\scriptsize S}} \le \pm 15V$	60			60			dB

Note 1: The maximum junction temperature of the LM102 is 150°C, while that of the LM302 is 85°C. For operating at elevated temperatures, devices in the H08 package must be derated based on a thermal resistance of 150°C/W, junction to ambient, or 20°C/W, junction to case.

Note 2: For supply voltages less than ± 15 V, the absolute maximum input voltage is equal to the supply voltage.

Note 3: It is necessary to insert a resistor (at least 5k and preferably 10k) in series with the input pin when the amplifier is driven from low impedance sources to prevent damage when the output is shorted and to ensure stability.

Note 4: These specifications apply for $\pm 12\text{V} \le \text{V}_\text{S} \le \pm 15\text{V}$ and $-55^\circ\text{C} \le \text{T}_\text{A} \le 125^\circ\text{C}$ for the LM102 and $0^\circ\text{C} \le \text{T}_\text{A} \le 70^\circ\text{C}$ for the LM302 unless otherwise specified.

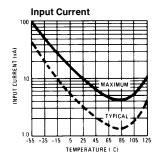
Note 5: Increased output swing under load can be obtained by connecting an external resistor between the booster and V - terminals. See curve.

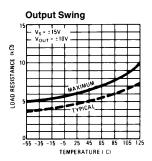
Note 6: Refer to RETS102X for the LM102H military specifications.

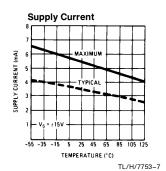
APPLICATION HINT

The input must be driven from a source impedance of typically 10 k Ω (5 k Ω Min) to maintain stability. The total source impedance will be reduced at high frequencies if there is stray capacitance at the input pin. In these cases, a 10 k Ω resistor should be inserted in series with the input, physically close to the input pin to minimize the stray capacitance and prevent oscillation.

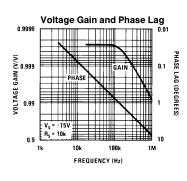
Guaranteed Performance Characteristics LM102

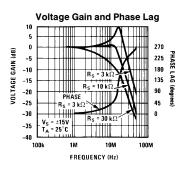


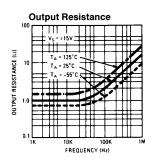


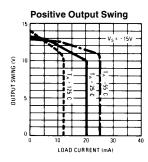


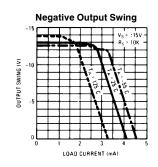
Typical Performance Characteristics LM102

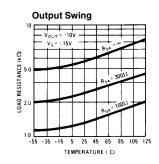


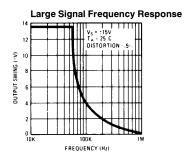


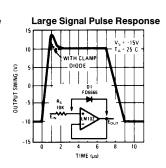


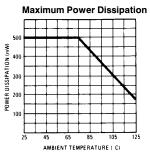




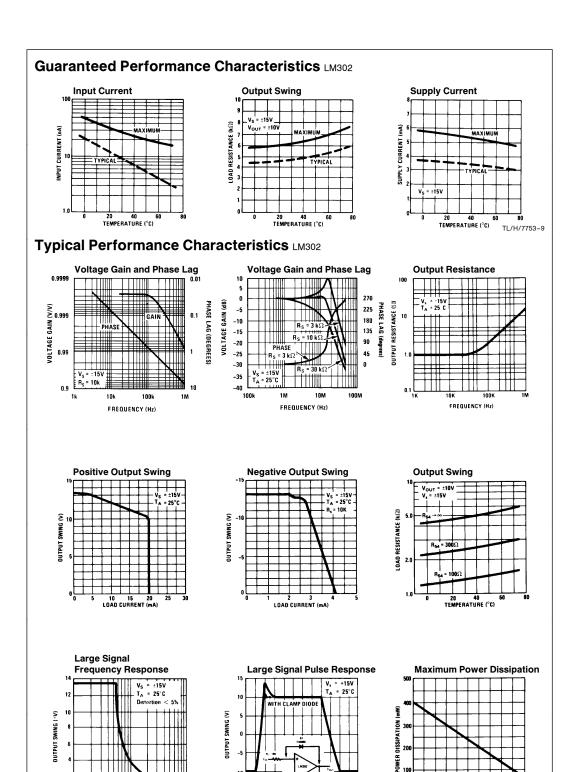






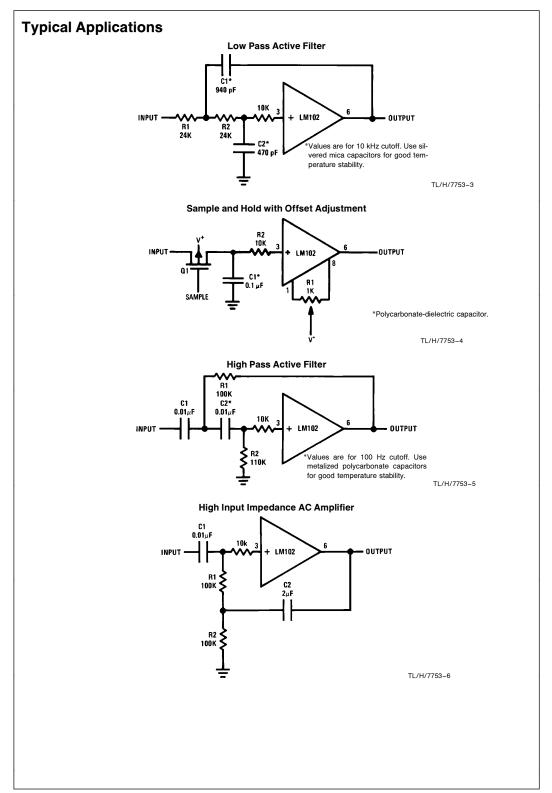


TL/H/7753-8



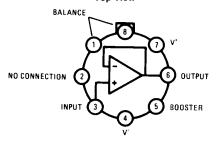
100K FREQUENCY (Hz) 35 45 55 65 AMBIENT TEMPERATURE (°C)

TL/H/7753-10



Connection Diagram

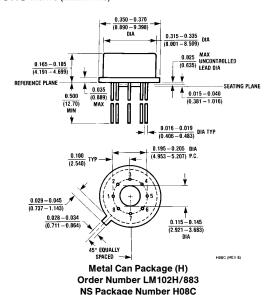
Metal Can Package **Top View**



TL/H/7753-2

Order Number LM102H/883 See NS Package Number H08C

Physical Dimensions inches (millimeters)



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National Semiconductor National Semiconductor Corporation 1111 West Bardin Road Arlington, TX 76017 Tel: 1(800) 272-9959 Fax: 1(800) 737-7018

National Semiconductor Europe

Fax: (+49) 0-180-530 85 86 Fax: (+49) U-18U-35U oo oo Email: onjwege tevm2.nsc.com Deutsch Tel: (+49) 0-180-530 85 85 English Tei: (+49) 0-180-532 78 32 Français Tel: (+49) 0-180-532 93 58 Italiano Tel: (+49) 0-180-534 16 80 **National Semiconductor** Hong Kong Ltd.
13th Floor, Straight Block,
Ocean Centre, 5 Canton Rd.

Tsimshatsui, Kowloon Hong Kong Tel: (852) 2737-1600 Fax: (852) 2736-9960

National Semiconductor

Japan Ltd.
Tel: 81-043-299-2309
Fax: 81-043-299-2408