

June 1989

5474/DM5474/DM7474 **Dual Positive-Edge-Triggered D Flip-Flops** with Preset, Clear and Complementary Outputs

General Description

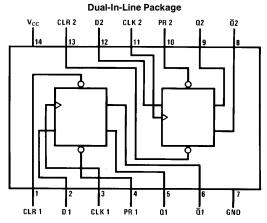
This device contains two independent positive-edge-triggered D flip-flops with complementary outputs. The information on the D input is accepted by the flip-flops on the positive going edge of the clock pulse. The triggering occurs at a voltage level and is not directly related to the transition time of the rising edge of the clock. The data on the D input may be changed while the clock is low or high without affecting the outputs as long as the data setup and hold times are not

violated. A low logic level on the preset or clear inputs will set or reset the outputs regardless of the logic levels of the other inputs.

Features

■ Alternate Military/Aerospace device (5474) is available. Contact a National Semiconductor Sales Office/Distributor for specifications.

Connection Diagram



TL/F/6526-1

Order Number 5474DMQB, 5474FMQB, DM5474J, DM5474W, DM7474M or DM7474N See NS Package Number J14A, M14A, N14A or W14B

Function Table

Inputs				Outputs		
PR	CLR	CLK	D	Q	Q	
L	Н	Х	Х	Н	L	
Н	L	Х	Х	L	Н	
L	L	Х	Х	H*	H*	
Н	Н	↑	Н	Н	L	
Н	Н	1	L	L	Н	
Н	Н	L	X	Q_0	\overline{Q}_0	

- H = High Logic LevelX = Either Low or High Logic Level
- = Low Logic Level
- 1 = Positive-going transition of the clock.
- = This configuration is nonstable; that is, it will not persist when either the preset and/or clear inputs return to their inactive (high) level.
- Q₀ = The output logic level of Q before the indicated input conditions were established.

Absolute Maximum Ratings (Note)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/Distributors for availability and specifications.

Supply Voltage Input Voltage 5.5V Operating Free Air Temperature Range

DM54 and 54 -55°C to +125°C DM74 0°C to +70°C

-65°C to +150°C Storage Temperature Range

Note: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Recommended Operating Conditions

Symbol	Parameter		DM5474			DM7474			Units
Symbol			Min	Nom	Max	Min	Nom	Max	Cinto
V _{CC}	Supply Voltage		4.5	5	5.5	4.75	5	5.25	٧
V _{IH}	High Level Input	Voltage	2			2			V
V _{IL}	Low Level Input Voltage				0.8			0.8	V
I _{OH}	High Level Output Current				-0.4			-0.4	mA
l _{OL}	Low Level Output Current				16			16	mA
f _{CLK}	Clock Frequency (Note 2)		0		15	0		15	MHz
t _W	t _W Pulse Width (Note 2)	Clock High	30			30			ns
		Clock Low	37			37			
		Clear Low	30			30			
		Preset Low	30			30			
t _{SU}	Input Setup Time (Notes 1 & 2)		20 ↑			20 ↑			ns
t _H	Input Hold Time (Notes 1 & 2)		5↑			5↑			ns
T _A	Free Air Operating Temperature		-55		125	0		70	°C

Note 1: The symbol (\uparrow) indicates the rising edge of the clock pulse is used for reference.

Note 2: $T_A = 25^{\circ}C$ and $V_{CC} = 5V$.

Electrical Characteristics over recommended operating free air temperature range (unless otherwise noted)

Symbol	Parameter	Condition	ons	Min	Typ (Note 3)	Max	Units
VI	Input Clamp Voltage	V _{CC} = Min, I _I =	- 12 mA			-1.5	V
V _{OH}	High Level Output Voltage	$V_{CC} = Min, I_{OH}$ $V_{IL} = Max, V_{IH}$		2.4	3.4		٧
V _{OL}	Low Level Output Voltage	$V_{CC} = Min, I_{OL}$ $V_{IH} = Min, V_{IL}$			0.2	0.4	V
II	Input Current @ Max Input Voltage	$V_{CC} = Max, V_I$	= 5.5V			1	mA
I _{IH}	High Level Input Current	$V_{CC} = Max$ $V_{I} = 2.4V$	D			40	μΑ
			Clock			80	
			Clear			120	
			Preset			40	
IIL	Low Level Input Current	$V_{CC} = Max$ $V_I = 0.4V$ (Note 6)	D			-1.6	- mA
			Clock			-3.2	
			Clear			-3.2	
			Preset			-1.6	
los	Short Circuit Output Current	V _{CC} = Max (Note 4)	DM54	-20		-55	mA
			DM74	-18		-55	
Icc	Supply Current	V _{CC} = Max (No	ote 5)		17	30	mA

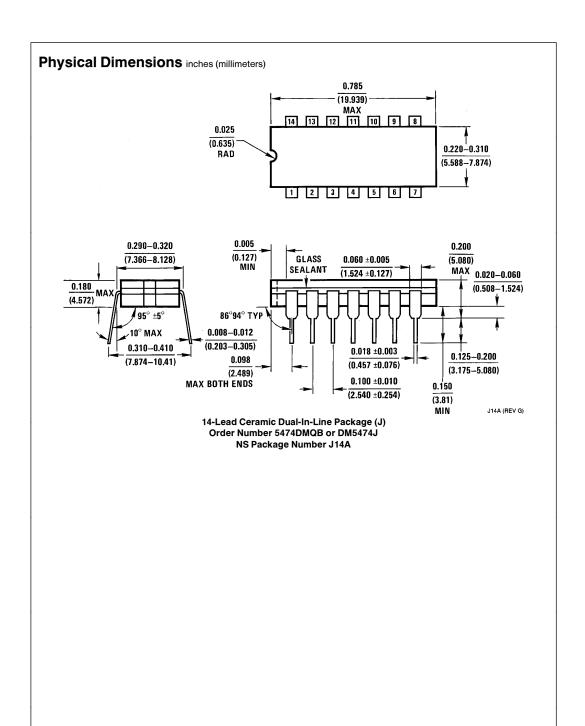
Note 3: All typicals are at $V_{CC}=5V$, $T_A=25^{\circ}C$.

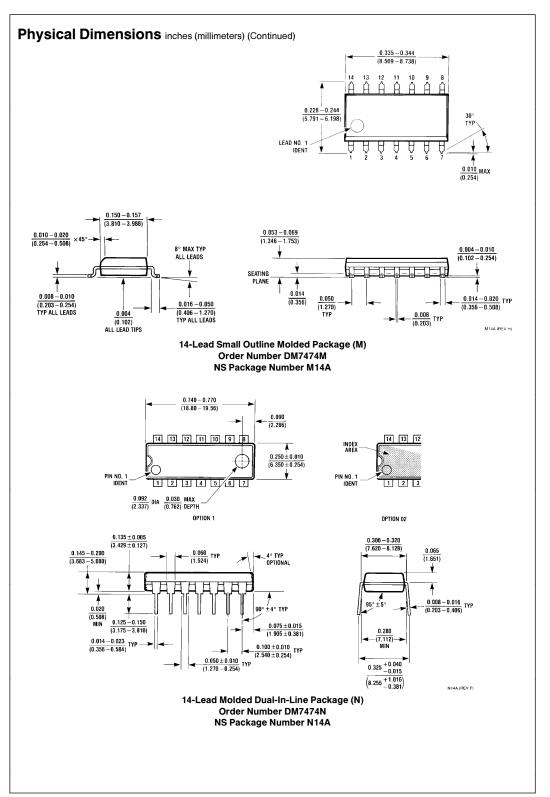
Note 4: Not more than one output should be shorted at a time.

 $\textbf{Note 5:} \ \ \textbf{With all outputs open, I}_{CC} \ \ \textbf{is measured with the Q and } \ \overline{\textbf{Q}} \ \ \textbf{outputs high in turn.} \ \ \textbf{At the time of measurement the clock is grounded.}$

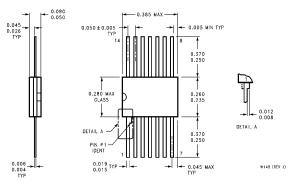
Note 6: Clear is tested with preset high and preset is tested with clear high.

Symbol	Parameter	From (Input) To (Output)	R _L = C _L =	Units	
			Min	Max	
f _{MAX}	Maximum Clock Frequency		15		MHz
t _{PHL}	Propagation Delay Time High to Low Level Output	Preset to $\overline{\mathbb{Q}}$		40	ns
t _{PLH}	Propagation Delay Time Low to High Level Output	Preset to Q		25	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	Clear to Q		40	ns
t _{PLH}	Propagation Delay Time Low to High Level Output	Clear to Q		25	ns
t _{PHL}	Propagation Delay Time High to Low Level Output	Clock to Q or Q		40	ns
t _{PLH}	Propagation Delay Time Low to High Level Output	Clock to Q or Q		25	ns





Physical Dimensions inches (millimeters) (Continued)



14-Lead Ceramic Flat Package (W) Order Number 5474FMQB or DM5474W NS Package Number W14B

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