.M710 Voltage Comparator February 1995

LM710

## National Semiconductor

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## LM710 **Voltage Comparator**

### **General Description**

The LM710 series are high-speed voltage comparators intended for use as an accurate, low-level digital level sensor or as a replacement for operational amplifiers in comparator applications where speed is of prime importance. The circuit has a differential input and a single-ended output, with saturated output levels compatible with practically all types of integrated logic.

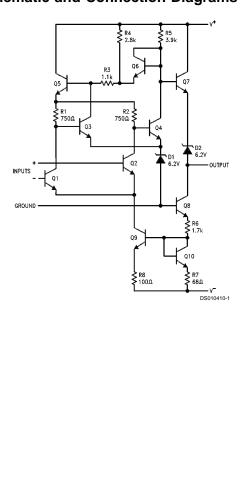
The device is built on a single silicon chip which insures low offset and thermal drift. The use of a minimum number of stages along with minority-carrier lifetime control (gold doping) makes the circuit much faster than operational amplifi-

Schematic and Connection Diagrams

R5 3.9k OF R3 <u>م</u> R? 750.0 6.2V 02 OUTPUT INPLIES GROUND 09 010 000 DS010410-1

ers in saturating comparator applications. In fact, the low stray and wiring capacitances that can be realized with monolithic construction make the device difficult to duplicate with discrete components operating at equivalent power levels.

The LM710 series are useful as pulse height discriminators, voltage comparators in high-speed A/D converters or go, no-go detectors in automatic test equipment. They also have applications in digital systems as an adjustable-threshold line receiver or an interface between logic types. In addition, the low cost of the units suggests them for applications replacing relatively simple discrete component circuitry.





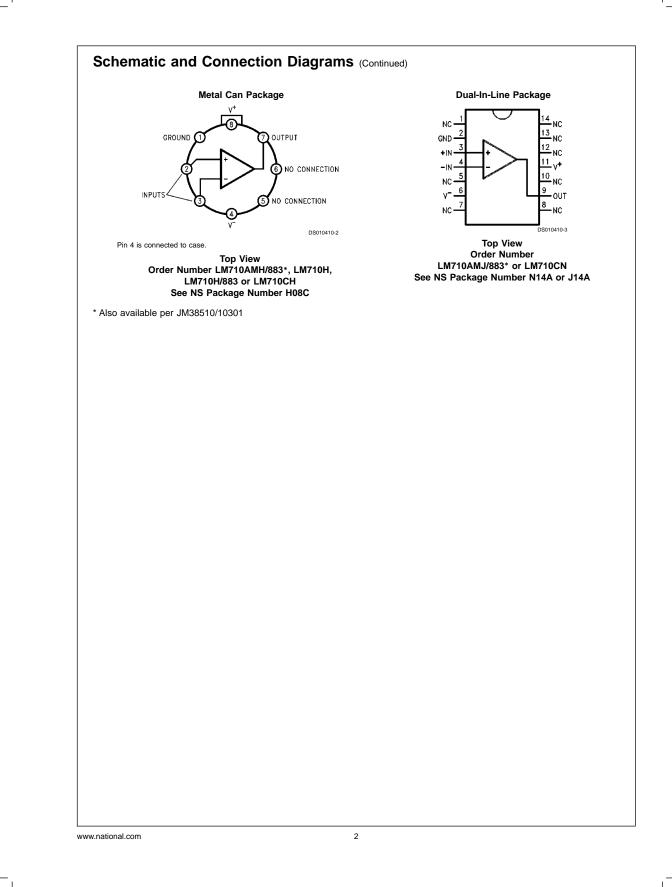


\* Also available per JM38510/10301

Order Number LM710AMW/883\* See NS Package Number W10A

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# Absolute Maximum Ratings (Note \*NO TARGET FOR FNXref NS0053\*)

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If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

Positive Supply Voltage	+14V
Negative Supply Voltage	-7V
Peak Output Current	10 mA
Output Short Circuit Duration	10 seconds
Differential Input Voltage	±5V
Input Voltage	±7V

Power Dissipation	
TO-99 (Note 1)	700 mW
Plastic Dual-In-Line Package	
(Note 2)	950 mW
Operating Temperature Range	
LM710	–55°C to +125°C
LM710C	0°C to +70°C
Storage Temperature Range	-65°C to +150°C
Lead Temperature	
(Soldering, 10 sec.)	260°C

## Electrical Characteristics (Note 3)

Parameter	Conditions	LM710			LM710C			Units
		Min	Тур	Max	Min	Тур	Max	1
Input Offset Voltage	$R_{S} \leq 200\Omega, V_{CM} = 0V, T_{A} = 25^{\circ}C$		0.6	2.0		1.6	5.0	mV
Input Offset Current	$V_{OUT} = 1.4V, T_A = 25^{\circ}C$		0.75	3.0		1.8	5.0	μA
Input Bias Current	T <sub>A</sub> = 25°C		13	20		16	25	μA
Voltage Gain	$T_A = 25^{\circ}C$	1250	1700		1000	1500		
Output Resistance	$T_A = 25^{\circ}C$		200			200		Ω
Output Sink Current	$V_{OUT} = 0, T_A = 25^{\circ}C$							
	$\Delta V_{IN} \ge 5 \text{ mV}$	2.0	2.5					mA
	$\Delta V_{IN} \ge 10 \text{ mV}$				1.6	2.5		mA
Response Time	$T_A = 25^{\circ}C$ (Note 4)		40			40		ns
Input Offset Voltage	$R_{S} \leq 200\Omega, V_{CM} = 0V$			3.0			6.5	mV
Average Temperature Coefficient	$T_{MIN} \le T_A \le T_{MAX}$		3.0	10		5.0	20	µV/°C
of Input Offset Voltage	$R_{\rm S} \le 50\Omega$							
Input Offset Current	$T_A = T_{A MAX}$		0.25	3.0			7.5	μA
	$T_A = T_A MIN$		1.8	7.0			7.5	μA
Average Temperature Coefficient	$25^{\circ}C \leq T_A \leq T_{MAX}$		5.0	25		15	50	nA/°C
of Input Offset Current	$T_{MIN} \le T_A \le 25^{\circ}C$		15	75		24	100	nA/°C
Input Bias Current	$T_A = T_{MIN}$		27	45		25	40	μA
Input Voltage Range	$V^- = -7V$	±5.0			±5.0			V
Common-Mode Rejection Ratio	$R_{S} \leq 200\Omega$	80	100		70	98		dB
Differential Input Voltage Range		±5.0			±5.0			V
Voltage Gain		1000			800			V/V
Positive Output Level	$-5 \text{ mA} \le I_{OUT} \le 0$							
	$V_{IN} \ge 5 \text{ mV}$	2.5	3.2	4.0				v
	$V_{IN} \ge 10 \text{ mV}$				2.5	3.2	4.0	v
Negative Output Level	$V_{IN} \ge 5 \text{ mV}$	-1.0	-0.5	0				V
5	$V_{IN} \ge 10 \text{ mV}$				-1.0	-0.5	0	v
Output Sink Current	$V_{\rm IN} \ge 5  \rm mV,  V_{\rm OUT} = 0$							
	$T_{A} = 125^{\circ}C$	0.5	1.7					mA
	$T_A = -55^{\circ}C$	1.0	2.3					mA
	$V_{IN} \ge 10 \text{ mV}, V_{OUT} = 0$	-			0.5			mA
	$0^{\circ}C \le T_{A} \le +70^{\circ}C$							
Positive Supply Current	$V_{IN} \ge 5 \text{ mV}$		5.2	9.0				mA
	$V_{IN} \ge 10 \text{ mV}$					5.2	9.0	mA
Negative Supply Current	$V_{IN} \ge 5 \text{ mV}$		4.6	7.0				mA
5	$V_{IN} \ge 10 \text{ mV}$		-			4.6	7.0	mA

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Electrical Characteristics (Note 3) (Continued)								
Parameter	Conditions		LM710			LM710C		
		Min	Тур	Max	Min	Тур	Max	1
Power Consumption	$I_{OUT} = 0$							
	$V_{IN} \ge 5 \text{ mV}$		90	150				mW
	$V_{IN} \ge 10 \text{ mV}$						150	mW

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Note 1: Rating applies for ambient temperatures of 25°C; derate linearly at 5.6 mW/°C for ambient temperatures above 25°C.

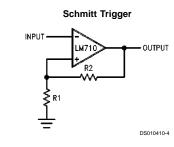
Note 2: Derate linearly at 9.5 mW/°C for ambient temperatures above 25°C.

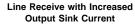
Note 3: These specifications appy for V<sup>+</sup> = 12V, V<sup>-</sup> = -6V,  $-55^{\circ}C \le T_A \le +125^{\circ}C$  for LM710 and  $0^{\circ}C \le T_A \le +70^{\circ}C$  for LM710C unless otherwise specified: The input offset voltage and input offset current (see definitions) are specified for a logic threshold voltage of 1.8V at  $-55^{\circ}C$ , 1.4V at 25<sup>o</sup>C, and 1V at 125<sup>o</sup>C for LM710 and 1.5V at 0^{\circ}C, 1.4V at 25<sup>o</sup>C, and 1.2V at 70<sup>o</sup>C for LM710C.

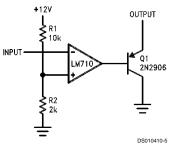
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Note 4: The response time specified (see definitions) is for a 100 mV input step with 5 mV overdrive (LM710) or a 10 mV overdrive (LM710C).

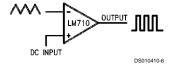
## **Typical Applications**

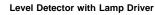


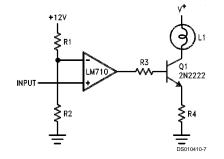




Pulse Width Modulator

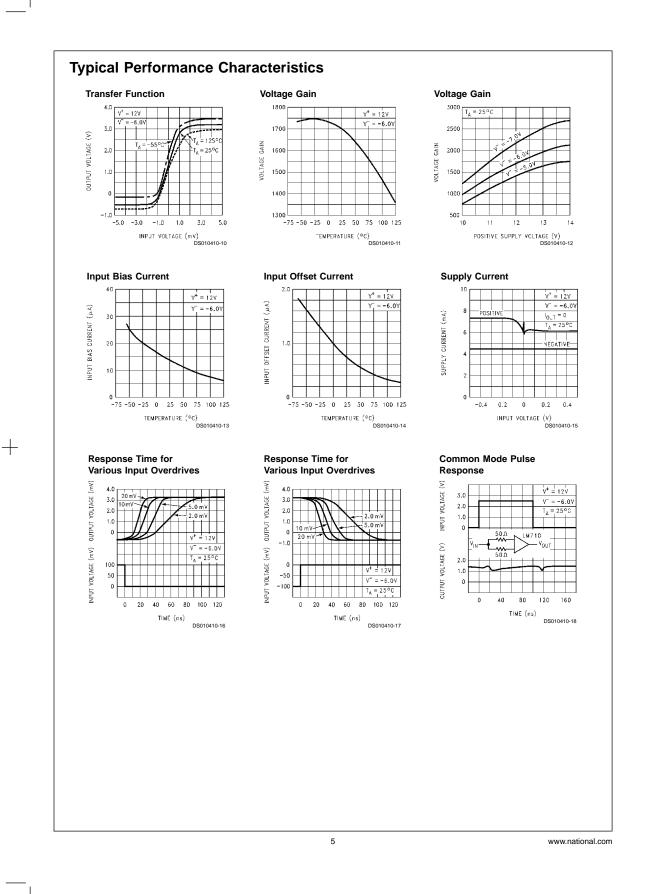


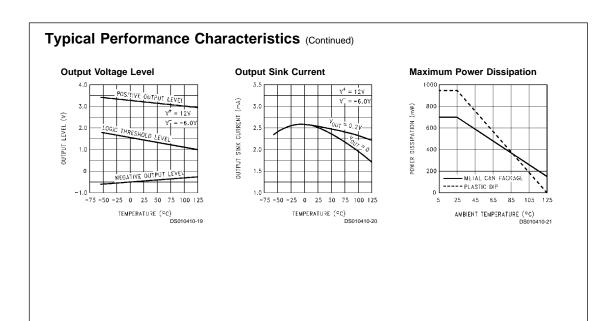




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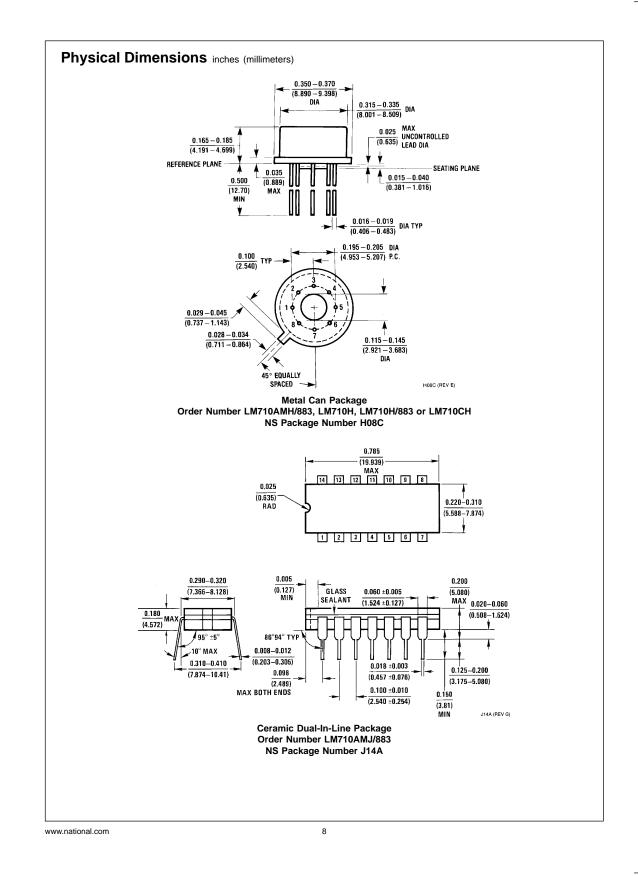
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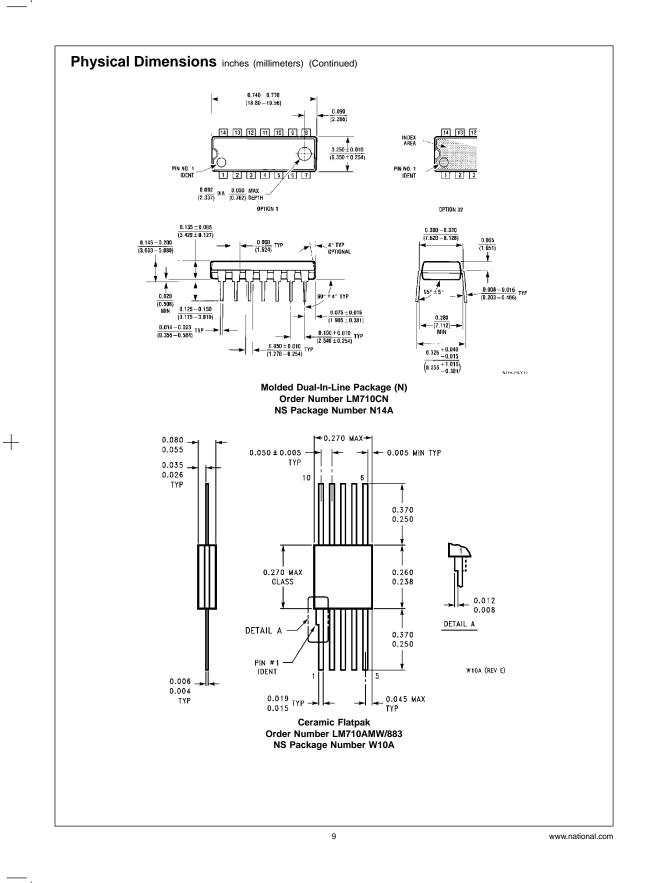
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National Semiconductor Corporation 1111 West Bardin Road Arlington, TX 76017 Tel: 1(800) 272-9959 Fax: 1(800) 737-7018

#### National Semiconductor

 Europe
 Fax:
 (+49) 0-180-530 85 86

 Email:
 cnjwge@tevm2.nsc.com

 Deutsch
 Tel:
 (+49) 0-180-530 85 85

 English
 Tel:
 (+49) 0-180-532 78 32

 Français
 Tel:
 (+49) 0-180-532 93 58

 Italiano
 Tel:
 (+49) 0-180-534 16 80

National Semiconductor Hong Kong Ltd. 13th Floor, Straight Block, Ocean Centre, 5 Canton Rd. Tsimshatsui, Kowloon Hong Kong Tei: (852) 2737-1600 Fax: (852) 2736-9960 National Semiconductor Japan Ltd. Tel: 81-043-299-2308 Fax: 81-043-299-2408

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