The RF Line Microwave Pulse Power Transistors

Designed for Class B and C common base amplifier applications in short pulse TACAN, IFF, and DME transmitters.

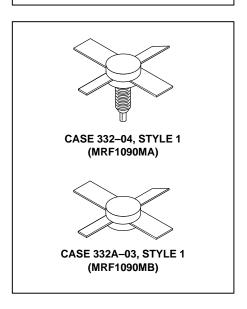
- Guaranteed Performance @ 1090 MHz, 50 Vdc Output Power = 90 Watts Peak Minimum Gain = 8.4 dB
- 100% Tested for Load Mismatch at All Phase Angles with 10:1 VSWR
- Industry Standard Package
- Nitride Passivated
- Gold Metallized for Long Life and Resistance to Metal Migration
- Internal Input Matching for Broadband Operation
- Circuit board photomaster available upon request by contacting RF Tactical Marketing in Phoenix, AZ.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Base Voltage	VCBO	70	Vdc
Emitter–Base Voltage	VEBO	4.0	Vdc
Collector-Current — Peak (1)	IC	6.0	Adc
Total Device Dissipation @ T _C = 25°C (1) (2) Derate above 25°C	PD	290 1.66	Watts W/°C
Storage Temperature Range	T _{stg}	-65 to +150	°C

MRF1090MA MRF1090MB

90 W PEAK, 960-1215 MHz MICROWAVE POWER TRANSISTORS NPN SILICON



THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case (3)	$R_{\theta JC}$	0.6	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
OFF CHARACTERISTICS	•				
Collector–Emitter Breakdown Voltage (I _C = 25 mAdc, V _{BE} = 0)	V(BR)CES	70	_	_	Vdc
Collector–Base Breakdown Voltage (I _C = 25 mAdc, I _E = 0)	V(BR)CBO	70	_	_	Vdc
Emitter–Base Breakdown Voltage (I _E = 5.0 mAdc, I _C = 0)	V(BR)EBO	4.0	_	_	Vdc
Collector Cutoff Current (V _{CB} = 50 Vdc, I _E = 0)	I _{CBO}	_	_	5.0	mAdc
ON CHARACTERISTICS	•				
DC Current Gain (4) (I _C = 2.5 Adc, V _{CE} = 5.0 Vdc)	hFE	10	30	_	_

NOTES:

(continued)

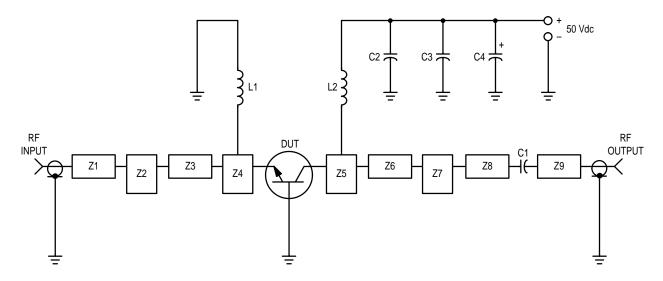
- 1. Pulse Width = 10 μ s, Duty Cycle = 1%.
- 2. These devices are designed for RF operation. The total device dissipation rating applies only when the device is operated as RF amplifiers.
- 3. Thermal Resistance is determined under specified RF operating conditions by infrared measurement techniques.
- 4. 80 μs Pulse on Tektronix 576 or equivalent.

REV 8



ELECTRICAL CHARACTERISTICS — **continued** ($T_C = 25^{\circ}C$ unless otherwise noted)

Characteristic	Symbol	Min	Тур	Max	Unit
DYNAMIC CHARACTERISTICS					
Output Capacitance (V _{CB} = 50 Vdc, I _E = 0, f = 1.0 MHz)	C _{ob}	_	12	16	pF
FUNCTIONAL TESTS (Pulse Width = 10 μs, Duty Cycle = 1.0%)					
Common–Base Amplifier Power Gain (V _{CC} = 50 Vdc, P _{out} = 90 W pk, f = 1090 MHz)	GPB	8.4	10.8	_	dB
Collector Efficiency (V _{CC} = 50 Vdc, P _{out} = 90 W pk, f = 1090 MHz)	η	35	40	_	%
Load Mismatch (V _{CC} = 50 Vdc, P _{out} = 90 W pk, f = 1090 MHz, VSWR = 10:1 All Phase Angles)	Ψ	No Degradation in Power Output			



C1, C2 — 220 pF Chip Capacitor, 100–mil ATC C3 — 0.1 μF C4 — 47 $\mu\text{F}/75$ V

L1, L2 — 3 Turns #18 AWG, 1/8" ID

Z1-Z9 — Distributed Microstrip Elements,

See Photomaster

Board Material — 0.031" Thick Glass Teflon, ε_r = 2.5

Figure 1. 1090 MHz Test Circuit

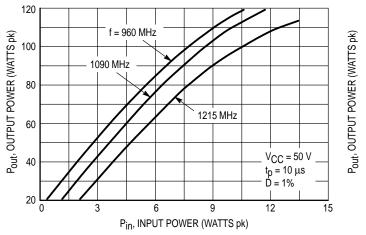


Figure 2. Output Power versus Input Power

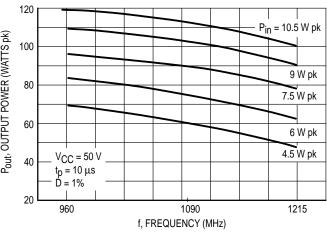


Figure 3. Output Power versus Frequency

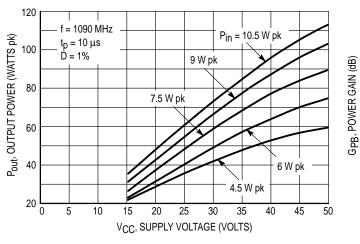
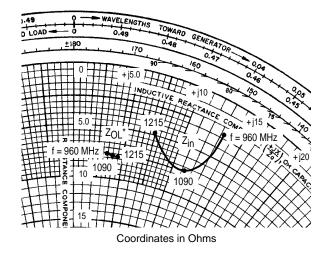


Figure 4. Output Power versus Supply Voltage

Figure 5. Power Gain versus Frequency



 $P_{out} = 90 \text{ W pk}$ $V_{CC} = 50 \text{ V}$ $t_p = 10 \text{ } \mu\text{s}$ D = 1%

F			
f	Z _{in}	Z _{OL} *	
MHz	Ohms	Ohms	
960	2.8 + j13.2	7.6 + j3.5	
1090	7.4 + j11.4	7.6 + j4.0	
1215	4.7 + j7.5	7.7 + j4.5	

Z_{OL}* = Conjugate of the optimum load impedance into which the device output operates at a given output power, voltage, and frequency.

Figure 6. Series Equivalent Input/Output Impedance

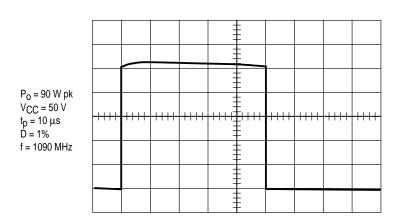
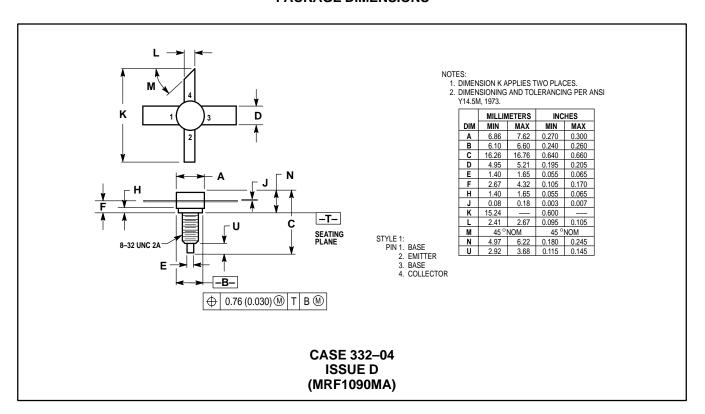
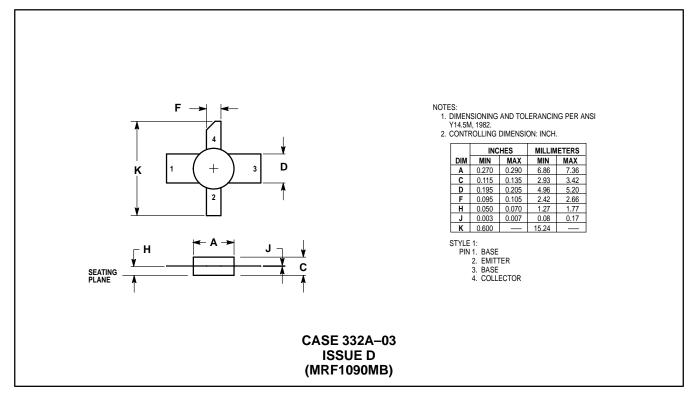


Figure 7. Typical Pulse Performance

PACKAGE DIMENSIONS





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