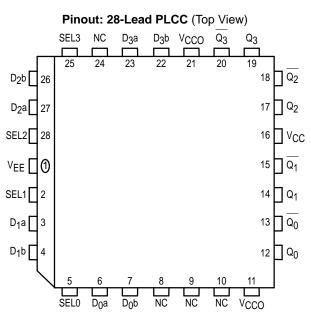
Quad 2:1 Multiplexer

The MC10E/100E157 contains four 2:1 multiplexers with differential outputs. The output data are controlled by the individual Select (SEL) inputs. The individual select control makes the devices well suited for random logic designs.

- Individual Select Controls
- 550ps Max. D to Output
- 800ps Max. SEL to Output
- Extended 100E VEE Range of 4.2V to 5.46V
- Internal 75kΩ Input Pulldown Resistors

MC10E157 MC100E157

QUAD 2:1 MULTIPLEXER



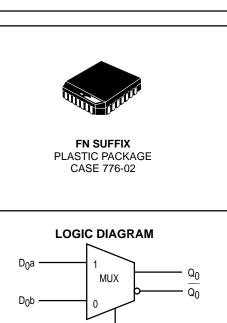
 * All V_CC and V_CCO pins are tied together on the die.

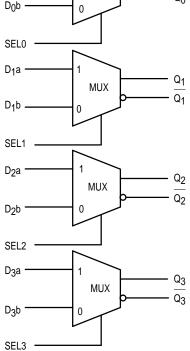
PIN NAMES

Pin	Function							
D ₀ a – D ₃ a	Input Data a							
$D_0b - D_3b$	Input Data b							
SEL0 – SEL3	Select Inputs							
$\underline{Q}_0 - \underline{Q}_3$	True Outputs							
Q ₀ – Q ₃	Inverted Outputs							

TRUTH TABLE

SEL	Data
Н	а
L	b







MC10E157 MC100E157

DC CHARACTERISTICS (V_{EE} = V_{EE}(min) to V_{EE}(max); V_{CC} = V_{CCO} = GND)

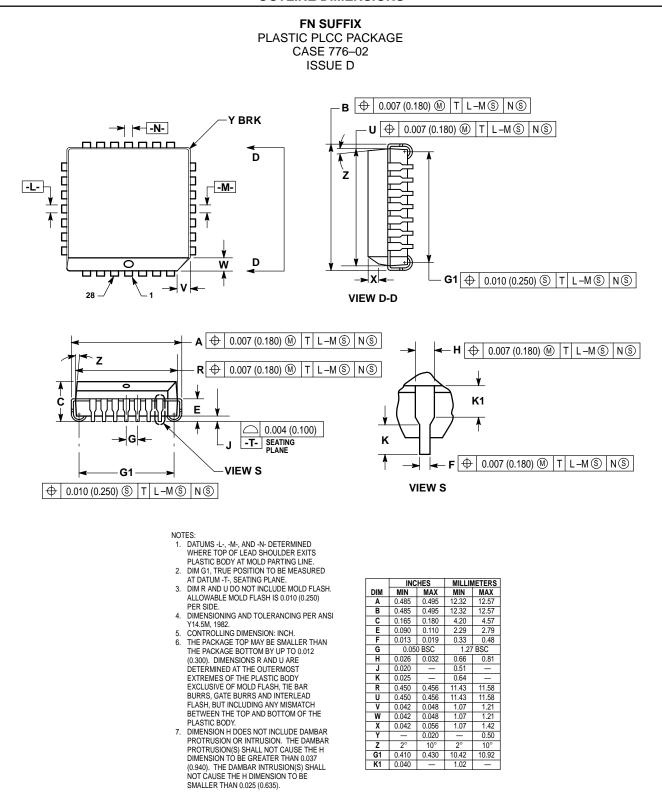
		0°C		25°C			85°C					
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
Ιн	Input HIGH Current										μA	
	D SEL			200 150			200 150			200 150		
IEE	Power Supply Current										mA	
	10E		32	38		32	38		32	38		
	100E		32	38		32	38		37	44		

AC CHARACTERISTICS ($V_{EE} = V_{EE}(min)$ to $V_{EE}(max)$; $V_{CC} = V_{CCO} = GND$)

		0°C			25°C			85°C				
Symbol	Characteristic	min	typ	max	min	typ	max	min	typ	max	Unit	Condition
^t PLH ^t PHL	Propagation Delay to Output D SEL	220 425	380 600	550 800	220 425	380 600	550 800	220 425	380 600	550 800	ps	
^t SKEW	Within-Device Skew		70			70			70		ps	1
t _r t _f	Rise/Fall Times 20 - 80%	275	400	650	275	400	650	275	400	650	ps	

1. Within-device skew is defined as identical transitions on similar paths through a device.





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How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. 1–800–441–2447 or 602–303–5454

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MFAX: RMFAX0@email.sps.mot.com - TOUCHTONE 602-244-6609 INTERNET: http://Design-NET.com JAPAN: Nippon Motorola Ltd.; Tatsumi–SPD–JLDC, 6F Seibu–Butsuryu–Center, 3–14–2 Tatsumi Koto–Ku, Tokyo 135, Japan. 03–81–3521–8315

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852–26629298



