Triacs

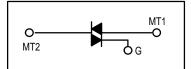
Silicon Bidirectional Thyristors

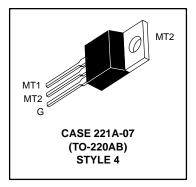
... designed primarily for full-wave ac control applications, such as light dimmers, motor controls, heating controls and power supplies; or wherever full-wave silicon gate controlled solid-state devices are needed. Triac type thyristors switch from a blocking to a conducting state for either polarity of applied anode voltage with positive or negative gate triggering.

- Blocking Voltage to 800 Volts
- All Diffused and Glass Passivated Junctions for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Gate Triggering Guaranteed in Four Modes

MAC212A Series

TRIACs 12 AMPERES RMS 600 thru 800 VOLTS





MAXIMUM RATINGS ($T_J = 25^{\circ}C$ unless otherwise noted.)

Rating	Symbol	Value	Unit
Repetitive Peak Off-State Voltage ⁽¹⁾ (T _J = -40 to +125°C, 1/2 Sine Wave 50 to 60 Hz, Gate Open)	VDRM		Volts
MAC212A8 MAC212A10		600 800	
On-State Current RMS (T _C = +85°C) Full Cycle Sine Wave 50 to 60 Hz	^I T(RMS)	12	Amp
Peak Non-repetitive Surge Current (One Full Cycle, 60 Hz, T _C = +85°C) preceded and followed by Rated Current	ITSM	100	Amp
Circuit Fusing Considerations (t = 8.3 ms)	I ² t	40	A ² S
Peak Gate Power ($T_C = +85^{\circ}C$, Pulse Width = 10 μ s)	PGM	20	Watts
Average Gate Power (T _C = +85°C, t = 8.3 ms)	P _{G(AV)}	0.35	Watt
Peak Gate Current (T _C = +85°C, Pulse Width = 10 μs)	I _{GM}	2	Amp
Operating Junction Temperature Range	TJ	-40 to +125	°C
Storage Temperature Range	T _{stg}	-40 to +150	°C

^{1.} V_{DRM} for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.



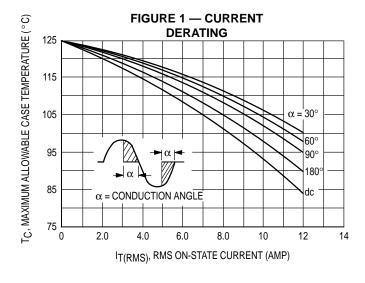
MAC212A Series

THERMAL CHARACTERISTICS

Symbol	Parameter	Value	Unit
R _θ JC R _θ JA	Thermal Resistance — Junction to Case — Junction to Ambient	2.0 62.5	°C/W
TL	Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 10 Seconds	260	°C

ELECTRICAL CHARACTERISTICS ($T_C = 25$ °C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak Blocking Current (Either Direction) (VD = Rated VDRM, Gate Open) TJ = 25°C TJ = +125°C	IDRM		_	10 2	μA mA
Peak On-State Voltage (Either Direction) I _{TM} = 17 A Peak; Pulse Width = 1 to 2 ms, Duty Cycle ≤ 2%	V _{TM}	_	1.3	1.75	Volts
Gate Trigger Current (Continuous dc) (Main Terminal Voltage = 12 Vdc, R _L = 100 Ohms) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-) MT2(-), G(+)	l _{GT}	_ _ _	12 12 20 35	50 50 50 75	mA
Gate Trigger Voltage (Continuous dc) (Main Terminal Voltage = 12 Vdc, R _L = 100 Ohms) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-) MT2(-), G(-) (Main Terminal Voltage = Rated V _{DRM} , R _L = 10 k Ω , T _J = +125°C) MT2(+), G(+); MT2(-), G(-); MT2(+), G(-)	Vgт		0.9 0.9 1.1 1.4	2 2 2 2.5 —	Volts
Holding Current (Either Direction) (Main Terminal Voltage = 12 Vdc, Gate Open, Initiating Current = 500 mA)	Ιн	_	6	50	mA
Turn-On Time (V_D = Rated V_{DRM} , I_{TM} = 17 A, I_{GT} = 120 mA, Rise Time = 0.1 μ s, Pulse Width = 2 μ s)	^t gt	_	1.5	_	μs
Critical Rate of Rise of Commutation Voltage (V_D = Rated V_{DRM} , I_{TM} = 17 A, Commutating di/dt = 6.1 A/ms, Gate Unenergized, T_C = +85°C)	dv/dt _(c)	_	5	_	V/μs
Critical Rate of Rise of Off-State Voltage (V_D = Rated V_{DRM} , Exponential Voltage Rise, Gate Open, T_C = +85°C)	dv/dt		100	_	V/μs



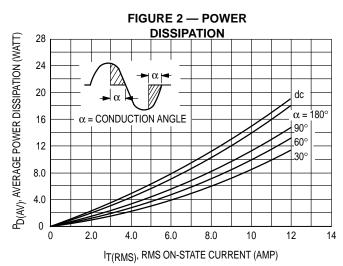


FIGURE 3 — MAXIMUM ON-STATE CHARACTERISTICS

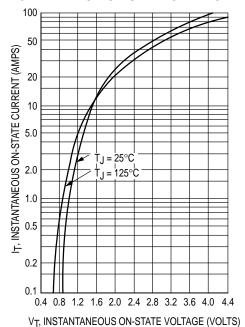
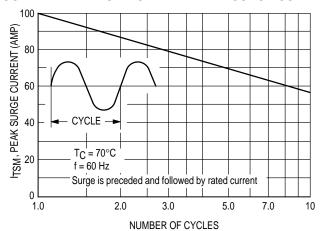
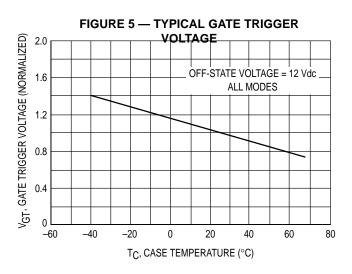
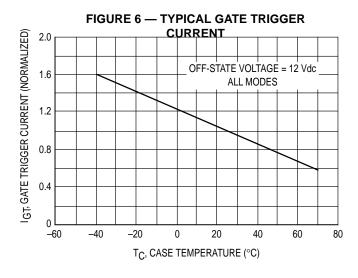


FIGURE 4 — MAXIMUM NON-REPETITIVE SURGE CURRENT







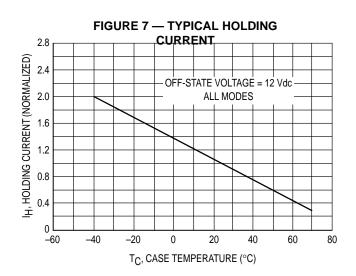
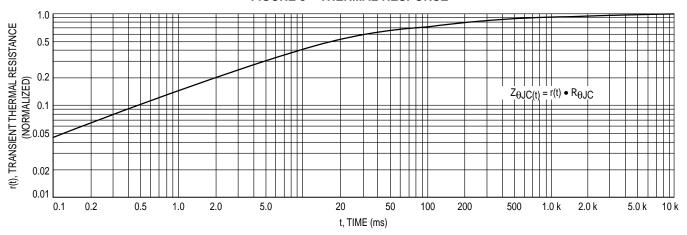
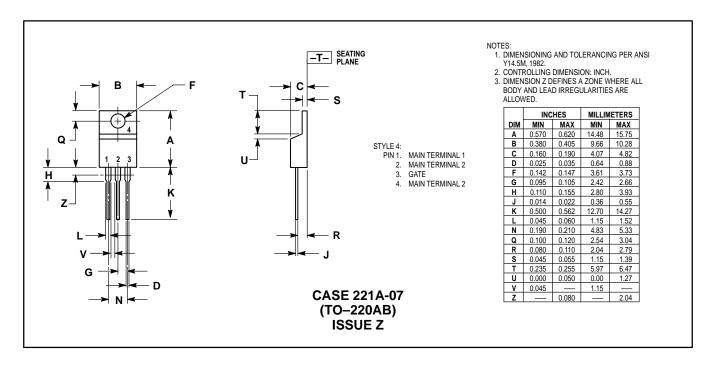


FIGURE 8 - THERMAL RESPONSE



PACKAGE DIMENSIONS



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