Advance Information

TRIACS

Silicon Bidirectional Thyristors

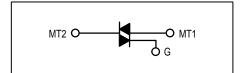
Designed primarily for full wave ac control applications, such as motor controls, heating controls or dimmers; or where ever full-wave, silicon gate-controlled devices are needed.

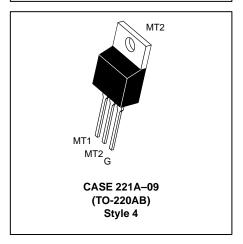
- High Commutating di/dt and High Immunity to dv/dt @ 125°C
- Minimizes Snubber Networks for Protection
- · Blocking Voltage to 800 Volts
- · On-State Current Rating of 16 Amperes RMS
- High Surge Current Capability 150 Amperes
- Industry Standard TO-220AB Package for Ease of Design
- · Glass Passivated Junctions for Reliability and Uniformity

MAC16C Series

Motorola preferred devices

TRIACS 16 AMPERES RMS 400 thru 800 VOLTS





MAXIMUM RATINGS (T_.J = 25°C unless otherwise noted)

Parameter		Symbol	Value	Unit
Peak Repetitive Off-State Voltage (1) Peak Repetitive Reverse Voltage (T _J = -40 to 125°C)	MAC16CD MAC16CM MAC16CN	VDRM VRRM	400 600 800	Volts
On-State RMS Current (All Conduction Angles; T _C = 80°C)		^I T(RMS)	16	А
Peak Non-Repetitive Surge Current (One Full Cycle, 60 Hz, T _J = 125°C)		ITSM	150	А
Circuit Fusing Consideration (t = 8.33 ms)		l ² t	93	A ² sec
Peak Gate Power (Pulse Width ≤ 1.0 μs, T _C = 80°C)		PGM	20	Watts
Average Gate Power (t = 8.3 ms, T _C = 80°C)		P _{G(AV)}	0.5	Watts
Operating Junction Temperature Range		TJ	-40 to +125	°C
Storage Temperature Range		T _{stg}	-40 to +150	°C

THERMAL CHARACTERISTICS

Thermal Resistance — Junction to Case — Junction to Ambient	R _Ð JC R _Ð JA	2.2 62.5	°C/W
Maximum Lead Temperature for Soldering Purposes 1/8" from Case for 5 Seconds	TL	260	°C

⁽¹⁾ V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

Preferred devices are Motorola recommended choices for future use and best overall value.



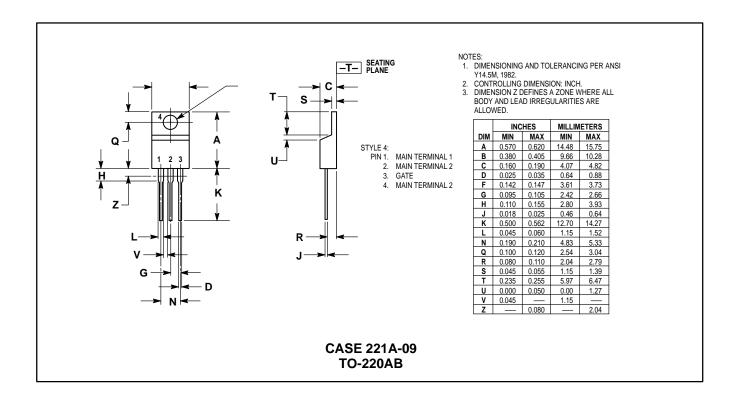
MAC16C Series

ELECTRICAL CHARACTERISTICS ($T_J = 25^{\circ}C$ unless otherwise noted)

Symbol	Characteristic	Min	Тур	Max	Unit
OFF CHA	RACTERISTICS				
^I DRM	Peak Repetitive Blocking Current (V_D = Rated V_{DRM} , Gate Open) T_J = 25°C T_J = 125°C	_ _	_ _	0.01 2.0	mA
ON CHAR	ACTERISTICS				
V _{TM}	Peak On-State Voltage ¹ (I _{TM} = ±21 A Peak)	_	_	1.6	V
I _{GT}	Continuous Gate Trigger Current (V _D = 12 V, R _L = 100 Ω) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-)	8.0 8.0 8.0	_ _ _	35 35 35	mA
lΗ	Holding Current (V _D = 12 V, Gate Open, Initiating Current = ±150 mA)	_	_	40	mA
ΙL	Latching Current ($V_D = 12 \text{ V}, I_G = 50 \text{ mA}$) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-)	_ _ _	_ _ _	60 60 60	mA
V _{GT}	Continuous Gate Trigger Voltage (V _D = 12 V, R _L = 100 Ω) MT2(+), G(+) MT2(+), G(-) MT2(-), G(-)	0.5 0.5 0.5	_ _ _	1.5 1.5 1.5	V
DYNAMIC	CHARACTERISTICS				•
(di/dt) _C	Rate of Change of Commutating Current 1 (V _D = 400 V, I _{TM} = 6.0 A, Commutating dv/dt = 20 V/ μ s, Gate Open, T _J = 125°C, f = 250 Hz, C _L = 10 μ F, L _L = 40 mH, with Snubber)	15	_	_	A/ms
dv/dt	Critical Rate of Rise of Off-State Voltage (VD = Rated VDRM, Exponential Waveform, Gate Open, TJ = 125°C)	600	_	_	V/µs
di/dt	Repetitive Critical Rate of Rise of On-State Voltage	_	_	20	A/μs

^{1.} Pulse Test: Pulse Width ≤ 2.0 ms, Duty Cycle ≤ 2%.

PACKAGE DIMENSIONS



MAC16C Series

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