MITSUBISHI Nch POWER MOSFET

FS5UMH-2

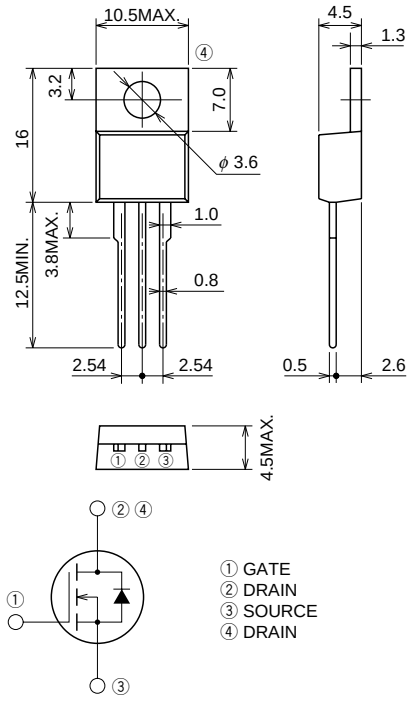
HIGH-SPEED SWITCHING USE

FS5UMH-2

- 2.5V DRIVE
- VDSS 100V
- rDS (ON) (MAX) 0.44Ω
- ID 5A
- Integrated Fast Recovery Diode (TYP.) 80ns

OUTLINE DRAWING

Dimensions in mm



TO-220

APPLICATION

Motor control, Lamp control, Solenoid control
DC-DC converter, etc.

MAXIMUM RATINGS (Tc = 25°C)

Symbol	Parameter	Conditions	Ratings	Unit
VDSS	Drain-source voltage	VGS = 0V	100	V
VGSS	Gate-source voltage	VDS = 0V	±10	V
ID	Drain current		5	A
IDM	Drain current (Pulsed)		20	A
IDA	Avalanche drain current (Pulsed)	L = 100µH	5	A
IS	Source current		5	A
ISM	Source current (Pulsed)		20	A
PD	Maximum power dissipation		20	W
Tch	Channel temperature		–55 ~ +150	°C
Tstg	Storage temperature		–55 ~ +150	°C
—	Weight	Typical value	2.0	g

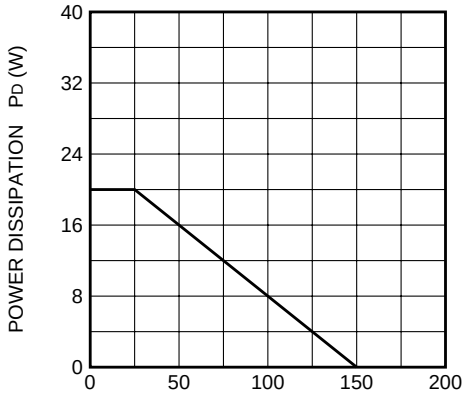
MITSUBISHI ELECTRIC

ELECTRICAL CHARACTERISTICS (Tch = 25°C)

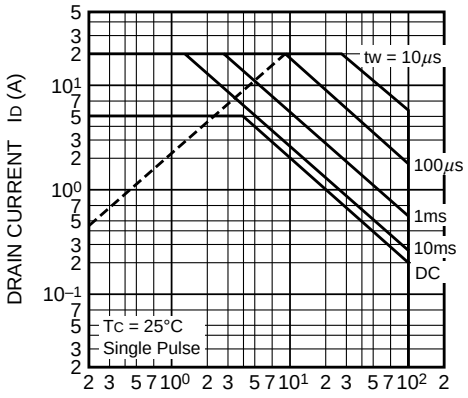
Symbol	Parameter	Test conditions	Limits			Unit
			Min.	Typ.	Max.	
$V_{(BR)DSS}$	Drain-source breakdown voltage	I_D = 1mA, V_{GS} = 0V	100	—	—	V
I_{GSS}	Gate-source leakage current	V_{GS} = ±10V, V_{DS} = 0V	—	—	±0.1	μA
I_{DSS}	Drain-source leakage current	V_{DS} = 100V, V_{GS} = 0V	—	—	0.1	mA
$V_{GS(th)}$	Gate-source threshold voltage	I_D = 1mA, V_{DS} = 10V	0.6	0.9	1.2	V
$r_{DS(ON)}$	Drain-source on-state resistance	I_D = 2A, V_{GS} = 4V	—	0.32	0.44	Ω
$r_{DS(ON)}$	Drain-source on-state resistance	I_D = 2A, V_{GS} = 2.5V	—	0.34	0.47	Ω
$V_{DS(ON)}$	Drain-source on-state voltage	I_D = 2A, V_{GS} = 4V	—	0.64	0.88	V
$\|y_{fs}\|$	Forward transfer admittance	I_D = 2A, V_{DS} = 5V	—	10	—	S
C_{iss}	Input capacitance	V_{DS} = 10V, V_{GS} = 0V, f = 1MHz	—	540	—	pF
C_{oss}	Output capacitance		—	75	—	pF
C_{rss}	Reverse transfer capacitance		—	20	—	pF
$t_{d(on)}$	Turn-on delay time	V_{DD} = 50V, I_D = 2A, V_{GS} = 4V, R_{GEN} = R_{GS} = 50Ω	—	12	—	ns
t_r	Rise time		—	18	—	ns
$t_{d(off)}$	Turn-off delay time		—	45	—	ns
t_f	Fall time		—	26	—	ns
V_{SD}	Source-drain voltage	I_S = 2A, V_{GS} = 0V	—	1.0	1.5	V
$R_{th(ch-c)}$	Thermal resistance	Channel to case	—	—	6.25	°C/W
t_{rr}	Reverse recovery time	I_S = 5A, dis/dt = –100A/μs	—	80	—	ns

PERFORMANCE CURVES

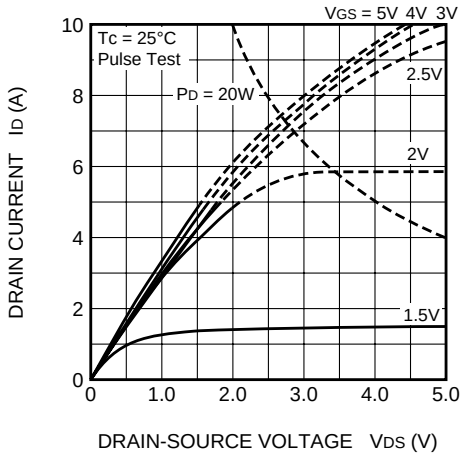
POWER DISSIPATION DERATING CURVE



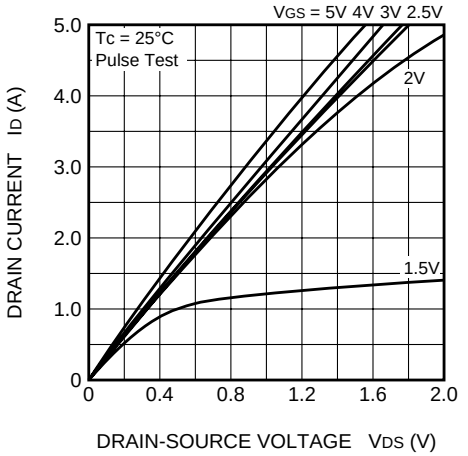
MAXIMUM SAFE OPERATING AREA



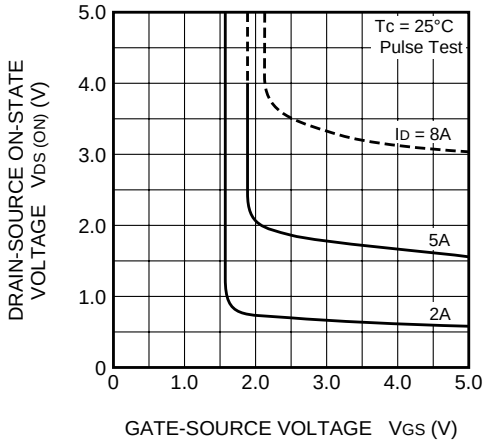
OUTPUT CHARACTERISTICS (TYPICAL)



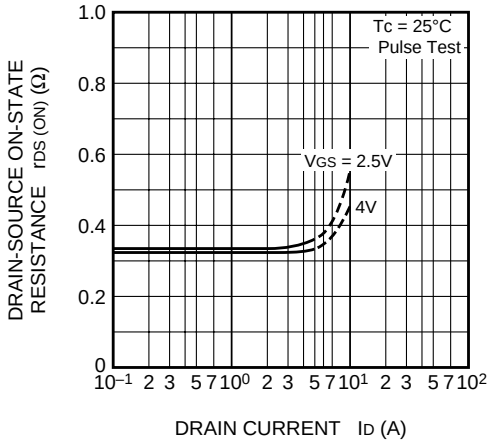
OUTPUT CHARACTERISTICS (TYPICAL)



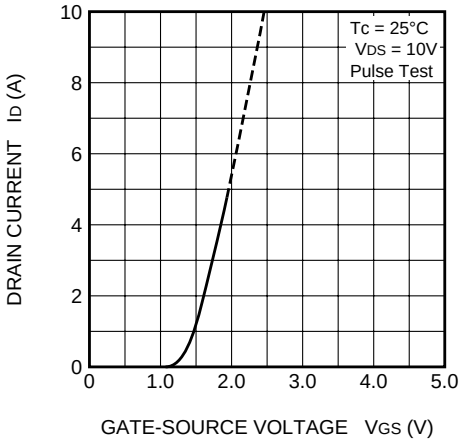
ON-STATE VOLTAGE VS. GATE-SOURCE VOLTAGE (TYPICAL)



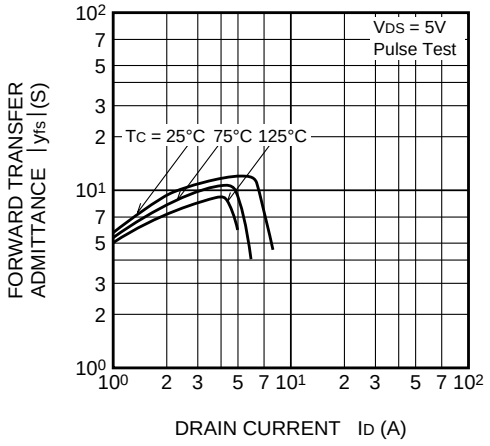
ON-STATE RESISTANCE VS. DRAIN CURRENT (TYPICAL)



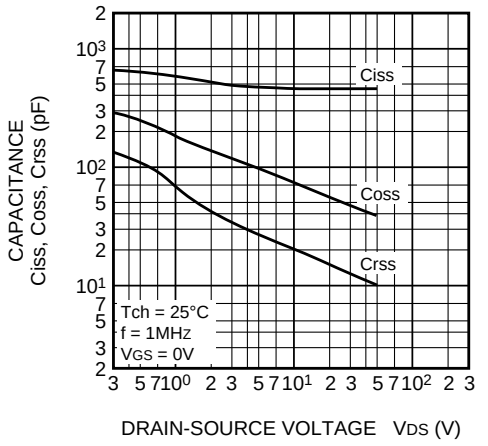
TRANSFER CHARACTERISTICS (TYPICAL)



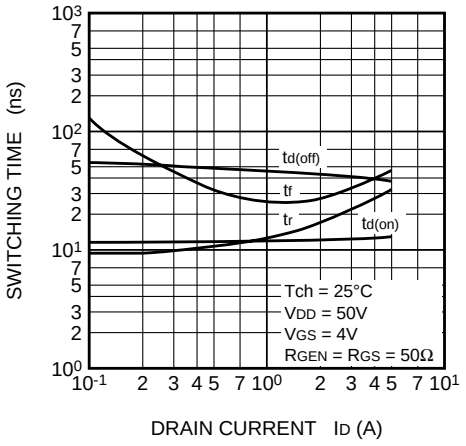
FORWARD TRANSFER ADMITTANCE VS.DRAIN CURRENT (TYPICAL)



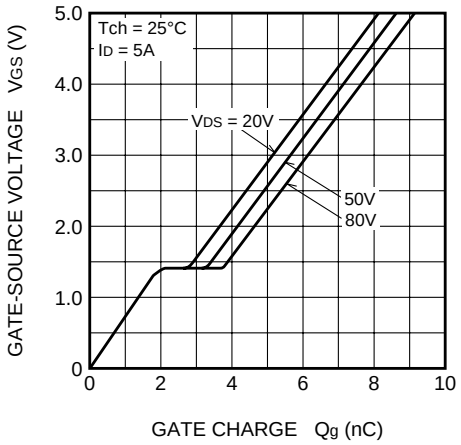
CAPACITANCE VS. DRAIN-SOURCE VOLTAGE (TYPICAL)



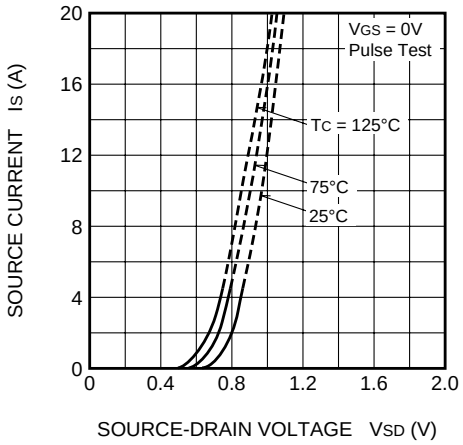
SWITCHING CHARACTERISTICS (TYPICAL)



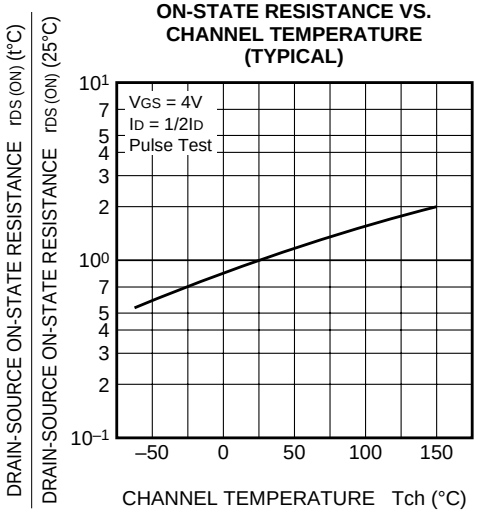
GATE-SOURCE VOLTAGE VS.GATE CHARGE (TYPICAL)



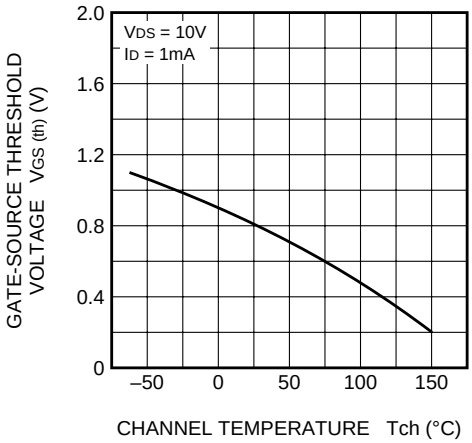
SOURCE-DRAIN DIODE FORWARD CHARACTERISTICS (TYPICAL)



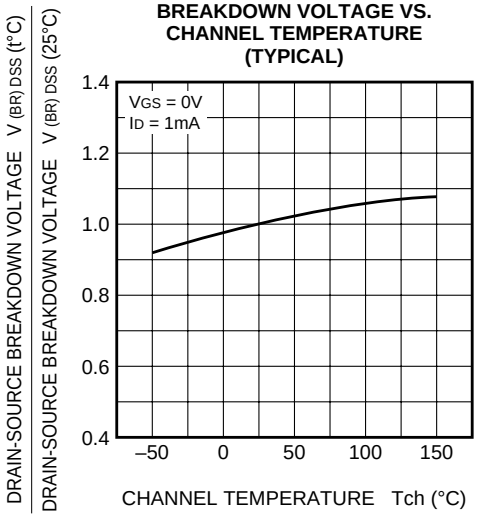
ON-STATE RESISTANCE VS. CHANNEL TEMPERATURE (TYPICAL)



THRESHOLD VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)



BREAKDOWN VOLTAGE VS. CHANNEL TEMPERATURE (TYPICAL)



TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS