

# 600kHz, 1A Switch PWM DC/DC Converter

#### **FEATURES**

- 1A,  $0.5\Omega$ , 30V Internal Switch
- Operates with V<sub>IN</sub> as Low as 1.5V
- 600kHz Fixed Frequency Operation
- Low-Battery Detector Stays Active in Shutdown
- Low V<sub>CESAT</sub> Switch: 410mV at 800mA
- Pin-for-Pin Compatible with the LT1317B
- Small 8-Lead MSOP and SO Packages

#### **APPLICATIONS**

- LCD Bias Supplies
- GPS Receivers
- Battery Backup
- Portable Electronic Equipment
- Diagnostic Medical Instrumentation

#### DESCRIPTION

The LT®1949 is a fixed frequency step-up DC/DC converter with a 1A,  $0.5\Omega$  internal switch. Capable of generating 10V at 175mA from a 3.3V input, the LT1949 is ideal for generating bias voltages for large screen LCD panels. Constant frequency 600kHz operation results in a low noise output that is easy to filter and the 30V switch rating allows output voltage up to 28V using a single inductor. An external compensation pin gives the user flexibility in optimizing loop compensation, allowing small low ESR ceramic capacitors to be used at the output. The 8-lead MSOP and SO packages ensure a low profile overall solution.

The LT1949 includes a low-battery detector that stays alive when the device goes into shutdown. Quiescent current in shutdown is  $25\mu$ A, while operating current is 4.5mA.

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## TYPICAL APPLICATION

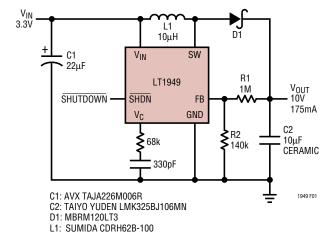


Figure 1. 3.3V to 10V/175mA DC/DC Converter

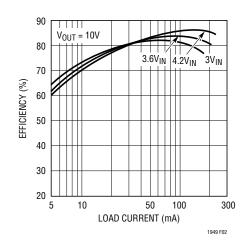


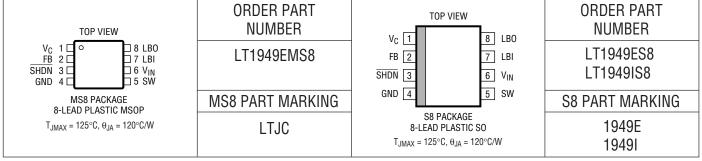
Figure 2. 3.3V to 10V Converter Efficiency



# **ABSOLUTE MAXIMUM RATINGS** (Note 1)

V <sub>IN</sub> , LBO Voltage	Junction Temperature
$\begin{tabular}{lllll} FB \ Voltage & & & V_{IN} + 0.3V \\ V_C \ Voltage & & 2V \\ \underline{LBI \ Voltage} & & 0V \le V_{LBI} \le 1V \\ \end{tabular}$	LT1949EMS840°C to 85°C LT1949ES8/LT1949IS840°C to 85°C

## PACKAGE/ORDER INFORMATION



Consult factory for Military grade parts.

## 

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
IQ	Quiescent Current		•		4.5	7.5	mA
		$V_{\overline{SHDN}} = 0V$	•		25	40	μΑ
$V_{FB}$	Feedback Voltage			1.22	1.24	1.26	V
			•	1.20	1.24	1.26	V
$I_{B}$	FB Pin Bias Current (Note 3)		•		12	80	nA
	Input Voltage Range		•	1.7		12	V
g <sub>m</sub>	Error Amp Transconductance	$\Delta I = 5\mu A$	•	70	140	240	μmhos
A <sub>V</sub>	Error Amp Voltage Gain				700		V/V
	Maximum Duty Cycle		•	80	85		%
	Switch Current Limit (Note 4)	V <sub>IN</sub> = 2.5V, Duty Cycle = 30%		1	1.13	1.5	А
		V <sub>IN</sub> = 2.5V, Duty Cycle = 30%	•	0.95		1.5	A
fosc	Switching Frequency		•	500	600	750	kHz
	Shutdown Pin Current	V <sub>SHDN</sub> = V <sub>IN</sub>	•		0.015	0.1	μА
		$V_{\overline{SHDN}} = 0V$	•		-2.3	-7	μΑ
	LBI Threshold Voltage			190	200	210	mV
			•	180	200	220	mV
	LBO Output Low	I <sub>SINK</sub> = 10μA	•		0.15	0.25	V
	LBO Leakage Current	V <sub>LBI</sub> = 250mV, V <sub>LBO</sub> = 5V	•		0.02	0.1	μА
	LBI Input Bias Current (Note 5)	V <sub>LBI</sub> = 150mV	•		5	60	nA
	Low-Battery Detector Gain	1MΩ Pull-Up			2000		V/V
	Switch Leakage Current	V <sub>SW</sub> = 5V	•		0.01	3	μА

## **ELECTRICAL CHARACTERISTICS**

**ELECTRICAL CHARACTERISTICS** The  $\bullet$  denotes specifications which apply over the full operating temperature range, otherwise specifications are  $T_A = 25^{\circ}C$ .  $V_{IN} = 2V$ ,  $V_{\overline{SHDN}} = 2V$  unless otherwise noted.

SYMBOL	PARAMETER	CONDITIONS		MIN	TYP	MAX	UNITS
	Switch V <sub>CESAT</sub>	I <sub>SW</sub> = 800mA			410		mV
		I <sub>SW</sub> = 500mA	•			400	mV
	Reference Line Regulation	$1.8V \le V_{IN} \le 12V$	•		0.08	0.15	%/V
	SHDN Input Voltage High		•	1.4		6	V
	SHDN Input Voltage Low		•			0.4	V

Note 1: Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

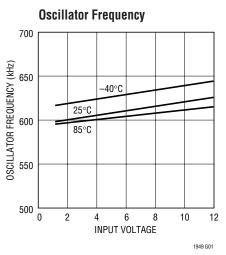
**Note 2:** The LT1949E is guaranteed to meet performance specifications from 0°C to 70°C. Specifications over the -40°C to 85°C operating temperature range are assured by design, characterization and correlation with statistical process controls.

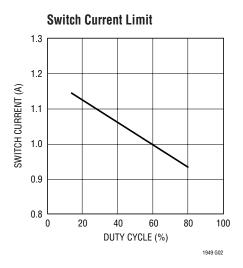
Note 3: Bias current flows into FB pin.

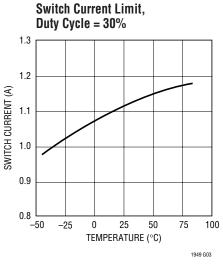
Note 4: Switch current limit guaranteed by design and/or correlation to static tests. Duty cycle affects current limit due to ramp generator.

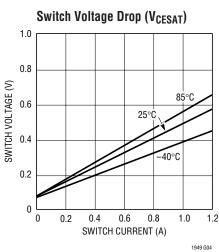
Note 5: Bias current flows out of LBI pin.

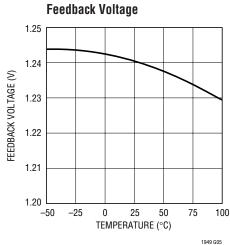
## TYPICAL PERFORMANCE CHARACTERISTICS

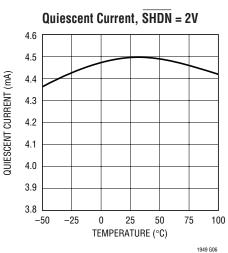






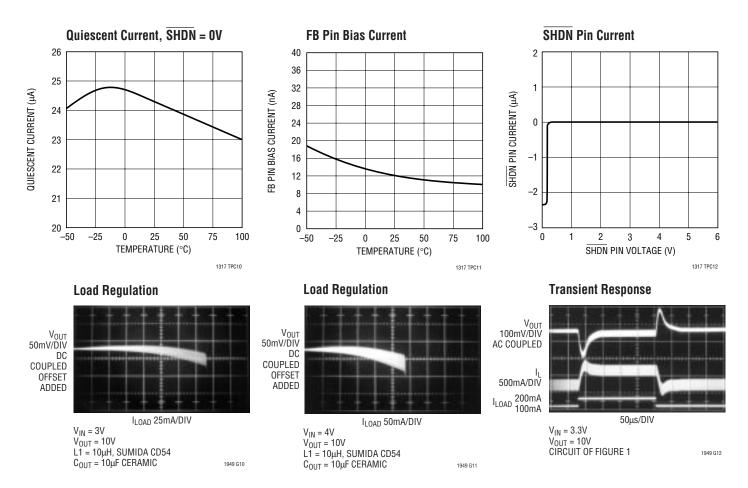








#### TYPICAL PERFORMANCE CHARACTERISTICS



## PIN FUNCTIONS

 $V_{C}$  (Pin 1): Compensation Pin for Error Amplifier. Connect a series RC network from this pin to ground. Typical values for compensation are a 68k/330pF combination when using ceramic output capacitors. Minimize trace area at  $V_{C}$ .

**FB** (**Pin 2**): Feedback Pin. Reference voltage is 1.24V. Connect resistor divider tap here. Minimize trace area at FB. Set  $V_{OUT}$  according to:  $V_{OUT} = 1.24V(1 + R1/R2)$ .

SHDN (Pin 3): Shutdown. Pull this pin low for shutdown mode (only the low-battery detector remains active). Leave this pin floating or tie to a voltage between 1.4V and 6V to enable the device. SHDN pin is logic level and need only meet the logic specification (1.4V for high, 0.4V for low).

**GND (Pin 4):** Ground. Connect directly to local ground plane.

**SW** (**Pin 5**): Switch Pin. Connect inductor/diode here. Minimize trace area at this pin to keep EMI down.

 $V_{IN}$  (Pin 6): Supply Pin. Must be bypassed close to the pin.

**LBI (Pin 7):** Low-Battery Detector Input. 200mV reference. Voltage on LBI must stay between ground and 700mV. Low-battery detector remains active in shutdown mode.

**LBO (Pin 8):** Low-Battery Detector Output. Open collector, can sink  $10\mu A$ . A  $1M\Omega$  pull-up is recommended.



## **BLOCK DIAGRAM**

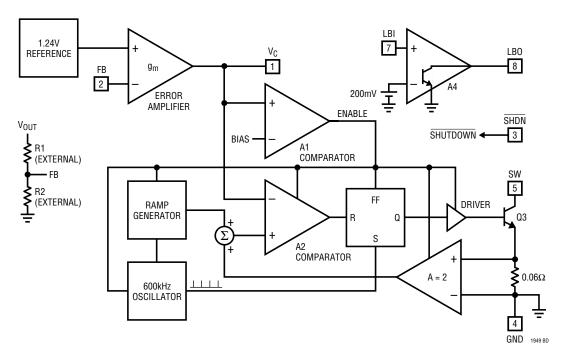


Figure 3. LT1949 Block Diagram

## **OPERATION**

The LT1949 is a current mode, fixed frequency step-up DC/DC converter with an internal 1A NPN power transistor. Operation can best be understood by referring to the Block Diagram.

At the beginning of each oscillator cycle, the flip-flop is set and the switch is turned on. Current in the switch ramps up until the voltage at A2's positive input reaches the  $V_{C}$  pin voltage, causing A2's output to change state and the switch to be turned off. The signal at A2's positive input is a summation of a signal representing switch current and a ramp generator (introduced to avoid subharmonic oscillations at duty factors greater than 50%). If the load increases,  $V_{OUT}$  (and FB) will drop slightly and the error amplifier will drive  $V_{C}$  to a higher voltage, causing current in the switch to increase. In this way, the error amplifier drives the  $V_{C}$  pin to the voltage necessary to satisfy the load. Frequency compensation is provided by an external series RC network connected between the  $V_{C}$  pin and ground.

#### **Layout Hints**

The LT1949 switches current at high speed, mandating careful attention to layout for proper performance. *You will not get advertised performance with careless layouts.* Figure 4 shows recommended component placement for a boost (step-up) converter. Follow this closely in your PC layout. Note the direct path of the switching loops. Input capacitor C1 *must* be placed close (<5mm) to the IC package. As little as 10mm of wire or PC trace from  $C_{\text{IN}}$  to  $V_{\text{IN}}$  will cause problems such as inability to regulate or oscillation.

The ground terminal of output capacitor C2 should tie close to Pin 4 of the LT1949. Doing this reduces dl/dt in the ground copper which keeps high frequency spikes to a minimum. The DC/DC converter ground should tie to the PC board ground plane at one place only, to avoid introducing dl/dt in the ground plane.



## **OPERATION**

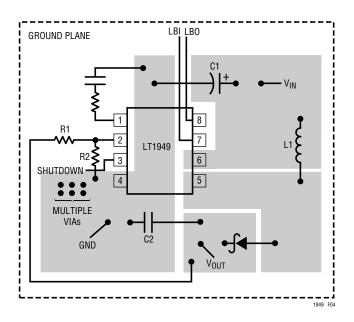


Figure 4. Recommended Component Placement for Boost Converter. Note Direct High Current Paths Using Wide PC Traces. Minimize Trace Area at Pin 1 (V<sub>C</sub>) and Pin 2 (FB). Use Multiple Vias to Tie Pin 4 Copper to Ground Plane. Use Vias at One Location Only to Avoid Introducing Switching Currents into the Ground Plane

## APPLICATIONS INFORMATION

#### **Low-Battery Detector**

The LT1949's low-battery detector is a simple PNP input gain stage with an open collector NPN output. The negative input of the gain stage is tied internally to a 200mV  $\pm 5\%$  reference. The positive input is the LBI pin. Arrangement as a low-battery detector is straightforward.

R1 VIN LT1949

R2 100k

R1 = V<sub>LB</sub> - 200mV

R1 = V<sub>LB</sub> - 200mV

2μA

Figure 5. Setting Low-Battery Detector Trip Point

Figure 5 details hookup. R1 and R2 need only be low enough in value so that the bias current of the LBI pin doesn't cause large errors. For R2, 100k is adequate. The 200mV reference can also be accessed as shown in Figure 6. The low-battery detector remains active in shutdown.

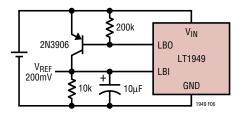
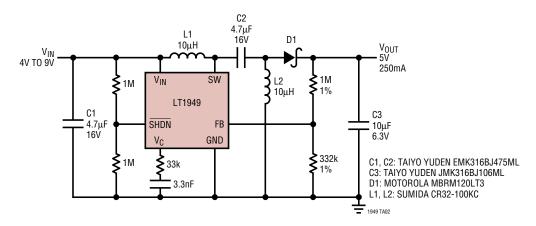


Figure 6. Accessing 200mV Reference



## TYPICAL APPLICATION

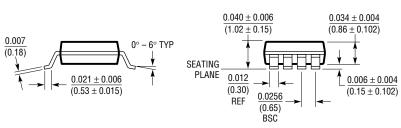
#### 4 Cell to 5V SEPIC Converter

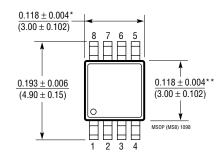


## PACKAGE DESCRIPTION Dimensions in inches (millimeters) unless otherwise noted.

#### MS8 Package 8-Lead Plastic MSOP

(LTC DWG # 05-08-1660)

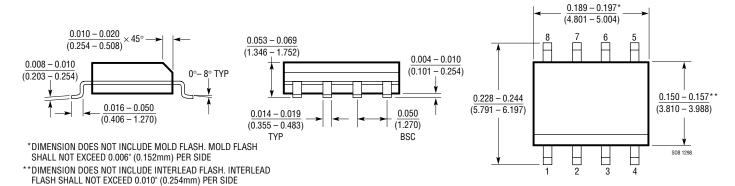




- \* DIMENSION DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS. MOLD FLASH, PROTRUSIONS OR GATE BURRS SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE
- \*\* DIMENSION DOES NOT INCLUDE INTERLEAD FLASH OR PROTRUSIONS.
  INTERLEAD FLASH OR PROTRUSIONS SHALL NOT EXCEED 0.006" (0.152mm) PER SIDE

#### S8 Package 8-Lead Plastic Small Outline (Narrow 0.150)

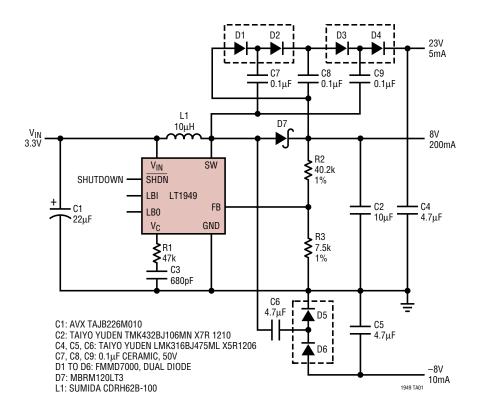
(LTC DWG # 05-08-1610)





## TYPICAL APPLICATION

#### Low Profile Triple Output LCD Bias Generator



## **RELATED PARTS**

PART NUMBER	DESCRIPTION	COMMENTS	
LT1054	High Power Regulated Charge Pump	Up to 100mA Output with No Inductors	
LT1302	High Output Current Micropower DC/DC Converter	5V/600mA from 2V, 2A Internal Switch, 200μA I <sub>Q</sub>	
LT1304	2-Cell Micropower DC/DC Converter	Low-Battery Detector Active in Shutdown	
LT1307B	Single Cell Micropower 600kHz PWM DC/DC Converter	3.3V at 75mA from 1 Cell, MSOP Package	
LT1308B	2A 600kHz PWM DC/DC Converter	TSSOP Package	
LT1317B	Micropower, 600kHz PWM DC/DC Converter	2 Cells to 3.3V at 200mA, MSOP Package	
LTC®1516	2-Cell to 5V Regulated Charge Pump	12μΑ I <sub>Q</sub> , No Inductors, 5V at 50mA from 3V Input	
LT1613	Single Cell 1.4MHz PWM DC/DC Converter	3.3V to 5V at 200mA, SOT-23 Package	
LTC1682	Doubler Charge Pump with Low Noise Linear Regulator	3.3V and 5V Outputs with 60µV <sub>RMS</sub> Noise, Up to 80mA Output	
LTC1754	Micropower 3.3V/5V Charge Pump with Shutdown	Up to 50mA Output, I <sub>Q</sub> = 13μA, SOT-23 Package	