

Fiber Optics

DC/DC (5 V/3.3 V) Single Mode 155 MBd ATM/SDH/SONET 1x9 Long Haul Transceiver

V23826-C18-C64 V23826-C18-C364

Features

- Compliant with ATM, SONET OC-3, SDH STM-1
- Meets mezzanine standard height of 9.8 mm
- Compact integrated transceiver unit with
 - MQW laser diode transmitter
 - InGaAs PIN photodiode receiver
 - Duplex SC receptacle
- Class 1 FDA and IEC laser safety compliant
- FDA Accession No. 9520890-20
- Single power supply (5 V or 3.3 V)
- Signal detect indicator
- PECL differential inputs and outputs
- Process plug included
- Input Signal Monitor
- · Wave solderable and washable with process plug inserted
- Typical dynamic range of 36 dB
- Industry standard multisource 1x9 footprint
- For distances of up to 40 km on single mode fiber



Part Number	Voltage	Signal Detect	Input	Output	
V23826-C18-C64	5 V	PECL	DC	DC	
V23826-C18-C364	3.3 V				
Add suffix to PIN	Shield options				
-C3	Metallized c	Metallized cover, forward springs			
-D3	Metallized c	over, backward spring	gs		



Pin Configuration

Pin Configuration

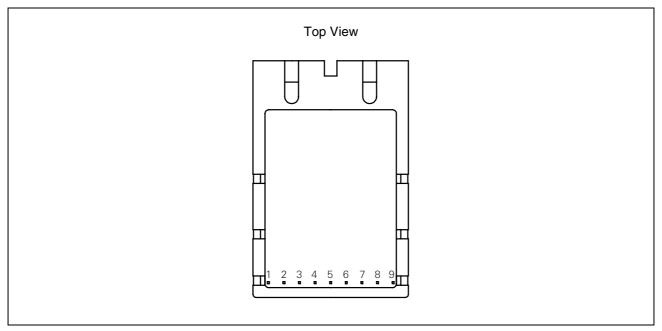


Figure 1

Pin Description

Pin No.	Symbol	Level/Logic	Function	Description
1	RxV _{EE}	Power Supply	Rx Ground	Negative power supply, normally ground
2	RD	PECL Output	Rx Output Data	Receiver output data
3	RDn			Inverted receiver output data
4	SD	PECL Output active high	Rx Signal Detect	A high level on this output shows that optical data is applied to the optical input.
5	RxV_{CC}	Power Supply	Rx +3.3 V / 5 V	Positive power supply,
6	TxV_{CC}		Tx +3.3 V / 5 V	+3.3 V / 5 V
7	TDn	PECL Input	Tx Input Data	Inverted transmitter input data
8	TD			Transmitter input data
9	TxV _{EE}	Power Supply	Tx Ground	Negative power supply, normally ground
S1/2		Mech. Support	Stud Pin	Not connected



Description

Description

The Infineon single mode ATM transceiver complies with the ATM Forum's Network Compatible ATM for Local Network Applications document and ANSI's Broadband ISDN - Customer Installation Interfaces, Physical Media Dependent Specification, T1.646-1995, Bellcore-SONET OC-3/LR-1 and ITU-T G.957 STM-1/L.1.1.

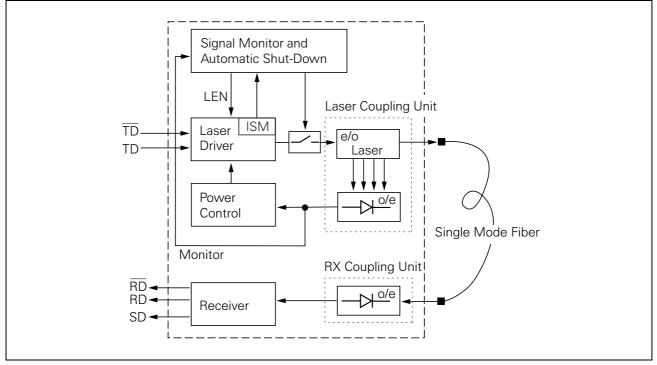
ATM was developed to facilitate solutions in multimedia applications and real time transmission. The data rate is scalable, and the ATM protocol is the basis of the broadband public networks being standardized in the International Telecommunications Union (ITU), the former International Telegraph and Telephone Consultative Committee (CCITT). ATM can also be used in local private applications.

The Infineon single mode ATM long haul transceiver is a single unit comprised of a transmitter, a receiver, and an SC receptacle. This design frees the customer from many alignment and PC board layout concerns. The module is designed for low cost WAN applications. It can be used as the network end device interface in workstations, servers, and storage devices, and in a broad range of network devices such as bridges, routers, intelligent hubs, and wide area ATM switches.

This transceiver operates at 155.520 Mbit/s from a single power supply (+5 V or +3.3 V). The differential data inputs and outputs are PECL compatible.

Functional Description

This transceiver is designed to transmit serial data via single mode cable.







Description

The transmitter converts electrical PECL compatible serial data (TD and TDnot) into optical serial data. It contains a laser driver circuit that drives the modulation and bias current of the laser diode. The currents are controlled by a power control circuit to guarantee constant output power of the laser over temperature and aging.

The power control uses the output of the monitor PIN diode (mechanically built into the laser coupling unit) as a controlling signal, to prevent the laser power from exceeding the operating limits.

This transceiver contains an Input Signal Monitor (ISM), that switches the optical power off if a continuously low level is applied at Data Input.

The receiver component converts the optical serial data into PECL compatible electrical data (RD and RDnot). The Signal Detect (SD, active high) shows whether optical data is present¹.

This module is a Class 1 laser product, due to an integrated automatic shutdown circuit that disables the laser when it detects transmitter failures.

Single fault condition is ensured by means of an integrated automatic shutdown circuit that disables the laser when it detects transmitter failures. A reset is only possible by turning the power off, and then on again.

The transceiver contains a supervisory circuit to monitor the power supply. This circuit makes an internal reset signal whenever the supply voltage drops below the reset threshold. It keeps the reset signal active for at least 15 milliseconds after the voltage has risen above the reset threshold. During this time the laser is inactive.

¹⁾ We recommend to switch off the transmitter supply (V_{CC} - Tx) if no transmitter input data is applied.



Description

Regulatory Compliance

Feature	Standard	Comments
Electrostatic Discharge (ESD) to the Electrical Pins	MIL-STD 883C Method 3015.4	Class 1 (>1000 V)
Immunity: Electrostatic Discharge (ESD) to the Duplex SC Receptacle	EN 61000-4-2 IEC 61000-4-2	Discharges of ±15 kV with an air discharge probe on the receptacle cause no damage.
Immunity: Radio Frequency Electromagnetic Field	EN 61000-4-3 IEC 61000-4-3	With a field strength of 10 V/m rms, noise frequency ranges from 3 MHz to 1 GHz. No effect on transceiver performance between the specification limits.
Emission: Electromagnetic Interference (EMI)	FCC Class B EN 55022 Class B CISPR 22	Noise frequency range: 30 MHz to 6 GHz, margins depend on PCB layout and chassis design.



Technical Data

Technical Data

Absolute Maximum Ratings

Parameter		Symbol	Symbol Limit		Unit
			min. max.		
Package Power Dissipation ¹⁾				1.5	W
Supply Voltage	5 V 3.3 V	$V_{\rm CC} - V_{\rm EE}$		7 5	V
Data Input Levels				$V_{\rm CC} + 0.5$	
Differential Data Input Voltage				2.5	
Operating Ambient Temperature			0	70	°C
Storage Ambient Temperature			-40	85	1
Soldering Conditions Temp/Time (MIL-STD 883C, Method 2003)				250/5.5	°C/s

¹⁾ For $V_{CC} - V_{EE}$ (min., max.). 50% duty cycle. The supply current does not include the load drive current of the receiver output.

Exceeding any one of these values may destroy the device immediately.



Technical Data

Recommended Operating Conditions

Parameter		Symbol		Limit Va	ues Un	
			min.	typ.	max.	
Ambient Temperature		T _{AMB}	0		70	°C
Power Supply Voltage	3.3 V 5 V	$V_{\rm CC} - V_{\rm EE}$	3.1 4.75	3.3 5	3.5 5.25	V
Supply Current ¹⁾	3.3 V 5 V	I _{CC}		175 190	250 270	mA
Transmitter				·	·	
Data Input High Voltage		$V_{\rm IH} - V_{\rm CC}$	-1165		-880	mV
Data Input Low Voltage)	$V_{\rm IL} - V_{\rm CC}$	-1810		-1475	
Input Data Rise/Fall Time, 10%–90%		$t_{\rm R}, t_{\rm F}$	0.4		1.3	ns
Receiver				1		1
Output Current		Io			25	mA
Input Center Wavelength		λ _C	1260		1360	nm

¹⁾ For $V_{CC} - V_{EE}$ (min., max.) 50% duty cycle. The supply current does not include the load drive current of the receiver output.

The electro-optical characteristics described in the following tables are valid only for use under the recommended operating conditions.

Transmitter Electro-Optical Characteristics

Parameter		Symbol	Limit Values			Unit
			min.	typ.	max.	
Output Power (Average	e) ¹⁾	Po	-5	-3	-0	dBm
Center Wavelength		λ _C	1280		1335	nm
Spectral Width (FWHM)	$D_{ }$		2.4	3	
Extinction Ratio (Dynar	nic)	ER	10			dB
Reset Threshold for TxV_{CC}^{2}	3.3 V 5 V	V _{TH}		2.7 3.5		V
Reset Active Time Out	2)	t _{RES}	15	25	35	ms
Eye Diagram ³⁾		ED				

 $^{\scriptscriptstyle 1)}$ Into single mode fiber, 9 μm diameter.

²⁾ Laser power is shut down if power supply is below V_{TH} and switched on if power supply is above V_{TH} after t_{RES} .

³⁾ Transmitter meets ANSI T1E1.2, SONET OC-3 and ITU-T G.957 mask patterns.



Technical Data

Receiver Electro-Optical Characteristics

Parameter	Symbol	Limit Value		ues	Unit
		min.	typ.	max.	
Sensitivity (Average Power) ¹⁾	P _{IN}		-36	-34	dBm
Saturation (Average Power)	P _{SAT}	-2			
Signal Detect Assert Level ²⁾	P _{SDA}		-37.5	-34	
Signal Detect Deassert Level ³⁾	P _{SDD}	-44	-40		
Signal Detect Hysteresis	$P_{\rm SDA} - P_{\rm SDD}$	1	2.5	6	dB
Signal Detect Assert Time	t _{ASS}			100	μs
Signal Detect Deassert Time	t _{DAS}			350	
Output Low Voltage ⁴⁾	$V_{\rm OL} - V_{\rm CC}$	-1950		-1620	mV
Output High Voltage ⁴⁾	$V_{\rm OH} - V_{\rm CC}$	-1025		-720	
Output Data, Rise/Fall Time, 20%–80%	$t_{\rm R}, t_{\rm F}$			375	ps
Output SD Rise/Fall Time ⁵⁾				40	ns

¹⁾ Minimum average optical power at which the BER is less than 1x10⁻¹⁰. Measured with a 2²³ - 1 NRZ PRBS as recommended by ANSI T1E1.2, SONET OC-3, and ITU-T G.957.

²⁾ An increase in optical power of data signal above the specified level will cause the SIGNAL DETECT to switch from a Low state to a High state.

³⁾ A decrease in optical power of data signal below the specified level will cause the SIGNAL DETECT to switch from a High state to a Low state.

⁴⁾ DC/DC for data, DC/DC PECL for Signal Detect, PECL compatible. Load is 50 Ω into V_{CC} – 2 V for data, 500 Ω to V_{EE} for Signal Detect. Measured under DC conditions. For dynamic measurements a tolerance of 50 mV should be added. V_{CC} = 3.3 V / 5 V. T_{AMB} = 25°C.

⁵⁾ PECL compatible. A high level on this output shows that an optical signal is applied to the optical input.



Eye Safety

Eye Safety

This laser based single mode transceiver is a Class 1 product. It complies with IEC 60825-1 and FDA 21 CFR 1040.10 and 1040.11.

To meet laser safety requirements the transceiver shall be operated within the Absolute Maximum Ratings.

Attention: All adjustments have been made at the factory prior to shipment of the devices. No maintenance or alteration to the device is required. Tampering with or modifying the performance of the device will result in voided product warranty. Do not view into the open optical port for more than 60 seconds.

Note: Failure to adhere to the above restrictions could result in a modification that is considered an act of "manufacturing," and will require, under law, recertification of the modified product with the U.S. Food and Drug Administration (ref. 21 CFR 1040.10 (i)).

Laser Data

Wavelength	1300 nm
Total output power (as defined by IEC: 50 mm aperture at 10 cm distance)	8 mW
Total output power (as defined by FDA: 7 mm aperture at 20 cm distance)	600 µW
Beam divergence	4°

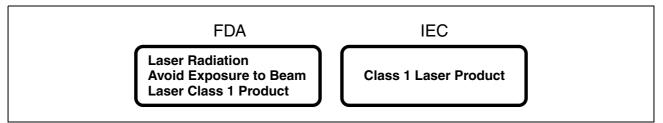


Figure 3 Required Labels

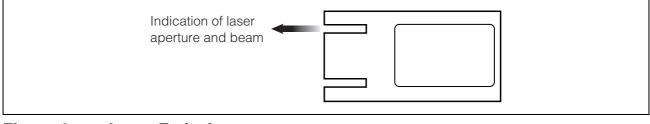


Figure 4 Laser Emission



Application Notes

Application Notes

ATM transceivers and matching circuits are high frequency components and shall be terminated as recommended in the application notes for proper EMI performance. Electromagnetic emission may be caused by these components.

To prevent emissions it is recommended that cutouts for the fiber connectors be designed as small as possible.

It is recommended that the Tx plug and the Rx plug be separated with a bar that divides the duplex SC opening.

V_{CC} SerDes 3 3 V/5 V TxVEE 9 V_{CC} В1 TxD 8 Tx+ C6 ECL/PECL Laser R10 Driver Driver TxD 7 Tx-88 VccTx L1 6 Serializer/ Vcc Deserializer 3.3 V/5 V Infineon Transceiver C1 V23826-C18-C64/C364 VccRx **DC/DC** Option 5 C3 C2 Signal Detect SD 4 SD to upper level R1 R2 R9 Limiting RD-RxD 3 C4 Pre RD-Amplifier Amp Receiver PLL etc. ↓<u>C</u>5 RD+ RxD 2 RD+ $\mathsf{RxV}_{\mathsf{EE}}$ 1 ВЗ R4 BB BB $C1/2/3 = 4.7 \,\mu F$ $R5/6 = 150 \Omega (3.3 V)$ C4/5/6/7 = 100 nF = 270 Ω (5 V) L1/2 $= 1 \mu H$ R9 = 270 Ω (3.3 V) R10/11 = 127 Ω (3.3 V) $= 510 \Omega (5 V)$ = 82 Ω (5 V) Place R1/2/3/4 close to SerDes chip, (depends on SerDes chip used) depends on SerDes chip used, R7/8 = 82 Ω (3.3 V) see application note of SerDes supplier. = 127 Ω (5 V) Place R7/8/10/11 close to Infineon Transceiver (depends on SerDes chip used)

Single Mode 155 MBd ATM 1x9 Long Haul Transceiver

Figure 5



Application Notes

This Application Note assumes Fiber Optic Transceivers using 5 V power supply and SerDes Chips using 3.3 V power supply. It also assumes no self biasing at the receiver data inputs (RD+/RD-) of the SerDes chip (Refer to the manufacturer data sheet for other applications). 3.3 V-Transceivers can be directly connected to SerDes-Chips using standard PECL Termination network.

Value of R1...R4 may vary as long as proper 50 Ω termination to V_{EE} or 100 Ω differential is provided. The power supply filtering is required for good EMI performance. Use short tracks from the inductor L1/L2 to the module $V_{\text{CC}}\text{Rx}/V_{\text{CC}}\text{Tx}$. Further application notes for electrical interfacing are available upon request. Ask for Appnote 82.

We strongly recommend a $V_{\rm EE}$ plane under the module for getting good EMI performance.

The transceiver contains an automatic shutdown circuit. Reset is only possible if the power is turned off, and then on again. ($V_{CC}Tx$ switched below V_{TH}).

Application Board available on request.



Shield Options

Shield Options

Shield with Forward Springs, -C3

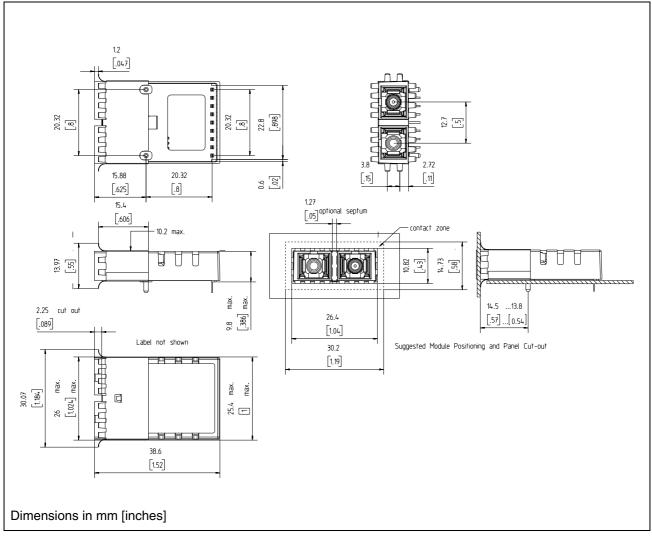
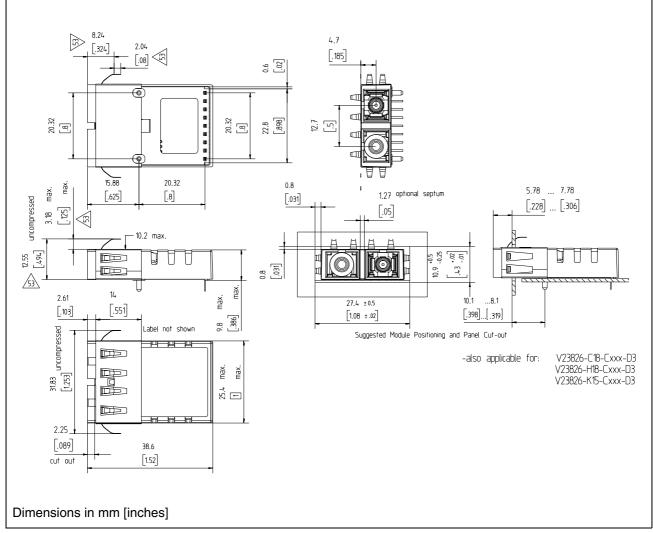


Figure 6



Shield Options

Shield with Backward Springs, -D3

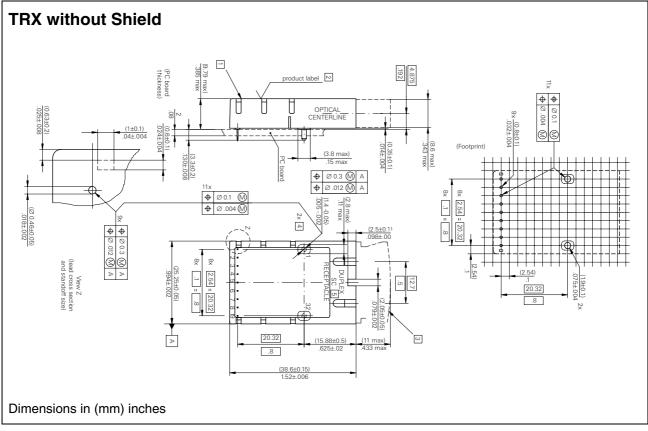






Package Outlines

Package Outlines





	C18-C64 C18-C364		
	n History:	2000-03-01	DS0
Previous	Version:		
Page	Subjects	(major changes since last revision)	
	Documer	t's layout has been changed: 2002-Aug.	

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Edition 2000-03-01

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