

Fairchild Reference Design

The following reference design supports inclusion of **FL6961** in design of LED illumination. It should be used in conjunction with the FL6961 datasheet as well as Fairchild's application notes and technical support team. Please visit Fairchild's website at <http://www.fairchildsemi.com>.

| Application | Fairchild Device | Input Voltage Range | Rated Output Power | Output Voltage (Rated Current) |
|------------------|------------------|-----------------------|--------------------|--------------------------------|
| LED Illumination | FL6961 | 90-265V _{AC} | 16.8W | 24V (0.7A) |

Key Features

- Boundary Mode PFC Controller
- Low Input Current THD
- Controlled On-time PWM
- Zero Current Detection
- Cycle-by-cycle Current Limiting
- Leading-edge blanking instead of RC filtering
- Low Start-up Current: 10uA (typical)
- Low Operating Current: 4.5mA (typical)
- Feedback Open-Loop Protection
- Programmable Maximum On-Time (MOT)
- Output Over-Voltage Clamping Protection
- Clamped Gate Output Voltage 16.5V

Figure 1. Schematic

2. Transformer

2.1. Transformer Schematic Diagram

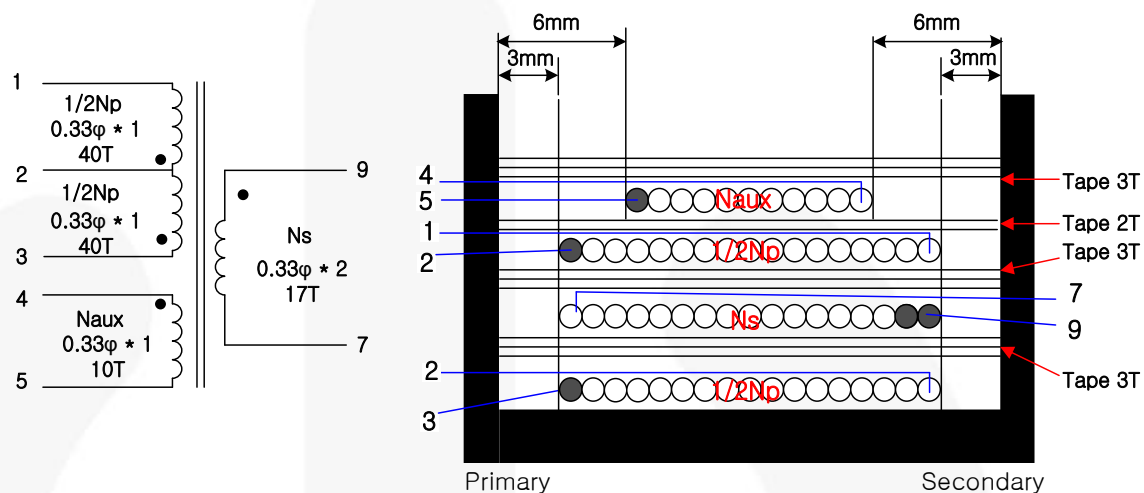


Figure 2. Transformer specifications & construction. [EE2525]

Table 1. Winding specifications.

| No | Winding | Pin(S → F) | Wire | Turns | Winding Method |
|----|--|------------|---------|-------|-------------------------|
| 1 | 1/2Np | 3 → 2 | 0.33φ×1 | 40Ts | Solenoid winding |
| 2 | Insulation : Polyester Tape t = 0.025mm, 1Layers | | | | |
| 3 | Ns | 9 → 7 | 0.33φ×2 | 17Ts | Solenoid winding |
| 4 | Insulation : Polyester Tape t = 0.025mm, 3Layers | | | | |
| 5 | 1/2Np | 2 → 1 | 0.33φ×1 | 40 Ts | Solenoid winding |
| 6 | Insulation : Polyester Tape t = 0.025mm, 3Layers | | | | |
| 7 | Naux | 4 → 5 | 0.33φ×1 | 10 Ts | Center Solenoid winding |
| 8 | Insulation : Polyester Tape t = 0.025mm, 3Layers | | | | |

Table 2. Electrical Characteristics.

| | Pin | Spec. | Remark |
|------------|------|-----------|-----------------------|
| Inductance | 3- 1 | 1mH ±7% | 1kHz, 1V |
| Leakage | 3- 1 | 10 uH Max | Short all output pins |

2.2. Performance

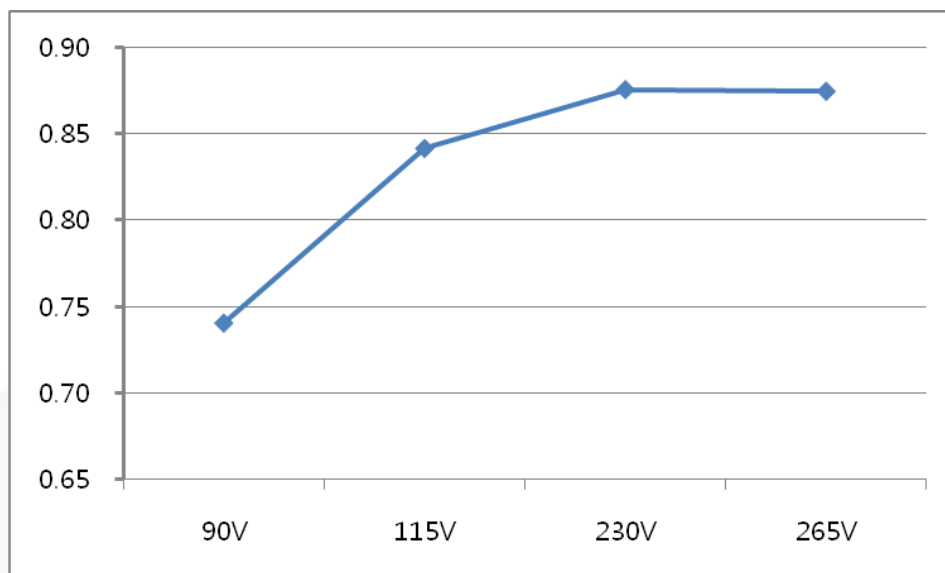


Figure 3. Efficiency Curve

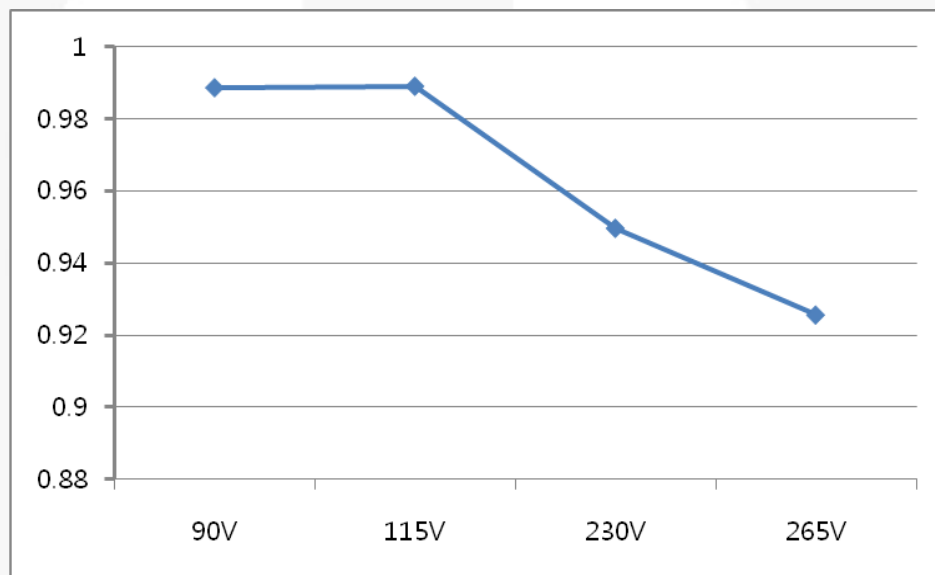


Figure 4. Power Factor Performance

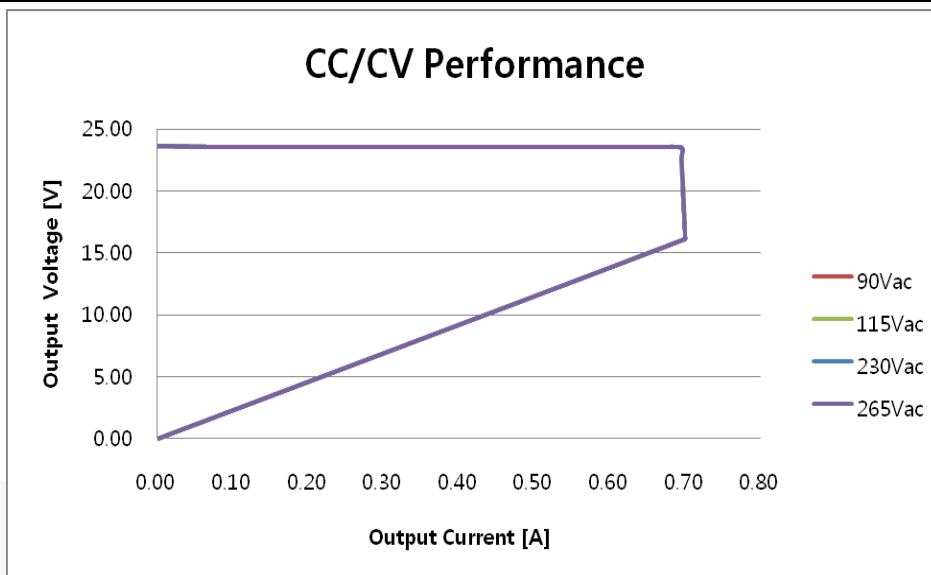


Figure 5. CC/CV Curve

3. Related Resources

[Datasheet link FL6961](#)

<http://www.fairchildsemi.com/referencedesign/>

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