

# **PNP Epitaxial Silicon Transistor**

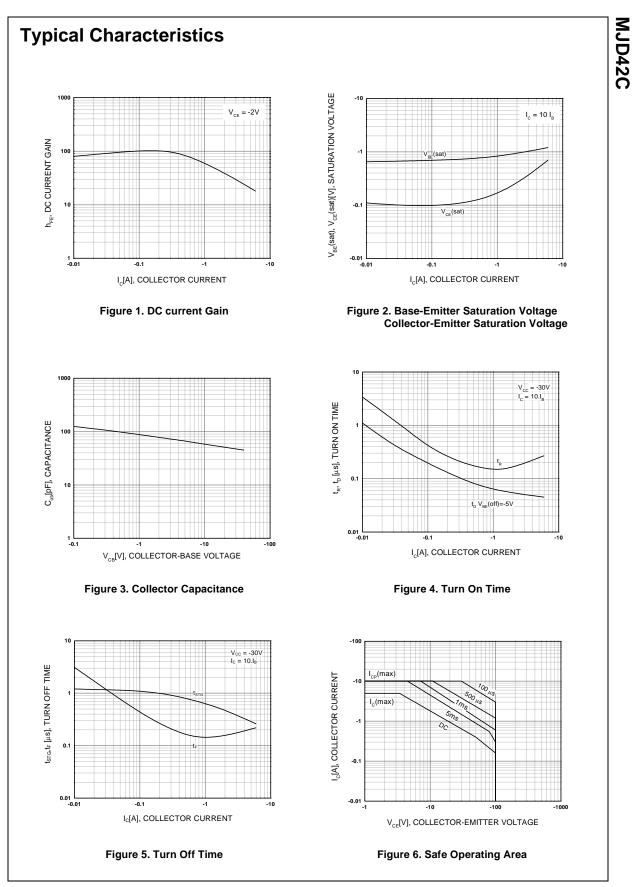
Symbol	Parameter	Value	Units
V <sub>CBO</sub>	Collector-Base Voltage	-100	V
V <sub>CEO</sub>	Collector-Emitter Voltage	-100	V
V <sub>EBO</sub>	Emitter-Base Voltage	-5	V
I <sub>C</sub>	Collector Current (DC)	-6	А
I <sub>CP</sub>	Collector Current (Pulse)	-10	А
I <sub>B</sub>	Base Current	-2	А
P <sub>C</sub>	Collector Dissipation (T <sub>C</sub> =25°C)	20	W
	Collector Dissipation (T <sub>a</sub> =25°C)	1.75	W
TJ	Junction Temperature	150	°C
T <sub>STG</sub>	Storage Temperature	- 65 ~ 150	°C

# Absolute Maximum Ratings T<sub>C</sub>=25°C unless otherwise noted

# Electrical Characteristics T<sub>C</sub>=25°C unless otherwise noted

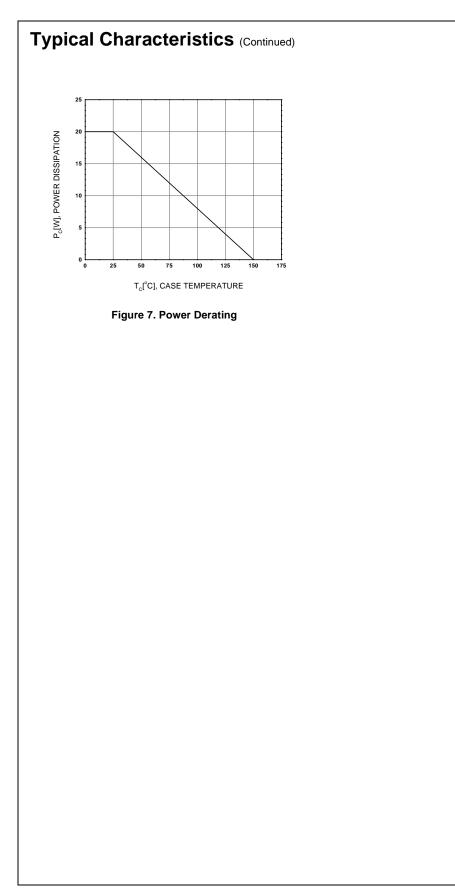
Symbol	Parameter	Test Condition	Min.	Max.	Units
V <sub>CEO</sub> (sus)	* Collector-Emitter Sustaining Voltage	I <sub>C</sub> = - 30mA, I <sub>B</sub> = 0	-100		V
I <sub>CEO</sub>	Collector Cut-off Current	$V_{CE} = -60V, I_B = 0$		-50	μΑ
I <sub>CES</sub>	Collector Cut-off Current	$V_{CE} = -100V, V_{BE} = 0$		-10	μΑ
I <sub>EBO</sub>	Emitter Cut-off Current	$V_{BE} = -5V, I_{C} = 0$		-0.5	mA
h <sub>FE</sub>	* DC Current Gain	$V_{CE} = -4V, I_{C} = -0.3A$	30		
		$V_{CE} = -4V, I_{C} = -3A$	15	75	
V <sub>CE</sub> (sat)	* Collector-Emitter Saturation Voltage	I <sub>C</sub> = -6A, I <sub>B</sub> = -600mA		-1.5	V
V <sub>BE</sub> (on)	* Base-Emitter ON Voltage	$V_{CE} = -6A, I_{C} = -4A$		-2	V
f <sub>T</sub>	Current Gain Bandwidth Product	V <sub>CE</sub> = -10V, I <sub>C</sub> = -500mA	3		MHz

\* Pulse Test: PW≤300µs, Duty Cycle≤2%

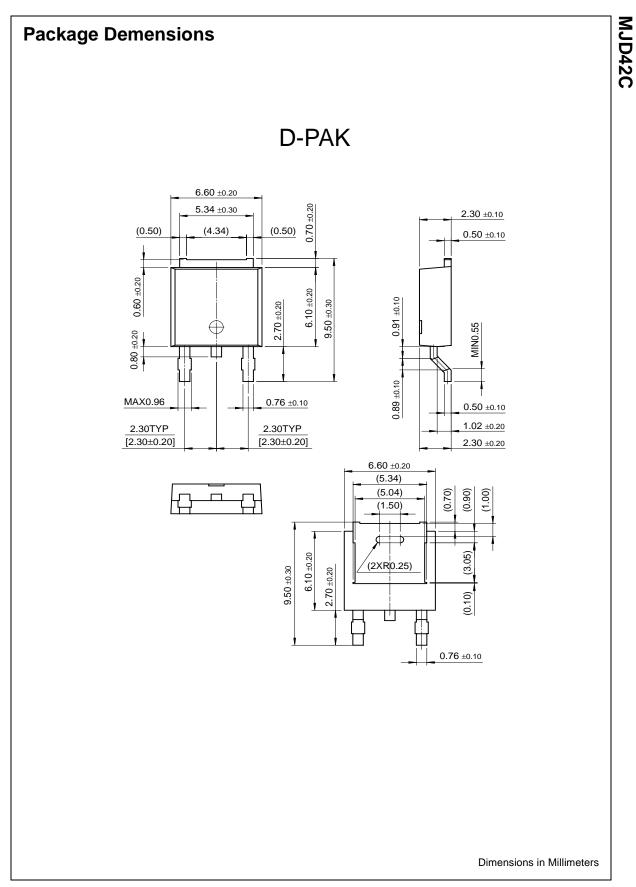


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