FAIRCHILD SEMICONDUCTOR™

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LCX Family Introduction

LCX is optimized for on-board buffering applications and offers mixed 2.5V/3.3V–5V capability by providing 5V over voltage tolerance on both the inputs and outputs. This family of popular gates, MSI, octal and 16-bit functions has maximum propagation delays similar to FAST® and FACT™. LCX also offers low CMOS power, low noise, and balanced output high-drive (±24 mA).

Features	Benefits
5V tolerant inputs and outputs.	Interfaces to 5V and/or 3V devices.
High speed (6.5 ns max delay for LCX244, 4.5 ns max. delay for LCX16244).	High speed for on-board buffering applications.
Very low static and dynamic power.	Saves power, extends battery life.
Low noise.	Low ground bounce, overshoot, undershoot, and EMI.
Extended 2.0V–3.6V V_{CC} supply voltage operation. (Guaranteed AC and DC Specifications from 2.3 - 3.6V).	Provides lower voltage operation without moving to a new family.
Balanced ±24 mA output drive.	Drives transmission lines down to 50Ω.
Power up/down high impedance inputs and outputs.	Facilitates power management and live insertion.
Gates, MSI, octal and 16-bit functions.	Broad portfolio and wide bit widths for on-board buffering applications.