

Discrete POWER & Signal **Technologies**

 $\frac{0.185(4.70)}{0.175(4.44)}$

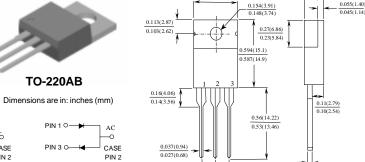
FEP16AT - FEP16JT

Features

- Low forward voltage drop.
- High surge current capacity.
- High current capability.

• High reliability.





0.105(2.67)





16 Ampere Glass Passivated Super Fast Rectifiers

Absolute Maximum Ratings*

T_A = 25°C unless otherwise noted

Suffix "D"

Symbol	Parameter	Value	Units	
lo	Average Rectified Current .375 " lead length @ T _A = 100°C	16	А	
İf(surge)	Peak Forward Surge Current 8.3 ms single half-sine-wave Superimposed on rated load (JEDEC method)	200	А	
P _D	Total Device Dissipation Derate above 25°C	8.33 66	W mW/°C	
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	15	°C/W	
$R_{\theta JL}$	Thermal Resistance, Junction to Lead	2.2	°C/W	
T _{stg}	Storage Temperature Range	-65 to +150	°C	
TJ	Operating Junction Temperature	-65 to +150	°C	

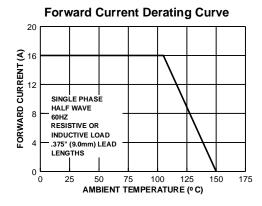
^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

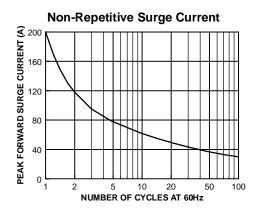
Electrical Characteristics

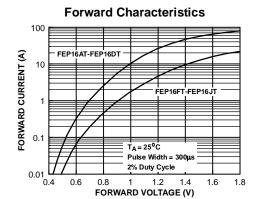
T_A = 25°C unless otherwise noted

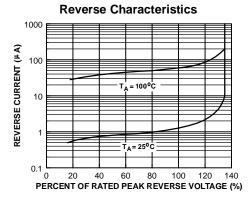
Paramet	Device							Units		
		16AT	16BT	16CT	16DT	16FT	16GT	16HT	16JT	
Peak Repetitive Reverse Voltage		50	100	150	200	300	400	500	600	V
Maximum RMS Voltage		35	70	105	140	210	280	350	420	V
DC Blocking Voltage	(Rated V _R)	50	100	150	200	300	400	500	600	V
Maximum Reverse Current @ rated V_R $T_A = 25^{\circ}C$ $T_A = 100^{\circ}C$		10 500						μΑ μΑ		
Maximum Reverse Red I _F = 0.5 A, I _R = 1.0	35 50					nS				
Maximum Forward Volt	0.95					1	.5	V		
Typical Junction Capac V _R = 4.0. f = 1.0 MH	85 60						pF			

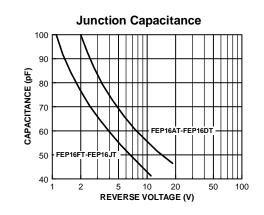
Typical Characteristics

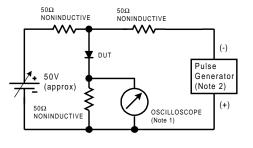


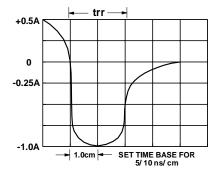












Reverse Recovery Time Characterstic and Test Circuit Diagram

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 $\begin{array}{lll} \mathsf{FACT} \ \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} & \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} \\ \mathsf{FAST}^{\otimes} & \mathsf{SuperSOT^{\mathsf{TM}}}\text{-}3 \\ \mathsf{FASTr^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}}\text{-}6 \\ \mathsf{GTO^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}}\text{-}8 \\ \mathsf{HiSeC^{\mathsf{TM}}} & \mathsf{TinyLogic^{\mathsf{TM}}} \end{array}$

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