



User Guide for FEB-L027 Evaluation Board

Reference Design Report for a 17.5W T8 LED Lamp Using FL7732

Featured Fairchild Product: FL7732

Direct questions or comments about this evaluation board to: "Worldwide Direct Support"

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Table of Contents

1.	Introduction	3
	1.1. General Description1.2. Features1.3. Internal Block Diagram	3 3 4
2.	General Specifications for Evaluation Board	5
3.	Evaluation Board	6
4.	Schematic	7
5.	Bill of Materials	8
6.	Transformer Design	10
7.	Electromagnetic Interference (EMI) Inductor (L1 & L2) Design	11
8.	Performance of Evaluation Board	12
	8.1. Test Condition & Equipments8.2. Startup	12 13
	8.3. Operation Waveforms	14
	8.4. Constant-Current Regulation.	15
	8.5. Short-LED/ Open-LED Protections	16
	8.6. System Efficiency	l / 10
	8.8 Operating Temperature	10
	8.9. Electromagnetic Interference (EMI)	20
9.	Revision History	21





The following user guide supports the evaluation kit for the FL7732. It should be used in conjunction with the FL7732 datasheet as well as Fairchild's application notes and technical support team. Please visit Fairchild's website at <u>www.fairchildsemi.com</u>.

1. Introduction

This document describes the proposed solution for a universal line voltage T8 LED lamp using the FL7732 Primary Side Regulator (PSR) single-stage controller. The input voltage range is $90V_{RMS} - 265V_{RMS}$. There is one DC output with a constant current of 500mA at $35V_{MAX}$. This document contains general description of FL7732, the power supply specification, schematic, bill of materials, and the typical operating characteristics.

1.1. General Description

The FL7732 is an active Power Factor Correction (PFC) controller using single-stage flyback topology. Primary-side regulation and single-stage topology reduce external components, such as input bulk capacitor and feedback circuitry, and minimize cost. To improve power factor and Total Harmonic Discharge (THD), constant on-time control is utilized with an internal error amplifier and a low-bandwidth compensator. Precise constant-current control regulates accurate output current, independent of input voltage and output voltage. Operating frequency is proportionally changed by output voltage to guarantee DCM operation with high efficiency and simple design. FL7732 provides open-LED, short-LED, and over-temperature protections.

1.2. Features

- Cost-Effective Solution: No Input Bulk Capacitor and Feedback Circuitry
- Power Factor Correction (PFC)
- Accurate Constant-Current (CC) Control
- Linear Frequency Control for Better Efficiency and Simpler Design
- Open-LED Protection
- Short-LED Protection
- Cycle-by-Cycle Current Limiting
- Over-Temperature Protection (OTP) with Auto Restart
- Low Startup Current: 20μA
- Low Operating Current: 5mA
- V_{DD} Under-Voltage Lockout (UVLO)
- Gate Output Maximum Voltage Clamped at 18V
- SOP-8 Package





1.3. Internal Block Diagram



Figure 1. Block Diagram of FL7732





2. General Specifications for Evaluation Board

Description		Symbol	Value	Comments
		V _{IN.MIN}	90V	Minimum Input Voltage
_	Voltage	V _{IN.MAX}	265V	Maximum Input Voltage
Input		V _{IN.NOMINAL}	110V / 220V	Nominal Input Voltage
	Frequency	f _{IN}	60Hz / 50Hz	Line Frequency
		Vout.min	17.5V	Minimum Output Voltage
	Voltage	V _{OUT.MAX}	37V	Maximum Output Voltage
Output		V _{OUT.NOMINAL}	35V	Nominal Output Voltage
Output		I _{OUT.NOMINAL}	500mA	Nominal Output Current
	Current		< ±2.59%	Line Input Voltage Change: 90~265V _{AC}
		CC Deviation	< ±1.82%	Output Voltage Change: 17.5~35V
		Eff _{90VAC}	87.02%	Efficiency at 90V _{AC} Line Input Voltage
		Eff _{120VAC}	Eff _{120VAC} 88.67% Efficiency at 120V _{AC} Line	
			89.22%	Efficiency at 140V _{AC} Line Input Voltage
	Inciency	Eff _{180VAC}	89.60%	Efficiency at 180V _{AC} Line Input Voltage
		Eff _{220VAC}	89.54%	Efficiency at 220V _{AC} Line Input Voltage
		Eff _{265VAC}	89.14%	Efficiency at 265V _{AC} Line Input Voltage
		PF / THD _{90VAC}	0.999 / 7.38%	PF / THD at 90V _{AC} Line Input Voltage
		PF / THD _{120VAC}	0.997 / 8.98%	PF / THD at $120V_{AC}$ Line Input Voltage
		PF / THD _{140VAC}	/ THD _{140VAC} 0.995 / 11.02% PF / THD at 140V _{AC} Line Input V	
		PF / THD _{180VAC}	0.990 / 13.87%	PF / THD at 180V _{AC} Line Input Voltage
		PF / THD _{220VAC}	0.978 / 16.63%	PF / THD at 220 V_{AC} Line Input Voltage
		PF / THD _{265VAC}	0.959 / 18.75%	PF / THD at $265V_{AC}$ Line Input Voltage
FL7732		T _{FL7732}	44.8°C	Open-Frame Condition ($T_A = 25^{\circ}C$) Temperature
Temperat	Primary MOSFET	T _{MOSFET}	59.6°C	Primary MOSFET Temperature
	Secondary Diode	T _{DIODE}	66.6°C	Secondary Diode Temperature
	Transformer	T _{TRANSFORMER}	64.5°C	Transformer Temperature

Table 1. Evaluation Board Specifications for LED Lighting Lamp

All data was measured with the board enclosed in a case and external temperature around $25C^{\circ}$.





3. Evaluation Board

Dimensions: 297.81 (L) × 17.14 (W) × 11.00 (H) [mm]



Evaluation Board





4. Schematic









5. Bill of Materials

ltem No.	Part Reference	Part Number	Qty.	Description	Manufacturer
1	C1, C2	MPE 400V473K 14S	2	47nF / 400V MPE Film Capacitor	Sungho
3	C3	C1206C332KDRACTU	1	3.3nF / 1kV SMD Capacitor 3216	Kemet
4	C4	KMG 22µF / 35V	1	22µF / 35V Electrolytic Capacitor	Samyoung
5	C5	C0805C104K5RACTU	1	100nF / 50V SMD Capacitor 2012	Kemet
7	C7	C0805C100J5GACTU	1	10pF / 50V SMD Capacitor 2012	Kemet
8	C8	C0805C225Z3VACTU	1	2.2µF / 25V SMD Capacitor 2012	Kemet
10	C10	C0805C101J5GACTU	1	100pF / 50V SMD Capacitor 2012	Kemet
11	C11	DEBB33A101KC1B	1	100pF / 1kV Ceramic Capacitor	Murata
12	C12, C13, C14	KMG 470µF / 50V	3	470µF / 50V Electrolytic Capacitor	Samyoung
15	CX1	MPX AC275V 473K	1	47nF / AC275V X-Capacitor	Carli
16	CY1	SCFz2E222M10BW	1	2.2nF / 250V Y-Capacitor	Samwha
17	D1, D2, D3, D4	IN4007	4	1000V / 1A General-Purpose Rectifier	Fairchild Semiconductor
18	D5, D6, D8, D9	UF4007	4	1000V / 1A Fast Rectifier	Fairchild Semiconductor
19	D7	1N4148	1	100V / 1A Small Signal Rectifier	Fairchild Semiconductor
21	F1	0672001.MXE	1	1A / 250V _{AC} Fuse	Littelfuse
22	L1	20mH	1	20mH	SPI Electronic
23	L2	40mH	1	40mH	SPI Electronic
24	L3	R08102KT00	2	1mH Filter Inductor, 8Ф	Bosung
25	L4	Jumper	1	5µH / Jump	
26	Q1	FDPF5N60NZ	1	4.5A / 600V Main MOSFET	Fairchild Semiconductor
27	R1, R2	RC1206JR-071ML	2	1MΩ SMD Resistor 3216	Yageo
28	R3, R4	RC1206JR-07100KL	2	100KΩ SMD Resistor 3216	Yageo
29	R5, R6, R7	RC1206JR-07750KL	3	750KΩ SMD Resistor 3216	Yageo
30	R8	RC1206JR-074R7L	1	4.7Ω SMD Resistor 3216	Yageo
31	R9	RC0805FR-07150KL	1	150KΩ SMD Resistor 2012	Yageo
32	R10	RC0805JR-0718KL	1	18KΩ SMD Resistor 2012	Yageo
33	R11	RC1206JR-0710RL	1	10Ω SMD Resistor 3216	Yageo
34	R12	RC0805JR-070RL	1	0Ω SMD Resistor 2012	Yageo
35	R13	RC0805JR-0710KL	1	10KΩ SMD Resistor 2012	Yageo
36	R14, R15	RC1206JR-071R5L	2	1.5Ω SMD Resistor 3216	Yageo
37	R16	RC1206FR-071RL	1	1.0Ω SMD Resistor 3216	Yageo
38	R17, R18	RC1206JR-0720RL	2	20Ω SMD Resistor 3216	Yageo
39	R19	RC1206JR-0747KL	1	47kΩ SMD Resistor 3216	Yageo
41	RV1	SVC 471 D-10A	1	Metal Oxide Varistor	Samwha
42	T1	PG1054.410NL (1150-CDC-MX4)	1	Transformer, 585µH	Pulse Electronics





Bill of Materials (Continued)

ltem No.	Part Reference	Part Number	Qty.	Description	Manufacturer
43	U1	FL7732_F116	1	Main Controller	Fairchild Semiconductor





6. Transformer Design

	e	Pulse			
NOTES: UNLESS OTHERWISE SPECIFIE . NOTCE: THIS IS A ROUS COMPONENT/REQUET ROUPONENT/REQUET RUND LALL ENVIRONMENTER CH BY THE DESIGN CENT . SOLERABLITY: CONTONES TO A . SOLERABLITY: CONTONES TO A . SOLERABLITY: CONTONES TO A . JERCE NOTURE: HALL . JERCE NOTURE: HALL . JERCE NOTURE: MILLINETERS] A . XX = $\pm 01 \pm 0.22$. XXX = \pm) PLIANT LIGES JFROWL ATERIAL WITH FLAMMABILITY WSI/J-STD-002, IPC/EN J-STD-003A. T0 +125'G LLIMETERS] WITH THE FOLLOWING REF FOR FEFERENCE ONLY. LOOS[±0.13]	UNLESS (2	SCHEMATIC	C.
		TURNS RAT PARAMETER OPERATING TEMP POLARITY	(IO LIMITS ARE SPEC	CIFED AS MEASURED WITH UNGAPPED SPECIFICATIONS -40°C ~ 125°C PER SCHEMATIC -2.222 ±2% (100%, @AT 100 KHz,	0.1 VRMS)
		TURNS RATIO INDUCTANCE LEAKAGE INDUCTANCE	(2-1) : (4-5) = (2-1) = 585 uH (2-1) = 6.0 uH WAX W	4.444 ±2% (100%, @AT 100 KHz, 0 ±15% (100%, @ AT 100 KHz, 0.1 VF MTH OTHER WINDING SHORTED (100%, @ AT 100	3.1 VRMS) 3MS) KHz, 0.1 VRNS)
		DCR	(2-1) = 1.75 OHM (8-9) = 0.40 OHM (4-5) = 0.45 OHM (1.2.4.5) TO (8,9) =	AS MAXIMUM (SAMPLE) AS MAXIMUN (SAMPLE) MS MAXIMUN (SAMPLE) 3K VAC FOR 1 MINUTE OR 3.3K VAC, FOR 6	S (100%)
& Copyright, 2010. Pulse Electro PULSE CONFIDENTIAL	anica Corp. All rights reserved. Drawing specif PRODUCT DESCRIPTION	fications subject to changer	ge without notice. ((12/2/11)	REV.
PROPRIETARY	X'FMR FLYBACK EEL12 TH		1	PG1054.410NL	MX4
	e	Pulse		.118±.008	



Figure 5. Transformer Bobbin Structure and Pin Configuration

Table 2. Electrical Characteristics

	Pin	Specifications	Remark
Inductance	2 – 1	585µH ± 10%	100kHz, 0.1V
Leakage	2 – 1	6µH	100kHz, 0.1V Short All Output Pins





7. Electromagnetic Interference (EMI) Inductor (L1 & L2) Design



Figure 6. Transformer Bobbin Structure and Pin Configuration

Table 3.	Winding	Specifications
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Part Ref.	Winding	Pin (S → F)	Wire	Turns	Inductance
1.4	N1	1 → 3	0.25 Φ (2UEW)	68.5	22mH Min.
LI	N2	$2 \rightarrow 4$	0.25 Φ (2UEW)	68.5	22mH Min.
1.0	N1	1 → 3	0.25 Φ (2UEW)	74.5	50mH Min.
L2	N2	2 → 4	0.25 Φ (2UEW)	74.5	50mH Min.

Table 4. Material List

No.	ltem	Material	Manufacturer	UL No.
1	PCB	CEM1	CHANG CHUN PLASTICS	E80513(S)
2	Core	R8K	VAKOS INDUSTRIES	
		TYPU-130	TA YA ELECTRIC WIRE & CABLE	E84201(S)
3	Wire	TYA1-130	TA YA ELECTRIC WIRE & CABLE	E84201(S)
		UEW-U	SIAM PACIFIC ELECTRIC WIRE & CABLE	E196993
4	Varnish	468-2(+)	RIPLEY RESIN ENGINEERING	E81777
5	Fnoxy	H907	DONG GUAN HUA CHUANG CHEMICALINDUSTRY	
C		G-9008	GUDAK CHEMISTRY TECH	





8. Performance of Evaluation Board

8.1. Test Condition & Equipments

Ambient Temperature	T _A = 25°C
Test Equipment	AC Power Source: ES2000S by PSTATIONES Power Analyzer: PZ4000 by YOKOGAWA Multi Meter: 2002 by KEITHLEY : 8842A by LIKE Oscilloscope: WaveRunner 104Xi by LeCroy EMI Test Receiver: ESCS30 by ROHDE & SCHWARZ Two-Line V-Network: ENV216 by ROHDE & SCHWARZ Thermometer: Therma CAM SC640 by FLIR SYSTEMS LED: EHP-AX08EL/GT01H-P03(3W) by Everlight





8.2. Startup

Startup time is 1.3s ($V_{IN} = 90V_{AC}$) ~ 330ms ($V_{IN} = 265V_{AC}$)



Figure 7. $V_{IN} = 90V_{AC} / 60Hz$, Startup Time at 10-LED (35V/500mA); C1 [V_{DD}], C2 [V_{IN}], C3 [V_{OUT}], C4 [I_{OUT}]

Figure 8. $V_{IN} = 115V_{AC} / 60Hz$ Startup Time at 10-LED (35V/500mA); C1 [V_{DD}], C2 [V_{IN}], C3 [V_{OUT}], C4 [I_{OUT}],





Figure 9. $V_{IN} = 230V_{AC} / 50Hz$, Startup Time at 10-LED (35V/500mA); C1 [V_{DD}], C2 [V_{IN}], C3 [V_{OUT}], C4 [I_{OUT}]

Figure 10. $V_{IN} = 265V_{AC} / 50Hz$, Startup Time at 10-LED (35V/500mA); C1 [V_{DD}], C2 [V_{IN}], C3 [V_{OUT}], C4 [I_{OUT}]





8.3. Operation Waveforms

Output current ripple is under 115mAp-p with a rated output current of 500mA.



Figure 11. V_{IN}= 90V_{AC} / 60Hz, Operation

Waveforms at 10-LED (35V/500mA); C1



Figure 12. $V_{IN} = 115V_{AC} / 60Hz$ Operation Waveforms at 10-LED (35V/500mA); C1 [V_{CS}], C2 [V_{IN}], C3 [V_{OUT}], C4 [I_{OUT}]





Figure 13. V_{IN} = 220V_{AC} / 50Hz, Operation Waveforms at 10-LED (35V/500mA); C1 [V_{CS}], C2 [V_{IN}], C3 [V_{OUT}], C4 [I_{OUT}]

Figure 14. $V_{IN} = 265V_{AC} / 50Hz$, Operation Waveforms at 10-LED (35V/500mA); C1 [V_{CS}], C2 [V_{IN}], C3 [V_{OUT}], C4 [I_{OUT}]





8.4. Constant-Current Regulation

Constant-current deviation in the wide output voltage range from 5-LED (17.5V) to 10-LED (35V) is less than $\pm 1.82\%$ at each line input voltage. Line regulation is less than $\pm 2.59\%$. The results were measured using LED load.





Output Current [A]



Table 5.	Constant-Current Regulation	by Output	Voltage Change	(17.5V ~ 35	V)
	Constant Surront Regulation	Sy Output	Vollago Ollango	(11101 00	•

Input Voltage	Min. Current [A]	Max. Current [A]	Tolerance
90V _{AC} [60Hz]	0.501	0.515	±1.36%
120V _{AC} [60Hz]	0.502	0.521	±1.82%
140V _{AC} [60Hz]	0.501	0.506	±0.49%
180V _{AC} [50Hz]	0.496	0.501	±0.50%
220V _{AC} [50Hz]	0.488	0.497	±0.91%
265V _{AC} [50Hz]	0.485	0.5	±1.50%

Table 6.	Constant-Current Regula	tion by Line Volt	age Change (90	~ 265V _{AC})
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Output Voltage	90V _{AC}	120V _{AC}	140V _{AC}	180V _{AC}	220V _{AC}	265V _{AC}	Tolerance
35V	0.515A	0.521A	0.506A	0.50A	0.494A	0.50A	±2.59%
31.5V	0.514A	0.512A	0.505A	0.496A	0.491A	0.498A	±2.24%
28V	0.509A	0.510A	0.502A	0.501A	0.488A	0.490A	±2.16%





LeCroy

8.5. Short-LED/ Open-LED Protections

In short-LED condition, the OCP level is reduced from 0.7V to 0.2V because the FL7732 lowers the OCP level when the V_S voltage is less than 0.4V during output diode conduction time. The results are measured using actual LED load.



Figure 16. $V_{IN} = 90V_{AC} / 60Hz$, Short-LED Condition; C1 [V_{DD}], C2 [V_{IN}], C3 [V_{OUT}], C4 [lout]

Figure 17. $V_{IN} = 265V_{AC} / 50Hz$, Short-LED Condition; C1 [V_{DD}], C2 [V_{IN}], C3 [V_{OUT}], C4 [I_{OUT}]

In open-LED condition, output voltage is limited around 30V by OVP in V_{DD} . Output over-voltage protection level can be controlled by the turn ratio of the auxiliary and secondary windings.



Figure 18. $V_{IN} = 90V_{AC} / 60Hz$, Open-LED Condition; C1 [V_{DD}], C2 [V_{IN}], C3 [V_{OUT}], C4 [I_{OUT}]



Figure 19. $V_{IN} = 265V_{AC} / 60Hz$, Open-LED Condition; C1 [V_{DD}], C2 [V_{IN}], C3 [V_{OUT}], C4 [I_{OUT}]





8.6. System Efficiency

System efficiency is $87.02\% \sim 89.6\%$ in $90 \sim 265 V_{AC}$ input voltage range. The results were measured after 30 minutes since startup by using LED load.

Efficiency



Input Voltage Figure 20. System Efficiency

Table 7. System Efficiency

Input Voltage	Input Power [W]	Output Current [A]	Output Voltage [V]	Output Power [W]	Efficiency
90V _{AC} [60Hz]	20.380	0.509	34.83	17.735	87.02%
120V _{AC} [60Hz]	20.219	0.514	34.88	17.928	88.67%
140V _{AC} [60Hz]	19.825	0.508	34.79	17.687	89.22%
180V _{AC} [50Hz]	19.453	0.503	34.68	17.430	89.60%
220V _{AC} [50Hz]	19.065	0.494	34.55	17.071	89.54%
265V _{AC} [50Hz]	19.198	0.495	34.55	17.113	89.14%





8.7. Power Factor & Total Harmonic Distortion

FL7732 shows excellent THD performance. THD is much less than 30% of the specification. Power factor is very high with enough margin from 0.9. The results were measured 30 minutes after startup.



Figure 21. Power Factor & Total Harmonic Distortion

Table 8.	Power Factor	& Total	Harmonic	Distortion
		a rotai	nannonic	Distortion

Input Voltage	PF	THD
90V _{AC} [60Hz]	0.999	7.38%
120V _{AC} [60Hz]	0.997	8.98%
140V _{AC} [60Hz]	0.995	11.02%
180V _{AC} [50Hz]	0.990	13.87%
220V _{AC} [50Hz]	0.978	16.63%
265V _{AC} [50Hz]	0.959	18.75%





8.8. Operating Temperature

Temperature of the all components on this board is less than 67°C.

The results were measured 60 minutes after startup.



Spot 43.1 °C Bottom 69.9 Max, 59.2 Circle Max, 55.3 Circle



Figure 23. Board Temperature - VIN [265VAC / 50Hz], 10-LED (35V / 500mA)

24.0





8.9. Electromagnetic Interference (EMI)

All measurements were conducted in observance of EN55022 criteria.

The results were measured 30 minutes after startup.



Figure 24. EMI Results – 10-LED (35V/500mA), Conduction Line; VIN = 220VAC



Figure 25. EMI Results – 10-LED (35V/500mA), Conduction Neutral; VIN = 220VAC





9. **Revision History**

Rev.	Date	Description
1.0.0	July 2011	Initial Release

WARNING AND DISCLAIMER

Replace components on the Evaluation Board only with those parts shown on the parts list (or Bill of Materials) in the Users' Guide. Contact an authorized Fairchild representative with any questions.

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