

# RazerThin® LEDs CxxxRT230-S0200

Cree's RazerThin LEDs combine highly efficient InGaN materials with Cree's proprietary G•SiC® substrate to deliver superior price/performance for blue and green LEDs. These vertically structured LED chips are small in size and require a low forward voltage. Cree's RT series chips are tested for conformity to optical and electrical specifications and the ability to withstand 1000 V ESD. Applications include keypad backlighting where sub-miniaturization and thinner form factors are required.

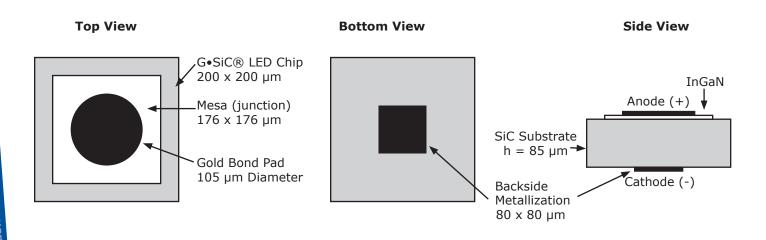
#### **FEATURES**

- Small Chip 200 x 200 x 85 μm
- Low Forward Voltage
  - 3.1 V Typical at 20 mA Blue & Traffic Green
  - 3.2 V Typical at 20 mA Green
- RT230™ LED Performance
  - 2.0 mW min. Blue
  - 1.5 mW min. Green
- Single Wire Bond Structure
- Class 2 ESD Rating

### **APPLICATIONS**

- Mobile Phone Key Pads
  - Blue LEDs
  - White LEDs
- Audio Product Display Lighting
- Mobile Appliance Keypads
- Automobile Dashboard Lighting

## CxxxRT230-S0200 Chip Diagram





| Maximum Ratings at T <sub>A</sub> = 25°C Notes 183           | CxxxRT230-S0200 |
|--|-----------------|
| DC Forward Current   | 30 mA           |
| Peak Forward Current (1/10 duty cycle @ 1kHz)                | 100 mA          |
| LED Junction Temperature                                     | 125°C           |
| Reverse Voltage  | 5 V             |
| Operating Temperature Range                                  | -40°C to +100°C |
| Storage Temperature Range                                    | -40°C to +100°C |
| Electrostatic Discharge Threshold (HBM) Note 2               | 1000 V          |
| Electrostatic Discharge Classification (MIL-STD-883E) Note 2 | Class 2         |

| Typical Electrical/Optical Characteristics at $T_A = 25$ °C, If = 20 mA Note 3 |                                      |      |                      |                                   |  |  |  |  |
|--|--------------------------------------|------|----------------------|-----------------------------------|--|--|--|--|
| Part Number  | Forward Voltage (V <sub>i</sub> , V) |      | (V <sub>f</sub> , V) | Reverse Current<br>[I(Vr=5V), μA] | Full Width Half Max $(\lambda_{ m p},{ m nm})$ |  |  |  |
|  | Min.                                 | Тур. | Max.                 | Max.                              | Тур.   |  |  |  |
| C460RT230-S0200  | 2.7                                  | 3.1  | 3.7                  | 2                                 | 21   |  |  |  |
| C470RT230-S0200  | 2.7                                  | 3.1  | 3.7                  | 2                                 | 22   |  |  |  |
| C505RT230-S0200  | 2.7                                  | 3.1  | 3.7                  | 2                                 | 30   |  |  |  |
| C527RT230-S0200  | 2.7                                  | 3.2  | 3.7                  | 2                                 | 35   |  |  |  |

| Mechanical Specifications    | CxxxRT230-S0200 |           |  |
|------------------------------|-----------------|-----------|--|
| Description                  | Dimension       | Tolerance |  |
| P-N Junction Area (μm)       | 176 x 176       | ± 25      |  |
| Top Area (μm)                | 200 x 200       | ± 25      |  |
| Bottom Area (µm)             | 200 x 200       | ± 25      |  |
| Chip Thickness (µm)          | 85              | ± 10      |  |
| Au Bond Pad Diameter (μm)    | 105             | -5, +15   |  |
| Au Bond Pad Thickness (µm)   | 1.2             | ± 0.5     |  |
| Back Contact Metal Area (µm) | 80 x 80         | ± 25      |  |

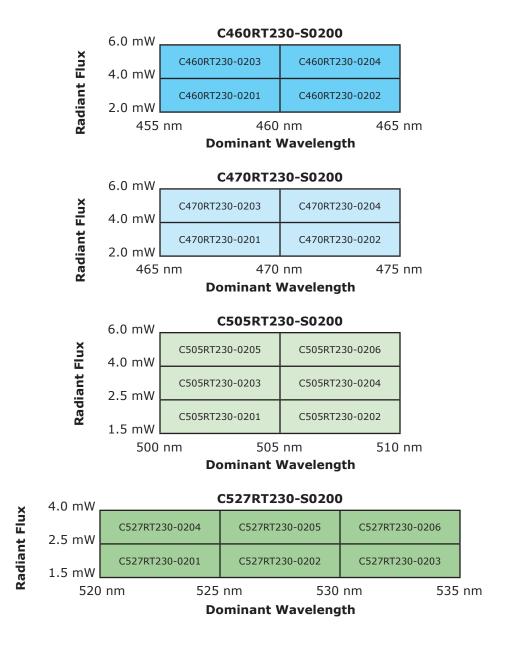
#### Notes:

- 1. Maximum ratings are package dependent. The above ratings were determined using a T-1 3/4 package (with Hysol OS4000 epoxy) for characterization. Ratings for other packages may differ. The forward currents (DC and Peak) are not limited by the die but by the effect of the LED junction temperature on the package. The junction temperature limit of 125°C is a limit of the T-1 3/4 package; junction temperature should be characterized in a specific package to determine limitations. Assembly processing temperature must not exceed 325°C (< 5 seconds).
- 2. Product resistance to electrostatic discharge (ESD) according to the HBM is measured by simulating ESD using a rapid avalanche energy test (RAET). The RAET procedures are designed to approximate the maximum ESD ratings shown. The RAET procedure is performed on each die. The ESD classification of Class 2 is based on sample testing according to MIL-STD-883E.
- 3. All products conform to the listed minimum and maximum specifications for electrical and optical characteristics when assembled and operated at 20 mA within the maximum ratings shown above. Efficiency decreases at higher currents. Typical values given are within the range of average values expected by manufacturer in large quantities and are provided for information only. All measurements were made using lamps in T-1 3/4 packages (with Hysol OS4000 epoxy). Optical characteristics measured in an integrating sphere using Illuminance E.
- 4. Caution: To obtain optimum output efficiency, the amount of epoxy used should be characterized based upon the specific application.
- 5. Specifications are subject to change without notice.



## Standard Bins for CxxxRT230-S0200

LED chips are sorted to the **radiant flux** and **dominant wavelength** bins shown. Sorted die sheets contain die from only one bin. Sorted die kit (CxxxRT230-S0200) orders may be filled with any or all bins (CxxxRT230-0xxx) contained in the kit. All radiant flux values are measured at If = 20 mA and all dominant wavelength values are measured at If = 20 mA.





## **Characteristic Curves**

These are representative measurements for the RT230 product. Actual curves will vary slightly for the various radiant flux and dominant wavelength bins.

