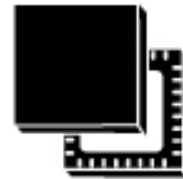


Near field communication transceiver

Data brief

Features

- Operating modes supported:
 - Reader/Writer
 - Card Emulation
 - Peer-to-Peer
- Hardware features
 - Dedicated internal frame controller
 - Highly integrated Analog Front End (AFE) for RF communications
 - Transmission and reception modes
- RF communication @13.56 MHz
 - ISO/IEC 14443 Type A and B in Reader and Card Emulation modes
 - ISO/IEC 15693 in Reader mode
 - ISO/IEC 18092 in Reader and Card Emulation modes
- Communication interfaces with a Host Controller
 - Serial peripheral interface (SPI) Slave interface up to 2 Mbps
 - Universal asynchronous receiver/transmitter (UART) up to 2 Mbps
- 32-lead, 5x5 mm, very thin fine pitch quad flat (VFQFPN) ECOPACK® package

**VFQFPN32 5x5 mm**

Applications

Typical protocols supported:

- ISO/IEC 14443-3 Type A and B cards and tags
- ISO/IEC 15693 and ISO/IEC 18000-3M1 tags
- NFC Forum tags: Types 1, 2, 3 and 4
- ST Dual Interface EEPROM

Typical STRFNFCA applications include:

- Handheld readers (OTP, PIN pad, POS)
 - E-payment, physical access control, transport, and government
- PC-Link (USB /Serial/PCMCIA)
 - E-payment, security access & authentication, data exchange)
- USB token
 - Security access & authentication, data exchange
- Integrated solution (chipset)
- Keyboard, laptop, set top box, printer, TV, etc.
- E-payment, data exchange, Bluetooth pairing, security access

1 Description

The STRFNFCA is an integrated transceiver IC for contactless applications.

The STRFNFCA manages frame coding and decoding in Reader, Card Emulation and Peer-to-Peer modes for standard applications such as near field communication (NFC), proximity and vicinity standards.

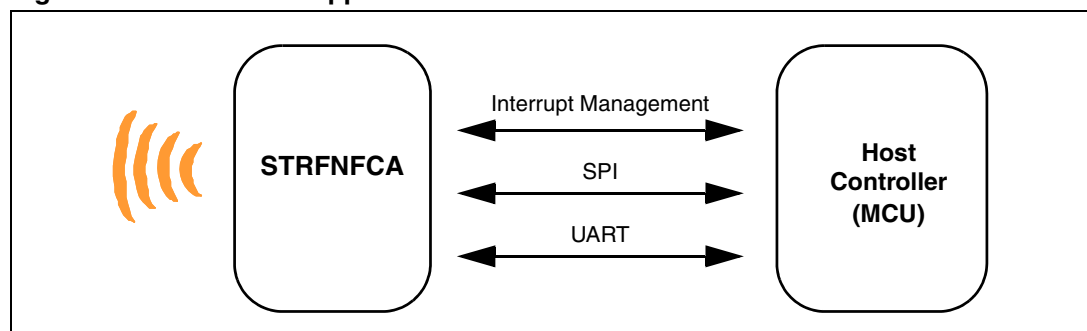
The STRFNFCA embeds an Analog Front End to provide the 13.56 MHz Air Interface.

The STRFNFCA supports ISO/IEC 14443 Type A and B in Reader and Card Emulation modes, ISO/IEC 15693 (single or double subcarrier in Reader mode only) and ISO/IEC 18092 protocols in Reader and Card Emulation modes.

The STRFNFCA also supports the reading of NFC Forum Type 1, 2, 3 and 4 tags.

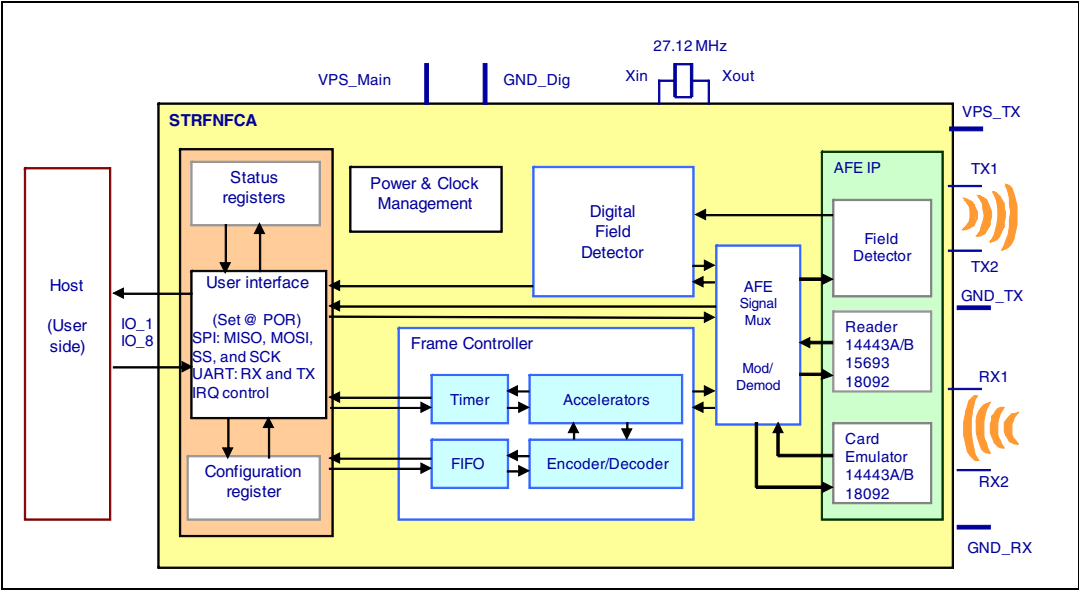
In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK® packages, depending on their level of environmental compliance. ECOPACK® specifications, grade definitions and product status are available at: www.st.com. ECOPACK® is an ST trademark.

Figure 1. STRFNFCA application overview



1.1 Block diagram

Figure 2. STRFNFCA block diagram



1.2 List of terms

Table 1. List of terms

Term	Meaning
DAC	Digital analog converter
GND	Ground
HFO	High frequency oscillator
LFO	Low frequency oscillator
MCU	Microcontroller unit
RFU	Reserved for future use
SPI	Serial peripheral interface
t _L	Low frequency period
t _{REF}	Reference time
UART	Universal asynchronous receiver-transmitter
WFE	Wait for event

2 Pin and signal descriptions

Figure 3. Pinout description

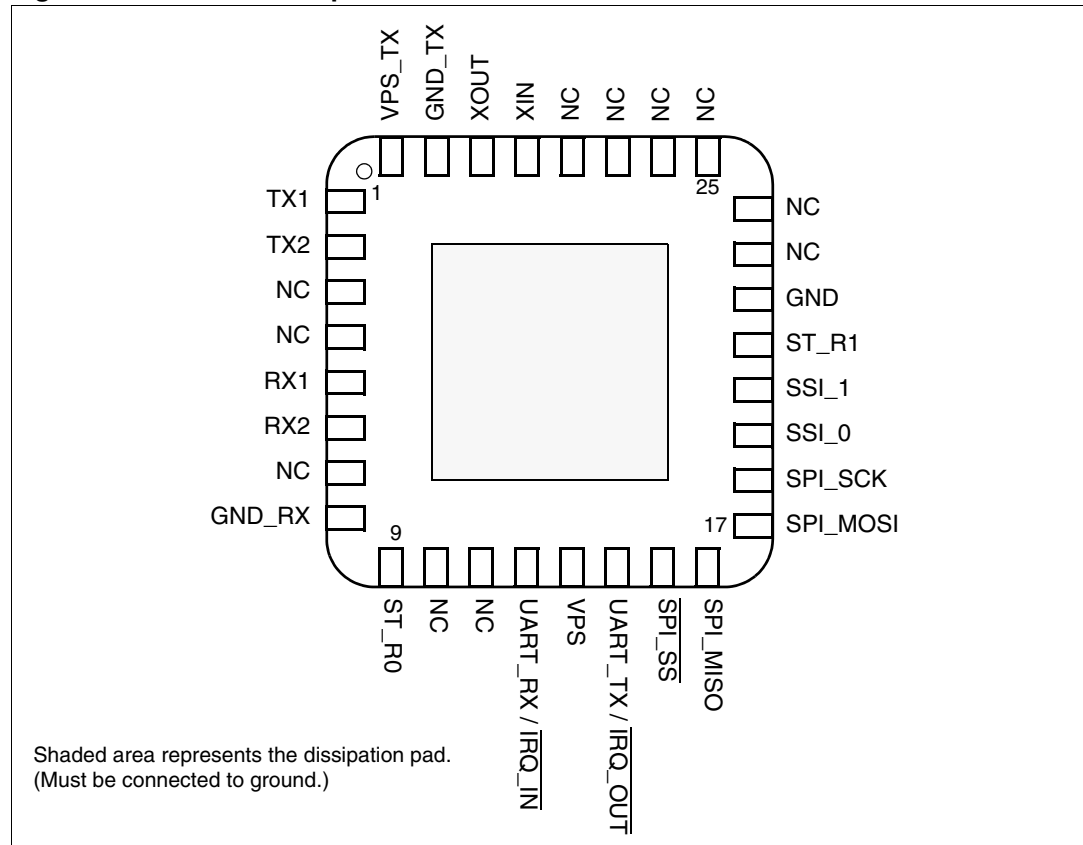


Table 2. Pin descriptions

Pin	Pin name	Type ⁽¹⁾	Main function	Alternate function
1	TX1	O	Driver output 1	
2	TX2	O	Driver output 2	
3	NC		Not connected	
4	NC		Not connected	
5	RX1	I	Receiver input 2	
6	RX2	I	Receiver input 1	
7	NC		Not connected	
8	GND_RX	P	Ground (analog)	
9	ST_R0	O	ST Reserved ⁽²⁾	
10	NC		Not connected	
11	NC		Not connected	
12	UART_RX / $\overline{\text{IRQ_IN}}$ ⁽²⁾	I	UART receive pin	Interrupt input
13	VPS	P	Main power supply	

Table 2. Pin descriptions (continued)

Pin	Pin name	Type (1)	Main function	Alternate function
14	UART_TX / $\overline{\text{IRQ_OUT}}$	O	UART transmit pin	Interrupt output
15	$\overline{\text{SPI_SS}}$	I	SPI Slave Select (active low)	
16	SPI_MISO	O	SPI Data, Slave Output ⁽³⁾	
17	SPI_MOSI	I	SPI Data, Slave Input ⁽³⁾	
18	SPI_SCK	I	SPI serial clock	
19	SSI_0	I	Select serial communication interface	
20	SSI_1	I	Select serial communication interface	
21	ST_R1	I	ST Reserved ⁽⁴⁾	
22	GND	P	Ground (digital)	
23	NC		Not connected	
24	NC		Not connected	
25	NC		Not connected	
26	NC		Not connected	
27	NC		Not connected	
28	NC		Not connected	
29	XIN		Crystal oscillator input	
30	XOUT		Crystal oscillator output	
31	GND_TX	P	Ground (RF drivers)	
32	VPS_TX	P	Power supply (RF drivers)	

1. I: Input, O: Output, and P: Power

2. Must add a capacitor to ground.

3. Must not be left floating.

4. Must be connected to V_{PS} .

3 Command summary

3.1 Command format

Fields <Cmd>, <RespCode> and <Len> are always 1 byte long. <Data> can be from 0 to 255 bytes.

- Direction: MCU to STRFNFCA
<CMD><Len><Data>
- Direction: STRFNFCA to MCU
<RespCode><Len><Data>

3.2 List of commands

[Table 3](#) lists the command set available for standard use.

Table 3. List of STRFNFCA commands

Code	Command	Description
01	IDN	Requests short information about STRFNFCA and its firmware version.
02	PROTOCOL SELECT	Select communication protocol and specify some protocol-related parameters.
03	POLL FIELD	Returns the current value of FieldDet flag.
04	SENDRECV	Sends data using previously selected protocol and receive the response of TAG.
05	LISTEN	Listens for the data using previously selected protocol.
06	SEND	Sends data using previously selected protocol.
08	RDREG	Read the Configuration register.
09	WRREG	Writes to the Configuration register.
0A	BAUDRATE	Sets UART baud rate.
0D	AC FILTER	Enables or disables the Anti-collision filter.
55	ECHO	STRFNFCA performs a serial interface ECHO command (reply data 0x55 or stops the Listening state when a listen command has been sent without error).
Other codes		ST Reserved

4 Electrical characteristics

4.1 Absolute maximum ratings

Table 4. Absolute maximum ratings

Symbol	Parameter	Value	Unit
VPS_Main	Supply voltage	−0.3 to 7.0	V
VPS_TX	Supply voltage (RF drivers)	−0.3 to 7.0	V
V _{IO}	Input or output voltage relative to ground	−0.3 to VPS_Main +0.3	V
V _{MaxCarrier}	Maximum input voltage (pins RX1 and RX2)	±14.0	V
T _A	Ambient operating temperature	−25 to +85	°C
	Ambient operating temperature (RF mode)	0 to +50	
T _{STG}	Storage temperature (Please also refer to package specification).	−65 to +150	°C
V _{ESD}	Electrostatic discharge voltage according to JESD22-A114, Human Body Model	2000	V
P _{TOT} ⁽¹⁾	Total power dissipation per package	1	W

1. Depending on the thermal resistance of package.

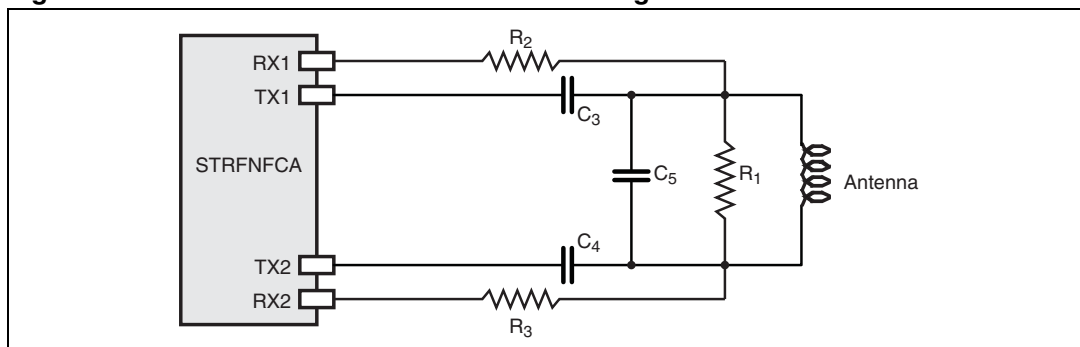
Note: *Stresses listed above may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.*

5 Evaluation kit environment

The Evaluation kit includes a complete range of hardware and software components from STMicroelectronics and third-party suppliers to demonstrate the efficiency of the STRFNFCA transceiver and help you to develop and debug your applications and evaluate STMicroelectronics RFID products.

- Tools and design kit
- An evaluation board including an example code
- A set of complete documentation including user manual and antenna recommendations

Figure 4. Reader and Card mode schematic diagram



6 Revision history

Table 5. Document revision history

Date	Revision	Changes
28-Jul-2010	1	Initial release.
15-Sep-2010	2	Removed pin descriptions and command summary.
14-Jun-2011	3	Updated communication interface frequencies.
01-Jul-2011	4	Removed Restricted Distribution document classification.
30-Aug-2011	5	Assigned document publication information.
16-Dec-2011	6	Modified Features . Updated Table 3: List of STRNFCA commands and Table 4: Absolute maximum ratings . Added Section 2: Pin and signal descriptions .

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