

## **STPS1045B**

## Power Schottky rectifier

### **Features**

- Negligible switching losses
- Low forward voltage drop
- Low capacitance
- High reverse avalanche surge capability
- Avalanche specification

### **Description**

High voltage dual Schottky rectifier suited for switch mode power supplies and other power converters.

Packaged in DPAK, this device is intended for use in high frequency circuitries where low switching losses are required.

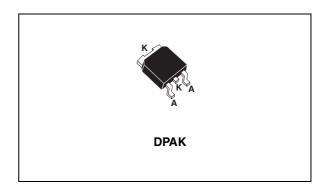


Table 1. Device summary

| Symbol               | Value  |
|----------------------|--------|
| I <sub>F(AV)</sub>   | 10 A   |
| V <sub>RRM</sub>     | 45 V   |
| Tj                   | 175 °C |
| V <sub>F</sub> (max) | 0.57 V |

**Characteristics STPS1045B** 

#### **Characteristics** 1

Table 2. **Absolute maximum ratings** 

| Symbol                       | Parameter   | Value         | Unit |   |
|------------------------------|---|---------------|------|---|
| $V_{RRM}$                    | Repetitive peak reverse voltage                                       |               | 45   | V |
| I <sub>F(RMS)</sub> /<br>pin | Forward rms current   |               | 7    | Α |
| I <sub>F(AV)</sub>           | Average forward current $T_c = 150  ^{\circ}\text{C}  \delta = 0.5$   |               | 10   | Α |
| I <sub>FSM</sub>             | Surge non repetitive forward current $t_p = 10 \text{ ms sinusoidal}$ |               | 75   | Α |
| I <sub>RRM</sub>             | Repetitive peak reverse current $t_p = 2 \mu s F = 1 kHz$             |               | 1    | Α |
| P <sub>ARM</sub>             | Repetitive peak avalanche power                                       | 4000          | W    |   |
| T <sub>stg</sub>             | Storage temperature range   | - 65 to + 150 | °C   |   |
| T <sub>j</sub>               | Maximum operating junction temperature                                | 175           | °C   |   |
| dV/dt                        | Critical rate of rise of reverse voltage                              | 10000         | V/µs |   |

 $<sup>\</sup>frac{dPtot}{dTj} < \frac{1}{Rth(j-a)}$  condition to avoid thermal runaway for a diode on its own heatsink

Table 3. Thermal parameters

| Symbol               | Parameter        | Value | Unit |
|----------------------|------------------|-------|------|
| R <sub>th(j-c)</sub> | Junction to case | 3     | °C/W |

Table 4. Static electrical characteristics

| Symbol  | Parameter               | Test conditions         |                       | Min. | Тур. | Max. | Unit |
|---|-------------------------|-------------------------|-----------------------|------|------|------|------|
| I <sub>R</sub> <sup>(1)</sup> Reverse leakage current | Reverse leakage         | T <sub>j</sub> = 25 °C  | V- <b>-</b> V         |      |      | 100  | μΑ   |
|   | T <sub>j</sub> = 125 °C | $V_R = V_{RRM}$         |                       | 7    | 15   | mA   |      |
| V <sub>F</sub> <sup>(2)</sup> Fo                      | Forward voltage drop    | T <sub>j</sub> = 25 °C  | I <sub>F</sub> = 10 A |      |      | 0.63 |      |
|   |                         | T <sub>j</sub> = 125 °C |                       |      | 0.50 | 0.57 | V    |
|   |                         | T <sub>j</sub> = 25 °C  | I <sub>F</sub> = 20 A |      |      | 0.84 | •    |
|   |                         | T <sub>j</sub> = 125 °C |                       |      | 0.65 | 0.72 |      |

<sup>1.</sup> Pulse test:  $tp = 5 \text{ ms}, \delta < 2\%$ 

To evaluate the conduction losses use the following equation: P = 0.42 x  $I_{F(AV)}$  + 0.015  $I_{F}^{2}_{(RMS)}$ 

$$P = 0.42 \times I_{F(AV)} + 0.015 I_{F}^{2}(RMS)$$

<sup>2.</sup> Pulse test: tp = 380  $\mu$ s,  $\delta$  < 2%

STPS1045B Characteristics

Figure 1. Average forward power dissipation Figure 2. Average forward current versus versus average forward current ambient temperature ( $\delta$ : 0.5)

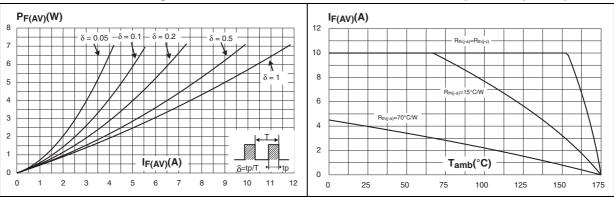


Figure 3. Normalized avalanche power derating versus pulse duration

Figure 4. Normalized avalanche power derating versus junction temperature

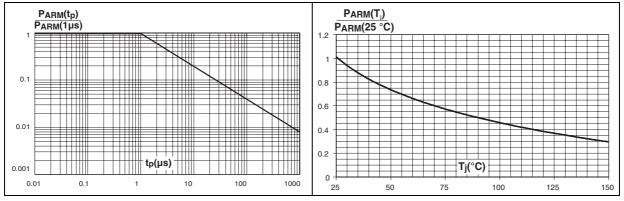
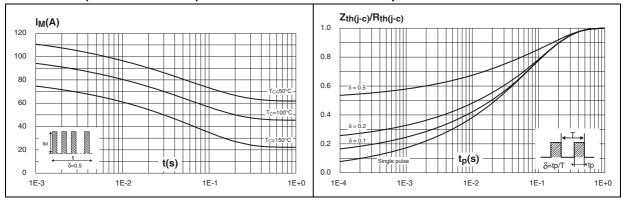


Figure 5. Non repetitive surge peak forward current versus overload duration (maximum values)

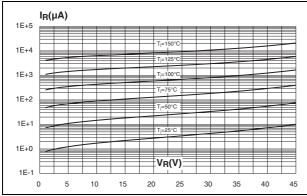
Figure 6. Relative variation of thermal impedance junction to case versus pulse duration



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Figure 7. Reverse leakage current versus reverse voltage applied (typical values)

Figure 8. Junction capacitance versus reverse voltage applied (typical values)



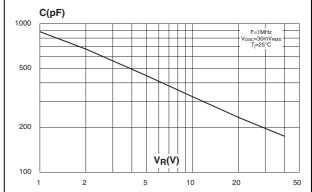
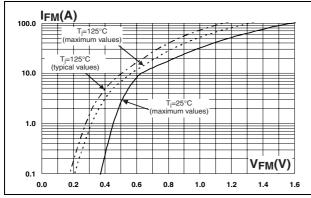
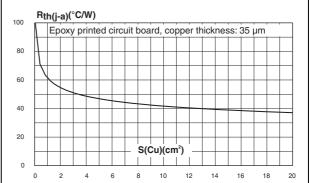


Figure 9. Forward voltage drop versus forward current

Figure 10. Thermal resistance junction to ambient versus copper surface under tab





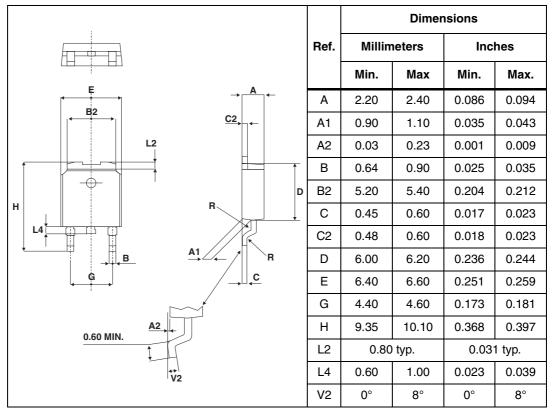
STPS1045B Package information

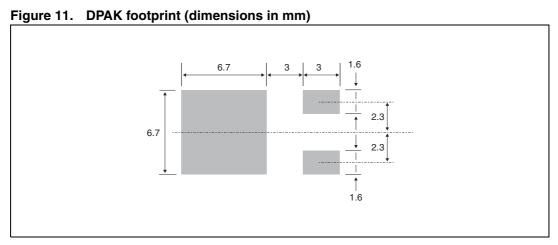
### 2 Package information

- Epoxy meets UL94, V0.
- Lead-free packages

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK<sup>®</sup> packages, depending on their level of environmental compliance. ECOPACK<sup>®</sup> specifications, grade definitions and product status are available at: <a href="https://www.st.com">www.st.com</a>. ECOPACK<sup>®</sup> is an ST trademark.

Table 5. DPAK dimensions





Ordering information STPS1045B

# **3** Ordering information

 Table 6.
 Ordering information

| Order code   | Marking | Package     | Weight | Base qty | Delivery mode |
|--------------|---------|-------------|--------|----------|---------------|
| STPS1045B    | S1045   | DPAK 0.30 q |        | 75       | Tube          |
| STPS1045B-TR | S1045   | DEAR        | 0.30 g | 2500     | Tape and reel |

Cooling method: by conduction (C)

# 4 Revision history

Table 7. Document revision history

| Date        | Revision | Changes                                      |  |  |
|-------------|----------|--|--|--|
| Jul-2003    | 3B       | Last issue                                   |  |  |
| 21-Apr-2005 | 4        | IPAK package removed                         |  |  |
| 03-Nov-2005 | 5        | DPAK foot print dimensions updated.          |  |  |
| 01-Jul-2010 | 6        | Updated Figure 9. Updated ECOPACK statement. |  |  |

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