



ST31000520AS

100564361 Rev. D November 2009

# **Revision history**

Revision	Date	Sheets affected or comments
Rev. A	04/16/09	Initial release.
Rev. B	06/08/09	4 & 11.
Rev. C	09/01/09	28. (add command - write uncorrectable = 45h); 11. (DC pwr req. update Idle Watts);1, 3, 8-9, 11, 14, 22 & 28. (remove 500GB model); 28. (Word 75 & 81 edit code);5. (corrected Non-Op shock value)
Rev. D	11/18/09	Updated drive specifications on Tables 1 -3; added "Warranty" specific section. Made minor text edits throughout.

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Publication number: 100564361, Rev. D November 2009

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When referring to drive capacity, one gigabyte, or GB, equals one billion bytes and one terabyte, or TB, equals one trillion bytes. Your computer's operating system may use a different standard of measurement and report a lower capacity. In addition, some of the listed capacity is used for formatting and other functions, and thus will not be available for data storage. Seagate reserves the right to change, without notice, product offerings or specifications.

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# 1.0 Introduction

This manual describes the functional, mechanical and interface specifications for the following Seagate Barracuda<sup>®</sup> LP Series model drives:

ST32000542AS ST31500541AS ST31000520AS

These drives provide the following key features:

- 5900 RPM spindle speed.
- High instantaneous (burst) data-transfer rates (up to 300MB per second).
- Perpendicular recording technology provides the drives with increased areal density.
- State-of-the-art cache and on-the-fly error-correction algorithms.
- Native Command Queueing with command ordering to increase performance in demanding applications.
- Full-track multiple-sector transfer capability without local processor intervention.
- Quiet operation.
- Compliant with RoHS requirements in China and Europe.
- SeaTools diagnostic software performs a drive self-test that eliminates unnecessary drive returns.
- Support for S.M.A.R.T. drive monitoring and reporting.
- Supports latching SATA cables and connectors.
- Worldwide Name (WWN) capability uniquely identifies the drive.

# 1.1 About the Serial ATA interface

The Serial ATA interface provides several advantages over the traditional (parallel) ATA interface. The primary advantages include:

- Easy installation and configuration with true plug-and-play connectivity. It is not necessary to set any jumpers or other configuration options.
- Thinner and more flexible cabling for improved enclosure airflow and ease of installation.
- Scalability to higher performance levels.

In addition, Serial ATA makes the transition from parallel ATA easy by providing legacy software support. Serial ATA was designed to allow you to install a Serial ATA host adapter and Serial ATA disk drive in your current system and expect all of your existing applications to work as normal.

The Serial ATA interface connects each disk drive in a point-to-point configuration with the Serial ATA host adapter. There is no master/slave relationship with Serial ATA devices like there is with parallel ATA. If two drives are attached on one Serial ATA host adapter, the host operating system views the two devices as if they were both "masters" on two separate ports. This essentially means both drives behave as if they are Device 0 (master) devices.

**Note.** The host adapter may, optionally, emulate a master/slave environment to host software where two devices on separate Serial ATA ports are represented to host software as a Device 0 (master) and Device 1 (slave) accessed at the same set of host bus addresses. A host adapter that emulates a master/slave environment manages two sets of shadow registers. This is not a typical Serial ATA environment.

The Serial ATA host adapter and drive share the function of emulating parallel ATA device behavior to provide backward compatibility with existing host systems and software. The Command and Control Block registers, PIO and DMA data transfers, resets, and interrupts are all emulated.

The Serial ATA host adapter contains a set of registers that shadow the contents of the traditional device registers, referred to as the Shadow Register Block. All Serial ATA devices behave like Device 0 devices. For additional information about how Serial ATA emulates parallel ATA, refer to the "Serial ATA International Organization: Serial ATA Revision 2.6". The specification can be downloaded from www.sata-io.org.

# 2.0 Drive specifications

Unless otherwise noted, all specifications are measured under ambient conditions, at 25°C, and nominal power. For convenience, the phrases *the drive* and *this drive* are used throughout this manual to indicate the following drive models:

ST32000542AS ST31500541AS ST31000520AS

Specification summary tables

The specifications listed in the following tables are for quick reference. For details on specification measurement or definition, see the appropriate section of this manual.

# Table 1: Drive specifications summary for 2000 and 1500GB models

Drive specification	ST32000542AS	ST31500541AS	
Formatted capacity (512 bytes/sector)*	2000GB	1500GB	
Guaranteed sectors	3,907,029,168	2,930,277,168	
Heads	8		
Disk	4		
Bytes per sector	512		
Default sectors per track	63		
Default read/write heads	16		
Default cylinders	16,383		
Recording density (max.)	1417kb/in		
Track density (avg.)	236 ktracks/in		
Areal density (avg.)	341.5 Gbits/in <sup>2</sup>		
Spindle speed	5900 RPM		
Internal data transfer rate (max.)	1285Mb/s		
Sustained data transfer rate OD (max.)	95MB/s		
I/O data-transfer rate (max.)	300MB/s		
ATA data-transfer modes supported	PIO modes: 0 to 4 Multiword DMA modes: 0 to 2 Ultra DMA modes: 0 to 6		
Cache buffer	32MB		
Height (max)	26.1mm / 1.028 in		
Width (max)	101.85mm / 4.010 in		
Length (max)	146.99mm / 5.787 in		
Weight (typical)	655g / 1.444 lb		
Average latency	5.1ms		
Power-on to ready (typical)	<13s		
Standby to ready (typical)	ady (typical) <12s		
Track-to-track seek time (typical)     <1.0ms (read)			
Average read (typical) Average write (typical)	<16.0ms <16.0ms		
Startup current (typical) 12V (peak)	2.0A		
Voltage tolerance (including noise)			
Operating temperature (Drive Case temperature)	0° to 60°C		
Nonoperating temperature (Ambient)	–40° to 70°C		
Temperature gradient (max)	20°C per hour max (operating) 30°C per hour max (nonoperating)		
Relative humidity	5% to 90% (operating) 5% to 95% (nonoperating)		
Relative humidity gradient (max.)	30% per hour		
Wet bulb temperature (max.)	37.7°C (operating) 40.0°C (nonoperating)		
Altitude, operating	-60.96m to 3,048m (-200 ft to 10,000+ ft)		
Altitude, nonoperating (below mean sea level, max)	-60.96m to 12,192m (-200 ft to 40,000+ ft)		

Drive specification	ST32000542AS	ST31500541AS	
Operational Shock (max)	70 Gs at 2ms	70 Gs at 2ms	
Non-Operational Shock (max)	300 Gs at 2ms		
Vibration, operating	5 to 350 Hz: 0.50 Gs 350 to 500 Hz: 0.25 Gs		
Vibration, nonoperating	5 to 350 Hz: 5.0 Gs 350 to 500 Hz: 2.0 Gs		
Drive acoustics, sound power			
Idle**	2.5 bels (typical) 2.6 bels (max)		
Seek Profile	2.6 bels (typical) 2.8 bels (max)		
Nonrecoverable read errors	1 per 10 <sup>14</sup> bits read		
Warranty       To determine the warranty for a specific drive, use a web browser to acce web page: <a href="support.seagate.com/customer/warranty">support.seagate.com/customer/warranty</a> validation.jsp         From this page, click on the "Verify Your Warranty" link. You will be asked drive serial number, model number (or part number) and country of purch will display the warranty information for your drive.		mer/warranty validation.jsp Warranty" link. You will be asked to provide the art number) and country of purchase. The system	
Contact start-stop cycles 50,000 at 25°C, 50% rel. humidity			
Supports Hotplug operation per the Serial ATA Revision 2.6 specification	Yes		

\*One Gbyte equals one billion bytes when referring to hard drive capacity. Accessible capacity may vary depending on operating environment and formatting.

\*\*During periods of drive idle, some offline activity may occur according to the S.M.A.R.T. specification, which may increase acoustic and power to operational levels.

Table 2:	Drive specifications summary for 1000GB models
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Drive specification	ST31000520AS
Formatted capacity (512 bytes/sector)*	1000GB
Guaranteed sectors	1,953,525,168
Heads	4
Disks	2
Bytes per sector	512
Default sectors per track	63
Default read/write heads	16
Default cylinders	16,383
Recording density (max)	1417kbits/in
Track density (avg)	236 ktracks/in avg
Areal density (avg)	341.5Gb/in <sup>2</sup> avg
Spindle speed	5900 RPM
Internal data transfer rate (max)	1285Mb/s
Sustained data transfer rate OD (max)	95MB/s
I/O data-transfer rate (max)	300MB/s
ATA data-transfer modes supported	PIO modes: 0 to 4 Multiword DMA modes: 0 to 2 Ultra DMA modes: 0 to 6
Cache buffer	32MB
Height (max)	26.1mm / 1.028 in
Width (max)	101.85mm / 4.010 in
Length (max)	146.99mm / 5.787 in
Weight (typical)	622g / 1.371 lb
Average latency	5.1ms
Power-on to ready (typical)	<12s
Standby to ready (typical)	<6s
Track-to-track seek time	<1.0ms (read) <1.2ms (write)
Average read (typical) Average write (typical)	<16.0ms <16.0ms
Startup current (typical) 12V (peak)	2.0A
Voltage tolerance (including noise)	5V ± 5% 12V ± 10%
Operating temperature (Drive Case temperature)	0° to 60°C
Nonoperating temperature (Ambient)	-40° to 70°C
Temperature gradient (max)	20°C per hour (operating) 30°C per hour (nonoperating)
Relative humidity	5% to 90% (operating) 5% to 95% (nonoperating)
Relative humidity gradient (max)	30% per hour
Wet bulb temperature (max)	37.7°C max (operating) 40.0°C max (nonoperating)
Altitude, operating	-60.96m to 3,048m (-200 ft to 10,000+ ft)
Altitude, nonoperating (below mean sea level, max)	-60.96m to 12,192m (-200 ft. to 40,000+ ft.)
Operational Shock (max)	70 Gs at 2ms

Drive specification	ST31000520AS
Non-Operational Shock (max.)	350 Gs at 2ms
Vibration, operating	5 to 350 Hz: 0.50 Gs 350 to 500 Hz: 0.25 Gs
Vibration, nonoperating	5 to 350 Hz: 5.0 Gs 350 to 500 Hz: 2.0 Gs
Drive acoustics, sound power	
ldle**	1.9 bels (typical) 2.1 bels (max)
Seek Profile	2.0 bels (typical) 2.2 bels (max)
Nonrecoverable read errors	1 per 10 <sup>14</sup> bits read
Warranty	To determine the warranty for a specific drive, use a web browser to access the following web page: <u>support.seagate.com/customer/warranty_validation.jsp</u> From this page, click on the "Verify Your Warranty" link. You will be asked to provide the drive serial number, model number (or part number) and country of purchase. The system will display the warranty information for your drive.
Contact start-stop cycles	50,000 at 25°C, 50% rel. humidity
Supports Hotplug operation per the Serial ATA Revision 2.6 specification	Yes

\*One Gbyte equals one billion bytes when referring to hard drive capacity. Accessible capacity may vary depending on operating environment and formatting.

\*\*During periods of drive idle, some offline activity may occur according to the S.M.A.R.T. specification, which may increase acoustic and power to operational levels.

# 2.1 Formatted capacity

Model	Formatted capacity*	Guaranteed sectors	Bytes per sector
ST32000542AS	2000GB	3,907,029,168	
ST31500541AS	1500GB	2,930,277,168	512
ST31000520AS	1000GB	1,953,525,168	

\*One Gbyte equals one billion bytes when referring to hard drive capacity. Accessible capacity may vary depending on operating environment and formatting.

#### 2.1.1 LBA mode

When addressing these drives in LBA mode, all blocks (sectors) are consecutively numbered from 0 to n-1, where *n* is the number of guaranteed sectors as defined above.

See Section 4.3.1, "Identify Device command" (words 60-61 and 100-103) for additional information about 48bit addressing support of drives with capacities over 137GB.

#### 2.2 Default logical geometry

Cylinders	Read/write heads	Sectors per track
16,383	16	63

#### LBA mode

When addressing these drives in LBA mode, all blocks (sectors) are consecutively numbered from 0 to n-1, where n is the number of guaranteed sectors as defined above.

#### 2.3 Recording and interface technology

Interface	Serial ATA (SATA)
Recording method	Perpendicular
Recording density (kbits/inch max)	1417kb/in
Track density (ktracks/inch avg)	236 ktracks/in
Areal density (Gb/in <sup>2</sup> avg)	341.5Gb/in <sup>2</sup>
Spindle speed (RPM)	5900 ( <u>+</u> 0.2%)
Internal data transfer rate (Mb/s max.)	1285Mb/s
Sustained data transfer rate (MB/s max.)	95MB/s
I/O data-transfer rate (MB/s max.)	300MB/s

### 2.4 Physical characteristics

	ST32000542AS and ST31500541AS	ST31000520AS
Maximum height	26.1mm / 1.028 in	
Maximum width	101.85mm / 4.010 in	
Maximum length	146.99mm / 5.787 in	
Typical weight	655g / 1.444 lb	622g /1.371 lb
Cache buffer	32MB / 32,768KB	

### 2.5 Access time

Access time measurements are taken with nominal power at 25°C ambient temperature. All times are measured using drive diagnostics. The specifications in the table below are defined as follows:

- Track-to-track access time is an average of all possible single-track seeks in both directions.
- Average access time is a true statistical random average of at least 5,000 measurements of seeks between random tracks, less overhead.

Typical access times (ms)	Read	Write
Track-to-track	<1.0	<1.2
Average	<16.0	<16.0

**Note.** These drives are designed to consistently meet the seek times represented in this manual. Physical seeks, regardless of mode (such as track-to-track and average), are expected to meet the noted values. However, due to the manner in which these drives are formatted, benchmark tests that include command overhead or measure logical seeks may produce results that vary from these specifications.

#### 2.6 Start/stop times

	2000 and 1500GB models	1000GB models
Power-on to Ready (s)	<10.0 (typical)	<8.0 (typical)
Standby to Ready (s)	<12.0 (typical)	<6.0 (typical)
Ready to spindle stop (s)	10 (max)	

# 2.7 Power specifications

The drive receives DC power (+5V or +12V) through a native SATA power connector. See Figure 2 on page 21.

#### 2.7.1 Power consumption

Power requirements for the drives are listed in the table on page 9. Typical power measurements are based on an average of drives tested, under nominal conditions, using 5.0V and 12.0V input voltage at 25°C ambient temperature.

#### • Spinup power

Spinup power is measured from the time of power-on to the time that the drive spindle reaches operating speed.

#### Read/write power and current

Read/write power is measured with the heads on track, based on a 16-sector write followed by a 32-ms delay, then a 16-sector read followed by a 32-ms delay.

### Operating power and current

Operating power is measured using 40 percent random seeks, 40 percent read/write mode (1 write for each 10 reads) and 20 percent drive idle mode.

### Idle mode power

Idle mode power is measured with the drive up to speed, with servo electronics active and with the heads in a random track location.

#### Standby mode

During Standby mode, the drive accepts commands, but the drive is not spinning, and the servo and read/ write electronics are in power-down mode.

## Table 3:DC power requirements

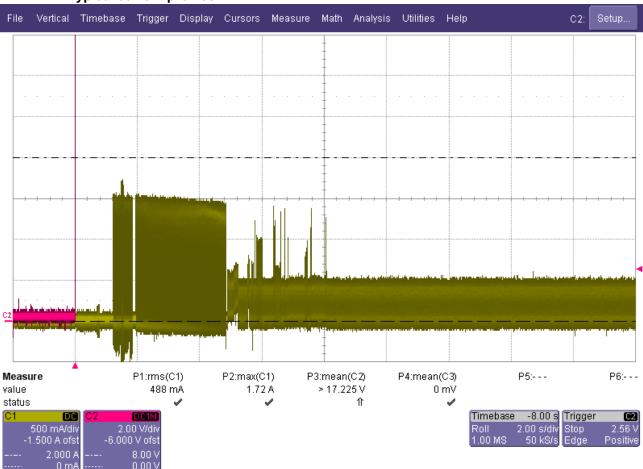
Power dissipation (2TB and 1.5TB models)	Avg (watts 25° C)	Avg 5V typ amps	Avg 12V typ amps
Spinup	—	—	2.0 (peak)
ldle* †	4.30	0.168	0.337
Operating (average)	6.80	0.360	0.420
Standby	0.500	0.350	0.010
Sleep	0.500	0.350	0.010

Power dissipation (1TB models)	Avg (watts 25° C)	Avg 5V typ amps	Avg 12V typ amps
Spinup	—	—	2.0 (peak)
Idle* †	3.00	0.239	0.216
Operating (average)	5.70	0.335	0.350
Standby	0.321	0.225	0.096
Sleep	0.321	0.225	0.096

\*During periods of drive idle, some offline activity may occur according to the S.M.A.R.T. specification, which may increase acoustic and power to operational levels.

†5W IDLE with DIPLM Enabled

**Note.** Drive supports PHY layer power management. When enabled, a power savings of ~300mw can be achieved in Idle, Standby and Sleep modes.



#### 2.7.1.1 Typical current profiles



## 2.7.2 Conducted noise

Input noise ripple is measured at the host system power supply across an equivalent 80-ohm resistive load on the +12V line or an equivalent 15-ohm resistive load on the +5V line.

- Using 12-volt power, the drive is expected to operate with a maximum of 120 mV peak-to-peak square-wave injected noise at up to 10 MHz.
- Using 5-volt power, the drive is expected to operate with a maximum of 100 mV peak-to-peak square-wave injected noise at up to 10 MHz.
- **Note.** Equivalent resistance is calculated by dividing the nominal voltage by the typical RMS read/write current.

### 2.7.3 Voltage tolerance

Voltage tolerance (including noise):

5V +10% / -7.5% 12V +10% / -7.5%

#### 2.7.4 Power-management modes

The drive provides programmable power management to provide greater energy efficiency. In most systems, you can control power management through the system setup program. The drive features the following power-management modes:

Power modes	Heads	Spindle	Buffer
Active	Tracking	Rotating	Enabled
Idle	Tracking	Rotating	Enabled
Standby	Parked	Stopped	Enabled
Sleep	Parked	Stopped	Disabled

#### Active mode

The drive is in Active mode during the read/write and seek operations.

#### • Idle mode

The buffer remains enabled, and the drive accepts all commands and returns to Active mode any time disk access is necessary.

#### • Standby mode

The drive enters Standby mode when the host sends a Standby Immediate command. If the host has set the standby timer, the drive can also enter Standby mode automatically after the drive has been inactive for a specifiable length of time. The standby timer delay is established using a Standby or Idle command. In Standby mode, the drive buffer is enabled, the heads are parked and the spindle is at rest. The drive accepts all commands and returns to Active mode any time disk access is necessary.

#### Sleep mode

The drive enters Sleep mode after receiving a Sleep command from the host. In Sleep mode, the drive buffer is disabled, the heads are parked and the spindle is at rest. The drive leaves Sleep mode after it receives a Hard Reset or Soft Reset from the host. After receiving a reset, the drive exits Sleep mode and enters Standby mode with all current translation parameters intact.

#### • Idle and Standby timers

Each time the drive performs an Active function (read, write or seek), the standby timer is reinitialized and begins counting down from its specified delay times to zero. If the standby timer reaches zero before any drive activity is required, the drive makes a transition to Standby mode. In both Idle and Standby mode, the drive accepts all commands and returns to Active mode when disk access is necessary.

# 2.8 Environmental specifications

#### 2.8.1 Ambient temperature

Ambient temperature is defined as the temperature of the environment immediately surrounding the drive. Actual drive case temperature should not exceed 69°C (156°F) within the operating ambient conditions.

Operating:	0° to 60°C (32° to 140°F)
Nonoperating: *	-40° to 70°C (-40° to 158°F)

\* See Section 2.12 for reliability projections.

#### 2.8.2 Temperature gradient

Operating:	20°C per hour (68°F per hour max), without condensation
Nonoperating:	30°C per hour (86°F per hour max)

#### 2.8.3 Humidity

#### 2.8.3.1 Relative humidity

Operating:	5% to 95% noncondensing (30% per hour max)
Nonoperating:	5% to 95% noncondensing (30% per hour max)

#### 2.8.3.2 Wet bulb temperature

Operating:	37.7°C (99.9°F max)
Nonoperating:	40°C (104°F max)

#### 2.8.4 Altitude

Operating:	-60.96 m to 3,048 m (-200 ft. to 10,000+ ft.)
Nonoperating:	-60.96 m to 12,192 m (-200 ft. to 40,000+ ft.)

## 2.8.5 Shock

All shock specifications assume that the drive is mounted securely with the input shock applied at the drive mounting screws. Shock may be applied in the X, Y or Z axis.

#### 2.8.5.1 Operating shock

These drives comply with the performance levels specified in this document when subjected to a maximum operating shock of 70 Gs based on half-sine shock pulses of 2ms during read operations. Shocks should not be repeated more than two times per second.

#### 2.8.5.2 Nonoperating shock

The nonoperating shock level that the drive can experience without incurring physical damage or degradation in performance when subsequently put into operation is 300 Gs based on a nonrepetitive half-sine shock pulse of 2ms duration.

#### 2.8.6 Vibration

All vibration specifications assume that the drive is mounted securely with the input vibration applied at the drive mounting screws. Vibration may be applied in the X, Y or Z axis.

#### 2.8.6.1 Operating vibration

The maximum vibration levels that the drive may experience while meeting the performance standards specified in this document are specified below.

5–350 Hz	0.50 Gs
350–500 Hz	0.25 Gs

#### 2.8.6.2 Nonoperating vibration

The maximum nonoperating vibration levels that the drive may experience without incurring physical damage or degradation in performance when subsequently put into operation are specified below.

5–350 Hz	5.0 Gs
350–500 Hz	2.0 Gs

#### 2.9 Acoustics

1TB model

Drive acoustics are measured as overall A-weighted acoustic sound power levels (no pure tones). All measurements are consistent with ISO document 7779. Sound power measurements are taken under essentially free-field conditions over a reflecting plane. For all tests, the drive is oriented with the cover facing upward.

Note. For seek mode tests, the drive is placed in seek mode only. The number of seeks per second is defined by the following equation:

(Number of seeks per second = 0.4 / (average latency + average access time)

	Idle <sup>[1]</sup>	Seek profile
2TB and 1.5TB model	2.5 bels (typ) 2.6 bels (max)	2.6 bels (typ) 2.8 bels (max)
1TB model	1.9 bels (typ)	2.0 bels (typ)

2.1 bels (max)

Table 4: Drive A-weighted Sound Power Levels (SWL, BA)

[1] During periods of drive idle, some offline activity may occur according to the S.M.A.R.T. specification, which may increase acoustic and power to operational levels.

2.2 bels (max)

#### 2.9.1 Test for Prominent Discrete Tones (PDTs)

Seagate follows the ECMA-74 standards for measurement and identification of PDTs. An exception to this process is the use of the absolute threshold of hearing. Seagate uses this threshold curve (originated in ISO 389-7) to discern tone audibility and to compensate for the inaudible components of sound prior to computation of tone ratios according to Annex D of the ECMA-74 standards.

#### 2.10 **Electromagnetic immunity**

When properly installed in a representative host system, the drive operates without errors or degradation in performance when subjected to the radio frequency (RF) environments defined in the following table:

Test	Description	Performance level	Reference standard
Electrostatic discharge	Contact, HCP, VCP: ± 4 kV; Air: ± 8 kV	В	EN61000-4-2: 95
Radiated RF immunity	80 to 1,000 MHz, 3 V/m, 80% AM with 1 kHz sine 900 MHz, 3 V/m, 50% pulse modulation @ 200 Hz	A	EN61000-4-3: 96 ENV 50204: 95
Electrical fast transient	$\pm$ 1 kV on AC mains, $\pm$ 0.5 kV on external I/O	В	EN61000-4-4: 95
Surge immunity	± 1 kV differential, ± 2 kV common, AC mains	В	EN61000-4-5: 95
Conducted RF immunity	150 kHz to 80 MHz, 3 Vrms, 80% AM with 1 kHz sine	А	EN61000-4-6: 97
Voltage dips, interrupts	0% open, 5 seconds 0% short, 5 seconds 40%, 0.10 seconds 70%, 0.01 seconds	СССВ	EN61000-4-11: 94

# 2.11 Warranty

To determine the warranty for a specific drive, use a web browser to access the following web page: <u>support.seagate.com/customer/warranty\_validation.jsp</u>

From this page, click on the "Verify Your Warranty" link. You will be asked to provide the drive serial number, model number (or part number) and country of purchase. The system will display the warranty information for your drive.

## 2.12 Agency certification

#### 2.12.1 Safety certification

These products are certified to meet the requirements of UL60950-1, CSA60950-1 and EN60950 and so marked as to the certify agency.

#### 2.12.2 Electromagnetic compatibility

Hard drives that display the CE mark comply with the European Union (EU) requirements specified in the Electromagnetic Compatibility Directive (2004/108/EC) as put into place 20 July 2007. Testing is performed to the levels specified by the product standards for Information Technology Equipment (ITE). Emission levels are defined by EN 55022, Class B and the immunity levels are defined by EN 55024.

Drives are tested in representative end-user systems. Although CE-marked Seagate drives comply with the directives when used in the test systems, we cannot guarantee that all systems will comply with the directives. The drive is designed for operation inside a properly designed enclosure, with properly shielded I/O cable (if necessary) and terminators on all unused I/O ports. Computer manufacturers and system integrators should confirm EMC compliance and provide CE marking for their products.

#### Korean RRL

If these drives have the Korean Communications Commission (KCC) logo, they comply with paragraph 1 of Article 11 of the Electromagnetic Compatibility control Regulation and meet the Electromagnetic Compatibility (EMC) Framework requirements of the Radio Research Laboratory (RRL) Communications Commission, Republic of Korea.

These drives have been tested and comply with the Electromagnetic Interference/Electromagnetic Susceptibility (EMI/EMS) for Class B products. Drives are tested in a representative, end-user system by a Korean-recognized lab.

- Family name: Barracuda LP Series
- Certificate number: STX-BarracudaLP (B)

#### Australian C-Tick (N176)

If these models have the C-Tick marking, they comply with the Australia/New Zealand Standard AS/NZ CISPR22 and meet the Electromagnetic Compatibility (EMC) Framework requirements of the Australian Communication Authority (ACA).

#### 2.12.3 FCC verification

These drives are intended to be contained solely within a personal computer or similar enclosure (not attached as an external device). As such, each drive is considered to be a subassembly even when it is individually marketed to the customer. As a subassembly, no Federal Communications Commission verification or certification of the device is required.

Seagate has tested this device in enclosures as described above to ensure that the total assembly (enclosure, disk drive, motherboard, power supply, etc.) does comply with the limits for a Class B computing device, pursuant to Subpart J, Part 15 of the FCC rules. Operation with noncertified assemblies is likely to result in interference to radio and television reception.

**Radio and television interference.** This equipment generates and uses radio frequency energy and if not installed and used in strict accordance with the manufacturer's instructions, may cause interference to radio and television reception.

This equipment is designed to provide reasonable protection against such interference in a residential installation. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause interference to radio or television, which can be determined by turning the equipment on and off, you are encouraged to try one or more of the following corrective measures:

- Reorient the receiving antenna.
- Move the device to one side or the other of the radio or TV.
- Move the device farther away from the radio or TV.
- Plug the computer into a different outlet so that the receiver and computer are on different branch outlets.

If necessary, you should consult your dealer or an experienced radio/television technician for additional suggestions. You may find helpful the following booklet prepared by the Federal Communications Commission: *How to Identify and Resolve Radio-Television Interference Problems*. This booklet is available from the Super-intendent of Documents, U.S. Government Printing Office, Washington, DC 20402. Refer to publication number 004-000-00345-4.

# 2.13 Environmental protection

Seagate designs its products to meet environmental protection requirements worldwide, including regulations restricting certain chemical substances.

## 2.13.1 European Union Restriction of Hazardous Substances (RoHS) Directive

The European Union Restriction of Hazardous Substances (RoHS) Directive, restricts the presence of chemical substances, including Lead, Cadmium, Mercury, Hexavalent Chromium, PBB and PBDE, in electronic products, effective July 2006. This drive is manufactured with components and materials that comply with the RoHS Directive.

## 2.13.2 China Restriction of Hazardous Substances (RoHS) Directive 中国限制危险物品的指令

This product has an Environmental Protection Use Period (EPUP) of 20 years. The following table contains information mandated by China's "Marking Requirements for Control of Pollution Caused by Electronic Information Products" Standard.



该产品具有20年的环境保护使用周期 (EPUP)。 下表包含了中国 "电子产品所导致的污染的控 制的记号要求"所指定的信息。

	-	Foxic or Haza	ardous Subst	ances or Ele	ments有毒有害物质	5或元素
Name of Parts 部件名称	Lead 铅 (Pb)	Mercury 汞 (Hg)	Cadmium 福 (Cd)	Hexavalent Chromium 六价铬 (Cr6+)	/	Polybrominated Diphenyl Ether 多溴二苯醚 (PBDE)
PCBA	Х	0	0	0	0	0
HDA	Х	0	0	0	0	0

"O" indicates the hazardous and toxic substance content of the part (at the homogenous material level) is lower than the threshold defined by the China RoHS MCV Standard.

"O"表示该部件(于同类物品程度上)所含的危险和有毒物质低于中国<sup>RoHS MCV</sup>标准所定义的门槛值。

"X" indicates the hazardous and toxic substance content of the part (at the homogenous material level) is over the threshold defined by the China RoHS MCV Standard.

"X"表示该部件(于同类物品程度上)所含的危险和有毒物质超出中国RoHS MCV标准所定义的门槛值。

## 2.14 Corrosive environment

Seagate electronic drive components pass accelerated corrosion testing equivalent to 10 years exposure to light industrial environments containing sulfurous gases, chlorine and nitric oxide, classes G and H per ASTM B845. However, this accelerated testing cannot duplicate every potential application environment. Users should use caution exposing any electronic components to uncontrolled chemical pollutants and corrosive chemicals as electronic drive component reliability can be affected by the installation environment. The silver, copper, nickel and gold films used in Seagate products are especially sensitive to the presence of sulfide, chloride, and nitrate contaminants. Sulfur is found to be the most damaging. In addition, electronic components should never be exposed to condensing water on the surface of the printed circuit board assembly (PCBA) or exposed to an ambient relative humidity greater than 95%. Materials used in cabinet fabrication, such as vulcanized rubber, that can outgas corrosive compounds should be minimized or eliminated. The useful life of any electronic equipment may be extended by replacing materials near circuitry with sulfide-free alternatives.

# 3.0 Configuring and mounting the drive

This section contains the specifications and instructions for configuring and mounting the drive.

# 3.1 Handling and static-discharge precautions

After unpacking, and before installation, the drive may be exposed to potential handling and electrostatic discharge (ESD) hazards. Observe the following standard handling and static-discharge precautions:

### Caution:

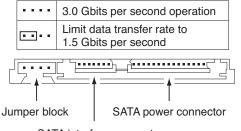
- Before handling the drive, put on a grounded wrist strap, or ground yourself frequently by touching the metal chassis of a computer that is plugged into a grounded outlet. Wear a grounded wrist strap throughout the entire installation procedure.
- Handle the drive by its edges or frame only.
- The drive is extremely fragile—handle it with care. Do not press down on the drive top cover.
- Always rest the drive on a padded, antistatic surface until you mount it in the computer.
- Do not touch the connector pins or the printed circuit board.
- Do not remove the factory-installed labels from the drive or cover them with additional labels. Removal voids the warranty. Some factory-installed labels contain information needed to service the drive. Other labels are used to seal out dirt and contamination.

# 3.2 Configuring the drive

Each drive on the Serial ATA interface connects point-to-point with the Serial ATA host adapter. There is no master/slave relationship because each drive is considered a master in a point-to-point relationship. If two drives are attached on one Serial ATA host adapter, the host operating system views the two devices as if they were both "masters" on two separate ports. Both drives behave as if they are Device 0 (master) devices.

Serial ATA drives are designed for easy installation. It is usually not necessary to set any jumpers on the drive for proper operation; however, if you connect the drive and receive a "drive not detected" error, your SATA-equipped motherboard or host adapter may use a chipset that does not support SATA speed autonegotiation. If you have a motherboard or host adapter that does not support autonegotiation:

- Install a jumper as shown in Figure 1 below to limit the data transfer rate to 1.5 Gbits per second (and leave the drive connected to the SATA-equipped motherboard or host adapter that doesn't support autonegotiation) or
- Install a SATA host adapter that supports autonegotiation, leave the drive jumper block set to "Normal operation" (see Figure 1 below), and connect the drive to that adapter. This option has the benefit of not limiting the drive to a 1.5 Gbits/sec transfer rate.



SATA interface connector

#### Figure 1. Serial ATA connectors

## 3.3 Serial ATA cables and connectors

The Serial ATA interface cable consists of four conductors in two differential pairs, plus three ground connections. The cable size may be 30 to 26 AWG with a maximum length of one meter (39.37 inches). See Table 6 for connector pin definitions. Either end of the SATA signal cable can be attached to the drive or host.

For direct backplane connection, the drive connectors are inserted directly into the host receptacle. The drive and the host receptacle incorporate features that enable the direct connection to be hot pluggable and blind mateable.

For installations which require cables, you can connect the drive as illustrated in Figure 2.

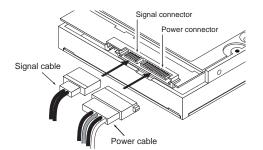


Figure 2. Attaching SATA cabling

Each cable is keyed to ensure correct orientation. Barracuda LP Series SATA drives support latching SATA connectors.

# 3.4 Drive mounting

You can mount the drive in any orientation using four screws in the side-mounting holes or four screws in the bottom-mounting holes. See Figure 3 for drive mounting dimensions. Follow these important mounting precautions when mounting the drive:

- Allow a minimum clearance of 0.030 inches (0.76 mm) around the entire perimeter of the drive for cooling.
- Use only 6-32 UNC mounting screws.
- The screws should be inserted no more than 0.150 inch (3.81 mm) into the bottom or side mounting holes.
- Do not overtighten the mounting screws (maximum torque: 6 inch-lb).

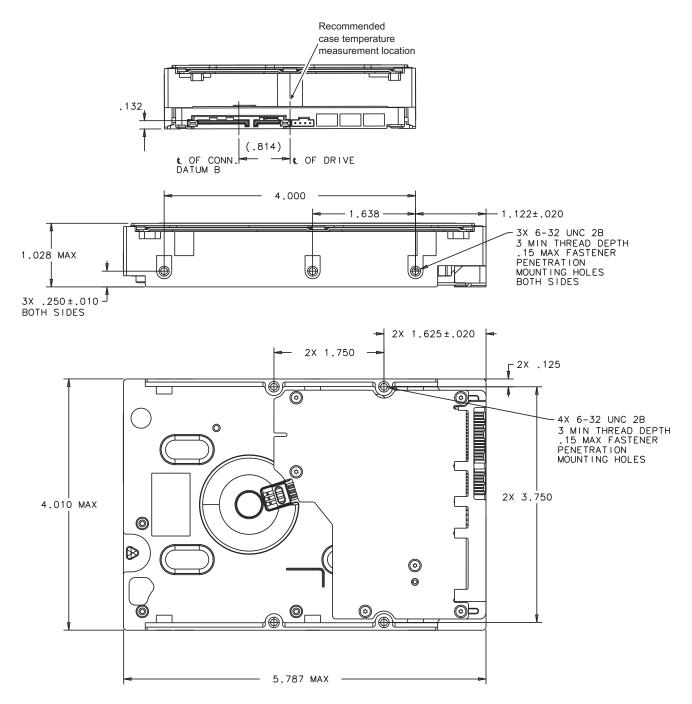


Figure 3. Mounting dimensions (2.0, 1.5 and 1.0 TB models)

# 4.0 Serial ATA (SATA) interface

These drives use the industry-standard Serial ATA interface that supports FIS data transfers. It supports ATA programmed input/output (PIO) modes 0–4; multiword DMA modes 0–2, and Ultra DMA modes 0–6.

For detailed information about the Serial ATA interface, refer to the "Serial ATA: High Speed Serialized AT Attachment" specification.

## 4.1 Hot-Plug compatibility

Barracuda LP Series SATA drives incorporate connectors which enable you to hot plug these drives in accordance with the Serial ATA Revision 2.6 specification. This specification can be downloaded from www.serialata.org.

# 4.2 Serial ATA device plug connector pin definitions

Table 6 summarizes the signals on the Serial ATA interface and power connectors.

Segment	Pin	Function	Definition
	S1	Ground	2nd mate
	S2	A+	Differential signal pair A from Phy
	S3	A-	
	S4	Ground	2nd mate
	S5	В-	Differential signal pair B from Phy
	S6	B+	
Signal	S7	Ground	2nd mate

 Table 6:
 Serial ATA connector pin definitions

#### Key and spacing separate signal and power segments

They and opacing separate signal and power segmente			
	P1	V <sub>33</sub>	3.3V power
	P2	V <sub>33</sub>	3.3V power
	P3	V <sub>33</sub>	3.3V power, pre-charge, 2nd mate
	P4	Ground	1st mate
	P5	Ground	2nd mate
	P6	Ground	2nd mate
Power	P7	V <sub>5</sub>	5V power, pre-charge, 2nd mate
	P8	V <sub>5</sub>	5V power
	P9	V <sub>5</sub>	5V power
	P10	Ground	2nd mate
	P11	Ground or LED signal	If grounded, drive does not use deferred spin
	P12	Ground	1st mate.
	P13	V <sub>12</sub>	12V power, pre-charge, 2nd mate
	P14	V <sub>12</sub>	12V power
	P15	V <sub>12</sub>	12V power

#### Notes:

- 1. All pins are in a single row, with a 1.27 mm (0.050") pitch.
- 2. The comments on the mating sequence apply to the case of backplane blindmate connector only. In this case, the mating sequences are:
  - the ground pins P4 and P12.
  - the pre-charge power pins and the other ground pins.
  - the signal pins and the rest of the power pins.
- 3. There are three power pins for each voltage. One pin from each voltage is used for pre-charge when installed in a blind-mate backplane configuration.
- 4. All used voltage pins  $(V_x)$  must be terminated.

# 4.3 Supported ATA commands

The following table lists Serial ATA standard commands that the drive supports. For a detailed description of the ATA commands, refer to the Serial ATA International Organization: Serial ATA Revision 2.6 (http://www.sata-io.org).

See "S.M.A.R.T. commands" on page 32 for details and subcommands used in the S.M.A.R.T. implementation.

Table 7:	Supported ATA commands
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Command name	Command code (in hex)
Check Power Mode	E5 <sub>H</sub>
Device Configuration Freeze Lock	B1 <sub>H</sub> / C1 <sub>H</sub>
Device Configuration Identify	B1 <sub>H</sub> / C2 <sub>H</sub>
Device Configuration Restore	B1 <sub>H</sub> / C0 <sub>H</sub>
Device Configuration Set	B1 <sub>H</sub> / C3 <sub>H</sub>
Device Reset	08 <sub>H</sub>
Download Microcode	92 <sub>H</sub>
Execute Device Diagnostics	90 <sub>H</sub>
Flush Cache	E7 <sub>H</sub>
Flush Cache Extended	EA <sub>H</sub>
Format Track	50 <sub>H</sub>
Identify Device	EC <sub>H</sub>
Idle	E3 <sub>H</sub>
Idle Immediate	E1 <sub>H</sub>
Initialize Device Parameters	91 <sub>H</sub>
Read Buffer	E4 <sub>H</sub>
Read DMA	C8 <sub>H</sub>
Read DMA Extended	25 <sub>H</sub>
Read DMA Without Retries	C9 <sub>H</sub>
Read Log Ext	2F <sub>H</sub>
Read Multiple	C4 <sub>H</sub>
Read Multiple Extended	29 <sub>H</sub>
Read Native Max Address	F8 <sub>H</sub>
Read Native Max Address Extended	27 <sub>H</sub>
Read Sectors	20 <sub>H</sub>
Read Sectors Extended	24 <sub>H</sub>
Read Sectors Without Retries	21 <sub>H</sub>
Read Verify Sectors	40 <sub>H</sub>
Read Verify Sectors Extended	42 <sub>H</sub>
Read Verify Sectors Without Retries	41 <sub>H</sub>
Recalibrate	10 <sub>H</sub>
Security Disable Password	F6 <sub>H</sub>
	•

Security Erase Prepare $F3_{H}$ Security Erase Unit $F4_{H}$ Security Erasesword $F1_{H}$ Security Unlock $F2_{H}$ Security Unlock $F2_{H}$ Security Unlock $F2_{H}$ SekSekSet Resures $EF_{H}$ Set Max Address $F9_{H}$ Note: Individual Get Max Address $P3_{H}$ Note: Individual Get Max Address $P3_{H}$ Set Max Address $F9_{H}$ Set Max Address $C0_{4}$ parameter as defined by the value placed in the Set Max Features regis- ter as defined $C6_{H}$ Sileep $E6_{H}$ Sileep $E6_{H}$ SM.A.R.T. Enable Operations $B0_{H} / D9_{H}$ S.M.A.R.T. Enable Operations $B0_{H} / D9_{H}$ S.M.A.R.T. Enable Operations $B0_{H} / D4_{H}$ S.M.A.R.T. Read Attribute Thresholds $B0_{H} / D4_{H}$ S.M.A.R.T. Read Data $B0_{H} / D4_{H}$ S.M.A.R.T. Reid Data $B0_{H} / D4_{H}$ S.M.A.R.T. Re	Command name	Command code (in hex)
Security Freeze $F5_{\rm H}$ Security Set Password $F1_{\rm H}$ Security Unlock $F2_{\rm H}$ Seek $70_{\rm H}$ Set Katures $EF_{\rm H}$ Set Max Address $F9_{\rm H}$ Note: Individual Set Max Address $F9_{\rm H}$ Note: Individual Set Max Address $F9_{\rm H}$ Note: Individual Set Max Address $C0_{\rm H}$ Password: $00_{\rm H}$ Set Max Address Extended $37_{\rm H}$ Set Multiple Mode $C6_{\rm H}$ SMA.R.T. Disable Operations $80_{\rm H}/D9_{\rm H}$ S.M.A.R.T. Enable/Disable Autosave $80_{\rm H}/D9_{\rm H}$ S.M.A.R.T. Enable/Operations $80_{\rm H}/D9_{\rm H}$ S.M.A.R.T. Read Attribute Thresholds $80_{\rm H}/D4_{\rm H}$ S.M.A.R.T. Read Log Sector $80_{\rm H}/D4_{\rm H}$ S.M.A.R.T. Read Log Sector $80_{\rm H}/D4_{\rm H}$ S.M.A.R.T. Read Log Sector $80_{\rm H}/D6_{\rm H}$ S.M.A.R.T. Read Data $80_{\rm H}/D6_{\rm H}$ S.M.A.R.T. Return Status $80_{\rm H}/D6_{\rm H}$ S.M.A.R.T. Write Log Sector $80_{\rm H}/D6_{\rm H}$ Standby Immediate $E0_{\rm H}$ Write DMA Extended $35_{\rm H}$ Write DMA Extended </td <td>Security Erase Prepare</td> <td>F3<sub>H</sub></td>	Security Erase Prepare	F3 <sub>H</sub>
Security Set PasswordF1 <sub>H</sub> Security UnlockF2 <sub>H</sub> Seek70 <sub>H</sub> Set Max AddressF9 <sub>H</sub> Set Max AddressF9 <sub>H</sub> Note: Individual Set Max Address00 <sub>H</sub> placed in the Set Max Fauruse registPassword: 01 <sub>H</sub> Lock: 02 <sub>H</sub> 02 <sub>H</sub> FeaturesEF6 <sub>H</sub> Set Max Address Extended37 <sub>H</sub> Set Max Address Extended37 <sub>H</sub> Set Max Address Extended37 <sub>H</sub> Set Max Address Extended66 <sub>H</sub> SleepE6 <sub>H</sub> S.M.A.R.T. Enable/Disable Autosave80 <sub>H</sub> / D9 <sub>H</sub> S.M.A.R.T. Enable/Disable Autosave80 <sub>H</sub> / D8 <sub>H</sub> S.M.A.R.T. Enable/Disable Autosave80 <sub>H</sub> / D4 <sub>H</sub> S.M.A.R.T. Read Data80 <sub>H</sub> / D4 <sub>H</sub> S.M.A.R.T. Read Data80 <sub>H</sub> / D4 <sub>H</sub> S.M.A.R.T. Read Data80 <sub>H</sub> / D3 <sub>H</sub> S.M.A.R.T. Read Log Sector80 <sub>H</sub> / D3 <sub>H</sub> S.M.A.R.T. Wite Log Sector80 <sub>H</sub> / D4 <sub>H</sub> Write DMACA <sub>H</sub> Write DMA Extended35 <sub>H</sub> Write DMA FUA ExtendedCD <sub>H</sub> Write DMA FUA ExtendedCD <sub>H</sub> Write Multiple	Security Erase Unit	F4 <sub>H</sub>
Security Unlock $F2_H$ Seek $70_H$ Set Features $EF_H$ Set Max Address $F9_H$ Note: Individual Set Max Address $O0_H$ placed in the Set Max Features register as defined to the right.Password: $01_H$ Password: $02_H$ Unlock: $03_H$ Set Max Address Extended $37_H$ Set Max Address Extended $37_H$ Set Multiple Mode $C6_H$ Set Max Address Extended $30_H / D8_H$ S.M.A.R.T. Disable Operations $B0_H / D8_H$ S.M.A.R.T. Enable/Disable Autosave $B0_H / D4_H$ S.M.A.R.T. Enable/Disable Autosave $B0_H / D4_H$ S.M.A.R.T. Read Ottime $B0_H / D4_H$ S.M.A.R.T. Read Ottime $B0_H / D4_H$ S.M.A.R.T. Read Data $B0_H / D4_H$ S.M.A.R.T. Read Data $B0_H / D4_H$ S.M.A.R.T. Read Log Sector $B0_H / D8_H$ S.M.A.R.T. Read Log Sector $B0_H / D8_H$ S.M.A.R.T. Read Log Sector $B0_H / D6_H$ S.M.A.R.T. Write Log Sector $B0_H / D0_H$ S.M.A.R.T. Write Log Extended $35_H$ Write DMA Extended $CD_H$ Write Multiple Extended $SB_H$ Write Multiple Extended <t< td=""><td>Security Freeze</td><td>F5<sub>H</sub></td></t<>	Security Freeze	F5 <sub>H</sub>
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Set Max AddressF9H HNote: Individual Set Max Address commands are identified by the value placed in the Set Mar Fatures registerAddress: Password: 00HSet Max Address Extended Set Max Extended Write DMA Extended Write DMA Extended Write DMA Extended Write Multiple Extended Write Multiple Extended Write Multiple Extended Write Setors Write Multiple Extended Write Setors Without Retries Net Setors Write Setors Extended Write Setors Extended Write Setors Extended Write Setors Extended Write Setors Sate Net Setors Extended Write Setors Extended Withe Withou Retries Write Setors Extended Wr	Seek	70 <sub>H</sub>
Note:Individual Set Max Address commands are identified by the value placed in the Set Max Peatures regis- ter as defined to the right.Address: $00_H$ Password: $00_H$ $01_H$ Lock: $02_H$ $02_H$ Freeze Lock: $00_H$ $03_H$ Set Max Address Extended $37_H$ C6 $_H$ $37_H$ Freeze Lock: $03_H$ Freeze Lock: $04_H$ Set Multiple ModeC6 $_H$ $Sep$ $Set$ Max Address Extended $37_H$ C6 $_H$ SileepE6 $_H$ $S.M.A.R.T. Enable/Disable AutosaveB0_H / D2_HS.M.A.R.T. Enable/Disable AutosaveB0_H / D2_HS.M.A.R.T. Enable/Disable AutosaveS.M.A.R.T. Enable/Disable AutosaveB0_H / D2_HS.M.A.R.T. Enable/Disable AutosaveS.M.A.R.T. Enable/Disable AutosaveB0_H / D2_HS.M.A.R.T. Eaclute OfflineS.M.A.R.T. Read DegrationsB0_H / D4_HS.M.A.R.T. Read DataS.M.A.R.T. Read DataB0_H / D5_HS.M.A.R.T. Read Log SectorS.M.A.R.T. Read Log SectorB0_H / D2_HS.M.A.R.T. Save Attribute ValuesB0_H / D2_HS.M.A.R.T. Write Log SectorB0_H / D2_HStandbyE2_HStandbyE2_HStandbyE2_HWrite DMACA_HWrite DMACA_H$	Set Features	EF <sub>H</sub>
commands are identified by the value placed in the Set Max Peatures regis- ter as defined to the right.Password: $0.01_{H}$ Unlock: $0.03_{H}$ Preced Lock: $0.04_{H}$ Set Max Address Extended $37_{H}$ Set Multiple Mode $C6_{H}$ Sleep $E6_{H}$ S.M.A.R.T. Disable Operations $B0_{H} / D9_{H}$ S.M.A.R.T. Enable/Disable Autosave $B0_{H} / D2_{H}$ S.M.A.R.T. Enable/Disable Autosave $B0_{H} / D2_{H}$ S.M.A.R.T. Enable/Disable Autosave $B0_{H} / D2_{H}$ S.M.A.R.T. Enable/Disable Autosave $B0_{H} / D4_{H}$ S.M.A.R.T. Read Deprations $B0_{H} / D4_{H}$ S.M.A.R.T. Read Deprations $B0_{H} / D4_{H}$ S.M.A.R.T. Read Data $B0_{H} / D5_{H}$ S.M.A.R.T. Read Log Sector $B0_{H} / D5_{H}$ S.M.A.R.T. Read Log Sector $B0_{H} / D3_{H}$ S.M.A.R.T. Save Attribute Values $B0_{H} / D6_{H}$ S.M.A.R.T. Write Log Sector $B0_{H} / D6_{H}$ Standby $E2_{H}$ Standby $E2_{H}$ Standby $E2_{H}$ Write DMA $CA_{H}$ Write DMA $CA_{H}$ Write DMA textended $35_{H}$ Write DMA textended $3F_{H}$ Write DMA Vithout Retries $CB_{H}$ Write DMA Uthout Retries $CB_{H}$ Write Multiple $C5_{H}$ Write Multiple Extended $39_{H}$ Write Multiple Extended $CE_{H}$ Write Multiple Extended $CE_{H}$ Write Sectors Without Retries $31_{H}$ Write Sectors Without Retries $34_{H}$ </td <td>Set Max Address</td> <td>F9<sub>H</sub></td>	Set Max Address	F9 <sub>H</sub>
Set Multiple Mode $CG_H$ Sleep $EG_H$ S.M.A.R.T. Disable Operations $BO_H / D9_H$ S.M.A.R.T. Enable/Disable Autosave $BO_H / D2_H$ S.M.A.R.T. Enable/Disable Autosave $BO_H / D2_H$ S.M.A.R.T. Enable Operations $BO_H / D8_H$ S.M.A.R.T. Execute Offline $BO_H / D4_H$ S.M.A.R.T. Read Attribute Thresholds $BO_H / D1_H$ S.M.A.R.T. Read Data $BO_H / D0_H$ S.M.A.R.T. Read Data $BO_H / D5_H$ S.M.A.R.T. Read Log Sector $BO_H / D5_H$ S.M.A.R.T. Return Status $BO_H / D5_H$ S.M.A.R.T. Save Attribute Values $BO_H / D5_H$ S.M.A.R.T. Write Log Sector $BO_H / D6_H$ Standby $E2_H$ Standby $E2_H$ Standby Immediate $EO_H$ Write DMA $CA_H$ Write DMA Extended $35_H$ Write DMA Extended $25_H$ Write DMA FUA Extended $CD_H$ Write DMA Without Retries $CB_H$ Write Multiple $C5_H$ Write Multiple FUA Extended $29_H$ Write Multiple FUA Extended $CE_H$ Write Multiple FUA Extended $CE_H$ Write Sectors $30_H$ Write Sectors $30_H$ Write Sectors $30_H$ Write Sectors $30_H$	commands are identified by the value placed in the Set Max Features regis-	Password: 01 <sub>H</sub> Lock: 02 <sub>H</sub> Unlock: 03 <sub>H</sub>
Sleep $E6_H$ S.M.A.R.T. Disable Operations $B0_H / D9_H$ S.M.A.R.T. Enable/Disable Autosave $B0_H / D2_H$ S.M.A.R.T. Enable/Disable Autosave $B0_H / D2_H$ S.M.A.R.T. Enable/Disable Autosave $B0_H / D8_H$ S.M.A.R.T. Execute Offline $B0_H / D4_H$ S.M.A.R.T. Execute Offline $B0_H / D1_H$ S.M.A.R.T. Read Data $B0_H / D0_H$ S.M.A.R.T. Read Data $B0_H / D0_H$ S.M.A.R.T. Read Log Sector $B0_H / D5_H$ S.M.A.R.T. Return Status $B0_H / DA_H$ S.M.A.R.T. Save Attribute Values $B0_H / D3_H$ S.M.A.R.T. Write Log Sector $B0_H / D6_H$ Standby $E2_H$ Standby $E2_H$ Standby Immediate $E0_H$ Write DMA $CA_H$ Write DMA Extended $CD_H$ Write DMA Extended $CD_H$ Write Log Extended $SF_H$ Write Multiple $C5_H$ Write Multiple FUA Extended $CE_H$ Write Sectors $30_H$ Write Sectors $30_H$ Write Sectors Without Retries $31_H$ Write Sectors Extended $34_H$	Set Max Address Extended	37 <sub>H</sub>
S.M.A.R.T. Disable Operations $B0_H / D9_H$ S.M.A.R.T. Enable/Disable Autosave $B0_H / D2_H$ S.M.A.R.T. Enable/Disable Autosave $B0_H / D8_H$ S.M.A.R.T. Enable Operations $B0_H / D8_H$ S.M.A.R.T. Execute Offline $B0_H / D4_H$ S.M.A.R.T. Read Attribute Thresholds $B0_H / D0_H$ S.M.A.R.T. Read Data $B0_H / D0_H$ S.M.A.R.T. Read Log Sector $B0_H / D5_H$ S.M.A.R.T. Return Status $B0_H / DA_H$ S.M.A.R.T. Save Attribute Values $B0_H / D3_H$ S.M.A.R.T. Save Attribute Values $B0_H / D6_H$ Standby $E2_H$ Standby $E2_H$ Standby Immediate $E0_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA $CA_H$ Write DMA FUA Extended $35_H$ Write Log Extended $3F_H$ Write Log Extended $39_H$ Write Multiple $C5_H$ Write Multiple FUA Extended $CE_H$ Write Sectors $30_H$ Write Sectors $30_H$ Write Sectors $31_H$ Write Sectors Kithout Retries $31_H$	Set Multiple Mode	C6 <sub>H</sub>
S.M.A.R.T. Enable/Disable Autosave $BO_H / D2_H$ S.M.A.R.T. Enable Operations $BO_H / D8_H$ S.M.A.R.T. Execute Offline $BO_H / D4_H$ S.M.A.R.T. Read Attribute Thresholds $BO_H / D1_H$ S.M.A.R.T. Read Attribute Thresholds $BO_H / D0_H$ S.M.A.R.T. Read Data $BO_H / D5_H$ S.M.A.R.T. Read Log Sector $BO_H / D3_H$ S.M.A.R.T. Save Attribute Values $BO_H / D3_H$ S.M.A.R.T. Write Log Sector $BO_H / D6_H$ Standby $E2_H$ Standby $E2_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA Extended $35_H$ Write DMA FUA Extended $CB_H$ Write Log Extended $3F_H$ Write Log Extended $3F_H$ Write Multiple $C5_H$ Write Multiple FUA Extended $39_H$ Write Multiple FUA Extended $CE_H$ Write Sectors $30_H$ Write Sectors $30_H$	Sleep	E6 <sub>H</sub>
S.M.A.R.T. Enable Operations $BO_H / D8_H$ S.M.A.R.T. Execute Offline $BO_H / D4_H$ S.M.A.R.T. Read Attribute Thresholds $BO_H / D1_H$ S.M.A.R.T. Read Data $BO_H / D0_H$ S.M.A.R.T. Read Data $BO_H / D5_H$ S.M.A.R.T. Read Log Sector $BO_H / DA_H$ S.M.A.R.T. Return Status $BO_H / DA_H$ S.M.A.R.T. Save Attribute Values $BO_H / DA_H$ S.M.A.R.T. Write Log Sector $BO_H / D6_H$ Standby $E2_H$ Standby Immediate $EO_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA Extended $35_H$ Write DMA FUA Extended $CD_H$ Write Log Extended $3F_H$ Write Log Extended $3F_H$ Write Multiple $C5_H$ Write Multiple $C5_H$ Write Multiple FUA Extended $30_H$ Write Sectors $30_H$ Write Sectors $30_H$ Write Sectors Without Retries $31_H$ Write Sectors Extended $34_H$	S.M.A.R.T. Disable Operations	B0 <sub>H</sub> / D9 <sub>H</sub>
S.M.A.R.T. Execute Offline $BO_H / D4_H$ S.M.A.R.T. Read Attribute Thresholds $BO_H / D1_H$ S.M.A.R.T. Read Data $BO_H / D0_H$ S.M.A.R.T. Read Log Sector $BO_H / D5_H$ S.M.A.R.T. Return Status $BO_H / DA_H$ S.M.A.R.T. Save Attribute Values $BO_H / DA_H$ S.M.A.R.T. Write Log Sector $BO_H / DA_H$ S.M.A.R.T. Write Log Sector $BO_H / D6_H$ Standby $E2_H$ Standby Immediate $EO_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA Extended $35_H$ Write DMA FUA Extended $CB_H$ Write DMA Without Retries $CB_H$ Write Log Extended $3F_H$ Write Multiple $C5_H$ Write Multiple FUA Extended $CE_H$ Write Sectors $30_H$ Write Sectors $30_H$ Write Sectors Without Retries $31_H$ Write Sectors Extended $34_H$	S.M.A.R.T. Enable/Disable Autosave	B0 <sub>H</sub> / D2 <sub>H</sub>
S.M.A.R.T. Read Attribute Thresholds $B0_H / D1_H$ S.M.A.R.T. Read Data $B0_H / D0_H$ S.M.A.R.T. Read Log Sector $B0_H / D5_H$ S.M.A.R.T. Return Status $B0_H / DA_H$ S.M.A.R.T. Save Attribute Values $B0_H / D3_H$ S.M.A.R.T. Write Log Sector $B0_H / D6_H$ Standby $E2_H$ Standby Immediate $E0_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA $CA_H$ Write DMA FUA Extended $35_H$ Write DMA Without Retries $CB_H$ Write Log Extended $3F_H$ Write Multiple $C5_H$ Write Multiple FUA Extended $29_H$ Write Multiple FUA Extended $CE_H$ Write Sectors $30_H$ Write Sectors Sithout Retries $31_H$ Write Sectors Extended $34_H$	S.M.A.R.T. Enable Operations	B0 <sub>H</sub> / D8 <sub>H</sub>
S.M.A.R.T. Read Data $BO_H / DO_H$ S.M.A.R.T. Read Log Sector $BO_H / DS_H$ S.M.A.R.T. Return Status $BO_H / DA_H$ S.M.A.R.T. Save Attribute Values $BO_H / DA_H$ S.M.A.R.T. Save Attribute Values $BO_H / DB_H$ S.M.A.R.T. Write Log Sector $BO_H / DG_H$ Standby $E2_H$ Standby Immediate $EO_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA Extended $35_H$ Write DMA FUA Extended $CD_H$ Write DMA Without Retries $CB_H$ Write Log Extended $3F_H$ Write Multiple $C5_H$ Write Multiple Extended $39_H$ Write Multiple FUA Extended $CE_H$ Write Sectors $30_H$ Write Sectors Without Retries $31_H$ Write Sectors Extended $34_H$	S.M.A.R.T. Execute Offline	B0 <sub>H</sub> / D4 <sub>H</sub>
S.M.A.R.T. Read Log Sector $B0_H / D5_H$ S.M.A.R.T. Return Status $B0_H / DA_H$ S.M.A.R.T. Save Attribute Values $B0_H / D3_H$ S.M.A.R.T. Write Log Sector $B0_H / D6_H$ Standby $E2_H$ Standby Immediate $E0_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA FUA Extended $35_H$ Write DMA FUA Extended $CB_H$ Write DMA Without Retries $CB_H$ Write Log Extended $3F_H$ Write Multiple $C5_H$ Write Multiple FUA Extended $CE_H$ Write Sectors $30_H$ Write Sectors Without Retries $31_H$ Write Sectors Extended $34_H$	S.M.A.R.T. Read Attribute Thresholds	B0 <sub>H</sub> / D1 <sub>H</sub>
S.M.A.R.T. Return Status $B0_H / DA_H$ S.M.A.R.T. Save Attribute Values $B0_H / D3_H$ S.M.A.R.T. Write Log Sector $B0_H / D6_H$ Standby $E2_H$ Standby Immediate $E0_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA Extended $35_H$ Write DMA FUA Extended $CD_H$ Write Log Extended $SF_H$ Write Log Extended $3F_H$ Write Multiple $C5_H$ Write Multiple FUA Extended $39_H$ Write Sectors $30_H$ Write Sectors Without Retries $31_H$ Write Sectors Extended $34_H$	S.M.A.R.T. Read Data	B0 <sub>H</sub> / D0 <sub>H</sub>
S.M.A.R.T. Save Attribute Values $BO_H / D3_H$ S.M.A.R.T. Write Log Sector $BO_H / D6_H$ Standby $E2_H$ Standby Immediate $E0_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA Extended $35_H$ Write DMA FUA Extended $CD_H$ Write DMA Without Retries $CB_H$ Write Log Extended $3F_H$ Write Multiple $C5_H$ Write Multiple Extended $39_H$ Write Sectors $30_H$ Write Sectors Without Retries $31_H$ Write Sectors Extended $34_H$	S.M.A.R.T. Read Log Sector	B0 <sub>H</sub> / D5 <sub>H</sub>
S.M.A.R.T. Write Log Sector $BO_H / D6_H$ Standby $E2_H$ Standby Immediate $E0_H$ Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA Extended $35_H$ Write DMA FUA Extended $CD_H$ Write DMA Without Retries $CB_H$ Write Log Extended $3F_H$ Write Multiple $C5_H$ Write Multiple FUA Extended $20_H$ Write Multiple Sectors $30_H$ Write Sectors Without Retries $31_H$ Write Sectors Extended $34_H$	S.M.A.R.T. Return Status	B0 <sub>H</sub> / DA <sub>H</sub>
StandbyE2 <sub>H</sub> Standby ImmediateE0 <sub>H</sub> Write BufferE8 <sub>H</sub> Write DMACA <sub>H</sub> Write DMA Extended35 <sub>H</sub> Write DMA FUA ExtendedCD <sub>H</sub> Write DMA Without RetriesCB <sub>H</sub> Write Log Extended3F <sub>H</sub> Write MultipleC5 <sub>H</sub> Write Multiple ExtendedCE <sub>H</sub> Write Sectors30 <sub>H</sub> Write Sectors Without Retries31 <sub>H</sub> Write Sectors Extended34 <sub>H</sub>	S.M.A.R.T. Save Attribute Values	B0 <sub>H</sub> / D3 <sub>H</sub>
Standby ImmediateE0HWrite BufferE8HWrite DMACAHWrite DMA Extended35HWrite DMA FUA ExtendedCDHWrite DMA Without RetriesCBHWrite Log Extended3FHWrite MultipleC5HWrite Multiple Extended39HWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	S.M.A.R.T. Write Log Sector	B0 <sub>H</sub> / D6 <sub>H</sub>
Write Buffer $E8_H$ Write DMA $CA_H$ Write DMA Extended $35_H$ Write DMA FUA Extended $CD_H$ Write DMA Without Retries $CB_H$ Write Log Extended $3F_H$ Write Multiple $C5_H$ Write Multiple Extended $39_H$ Write Multiple FUA Extended $CE_H$ Write Sectors $30_H$ Write Sectors Without Retries $31_H$ Write Sectors Extended $34_H$	Standby	E2 <sub>H</sub>
Write DMACA <sub>H</sub> Write DMA Extended35 <sub>H</sub> Write DMA FUA ExtendedCD <sub>H</sub> Write DMA Without RetriesCB <sub>H</sub> Write Log Extended3F <sub>H</sub> Write MultipleC5 <sub>H</sub> Write Multiple Extended39 <sub>H</sub> Write Multiple FUA ExtendedCE <sub>H</sub> Write Sectors30 <sub>H</sub> Write Sectors Without Retries31 <sub>H</sub> Write Sectors Extended34 <sub>H</sub>	Standby Immediate	E0 <sub>H</sub>
Write DMA Extended35HWrite DMA FUA ExtendedCDHWrite DMA Without RetriesCBHWrite Log Extended3FHWrite MultipleC5HWrite Multiple Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	Write Buffer	E8 <sub>H</sub>
Write DMA FUA ExtendedCDHWrite DMA Without RetriesCBHWrite Log Extended3FHWrite MultipleC5HWrite Multiple Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	Write DMA	CA <sub>H</sub>
Write DMA Without RetriesCBHWrite Log Extended3FHWrite MultipleC5HWrite Multiple Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	Write DMA Extended	35 <sub>H</sub>
Write Log Extended       3F <sub>H</sub> Write Multiple       C5 <sub>H</sub> Write Multiple Extended       39 <sub>H</sub> Write Multiple FUA Extended       CE <sub>H</sub> Write Sectors       30 <sub>H</sub> Write Sectors Without Retries       31 <sub>H</sub> Write Sectors Extended       34 <sub>H</sub>	Write DMA FUA Extended	CD <sub>H</sub>
Write MultipleC5 <sub>H</sub> Write Multiple Extended39 <sub>H</sub> Write Multiple FUA ExtendedCE <sub>H</sub> Write Sectors30 <sub>H</sub> Write Sectors Without Retries31 <sub>H</sub> Write Sectors Extended34 <sub>H</sub>	Write DMA Without Retries	CB <sub>H</sub>
Write Multiple Extended39HWrite Multiple FUA ExtendedCEHWrite Sectors30HWrite Sectors Without Retries31HWrite Sectors Extended34H	Write Log Extended	3F <sub>H</sub>
Write Multiple FUA Extended     CE <sub>H</sub> Write Sectors     30 <sub>H</sub> Write Sectors Without Retries     31 <sub>H</sub> Write Sectors Extended     34 <sub>H</sub>	Write Multiple	C5 <sub>H</sub>
Write Sectors     30 <sub>H</sub> Write Sectors Without Retries     31 <sub>H</sub> Write Sectors Extended     34 <sub>H</sub>	Write Multiple Extended	39 <sub>H</sub>
Write Sectors Without Retries     31 <sub>H</sub> Write Sectors Extended     34 <sub>H</sub>	Write Multiple FUA Extended	CEH
Write Sectors Extended 34 <sub>H</sub>	Write Sectors	30 <sub>H</sub>
	Write Sectors Without Retries	31 <sub>H</sub>
Write Uncorrectable 45 <sub>H</sub>	Write Sectors Extended	34 <sub>H</sub>
	Write Uncorrectable	45 <sub>H</sub>

# 4.3.1 Identify Device command

The Identify Device command (command code  $EC_H$ ) transfers information about the drive to the host following power up. The data is organized as a single 512-byte block of data, whose contents are shown in Table 7 on page 25. All reserved bits or words should be set to zero. Parameters listed with an "x" are drive-specific or vary with the state of the drive. See Section 2.0 on page 3 for default parameter settings.

The following commands contain drive-specific features that may not be included in the Serial ATA specification.

Word	Description	Value
0	Configuration information: • Bit 15: 0 = ATA; 1 = ATAPI • Bit 7: removable media • Bit 6: removable controller • Bit 0: reserved	0C5A <sub>H</sub>
1	Number of logical cylinders	16,383
2	ATA-reserved	0000 <sub>H</sub>
3	Number of logical heads	16
4	Retired	0000 <sub>H</sub>
5	Retired	0000 <sub>H</sub>
6	Number of logical sectors per logical track: 63	003F <sub>H</sub>
7–9	Retired	0000 <sub>H</sub>
10–19	Serial number: (20 ASCII characters, 0000 <sub>H</sub> = none)	ASCII
20	Retired	0000 <sub>H</sub>
21	Retired	0400 <sub>H</sub>
22	Obsolete	0000 <sub>H</sub>
23–26	Firmware revision (8 ASCII character string, padded with blanks to end of string)	x.xx
27–46	Drive model number: (40 ASCII characters, padded with blanks to end of string)	
47	(Bits 7–0) Maximum sectors per interrupt on Read multiple and Write multiple (16)	8010 <sub>H</sub>
48	Reserved	0000 <sub>H</sub>
49	Standard Standby timer, IORDY supported and may be disabled	2F00 <sub>H</sub>
50	ATA-reserved	0000 <sub>H</sub>
51	PIO data-transfer cycle timing mode	0200 <sub>H</sub>
52	Retired	0200 <sub>H</sub>
53	Words 54–58, 64–70 and 88 are valid	0007 <sub>H</sub>
54	Number of current logical cylinders	xxxx <sub>H</sub>
55	Number of current logical heads	xxxx <sub>H</sub>
56	Number of current logical sectors per logical track	xxxx <sub>H</sub>
57–58	Current capacity in sectors	xxxx <sub>H</sub>
59	Number of sectors transferred during a Read Multiple or Write Multiple command	xxxx <sub>H</sub>

Word	Description	Value
60–61	Total number of user-addressable LBA sectors available (see Section 2.1 for related information) *Note: The maximum value allowed in this field is: 0FFFFFFh (268,435,455 sectors, 137GB). Drives with capacities over 137GB will have 0FFFFFFh in this field and the actual number of user-address- able LBAs specified in words 100-103. This is required for drives that support the 48-bit addressing feature.	0FFFFFFh*
62	Retired	0000 <sub>H</sub>
63	Multiword DMA active and modes supported (see note following this table)	<i>xx</i> 07 <sub>H</sub>
64	Advanced PIO modes supported (modes 3 and 4 supported)	0003 <sub>H</sub>
65	Minimum multiword DMA transfer cycle time per word (120 nsec)	0078 <sub>H</sub>
66	Recommended multiword DMA transfer cycle time per word (120 nsec)	0078 <sub>H</sub>
67	Minimum PIO cycle time without IORDY flow control (240 nsec)	00F0 <sub>H</sub>
68	Minimum PIO cycle time with IORDY flow control (120 nsec)	0078 <sub>H</sub>
69–74	ATA-reserved	0000 <sub>H</sub>
75	Queue depth	001F <sub>H</sub>
76	Serial ATA capabilities	xxxx <sub>H</sub>
77	Reserved for future Serial ATA definition	xxxx <sub>H</sub>
78	Serial ATA features supported	xxxx <sub>H</sub>
79	Serial ATA features enabled	xxxx <sub>H</sub>
80	Major version number	003E <sub>H</sub>
81	Minor version number	0028 <sub>H</sub>
82	Command sets supported	364B <sub>H</sub>
83	Command sets supported	7C03 <sub>H</sub>
84	Command sets support extension (see note following this table)	4003 <sub>H</sub> See Word 108-111 note. (4003H = 0100000000000011 binary)
85	Command sets enabled	30xx <sub>H</sub>
86	Command sets enabled	0001 <sub>H</sub>
87	Command sets enable extension	4000 <sub>H</sub>
88	Ultra DMA support and current mode (see note following this table)	<i>xx</i> 3F <sub>H</sub>
89	Security erase time	0000 <sub>H</sub>
90	Enhanced security erase time	0000 <sub>H</sub>
92	Master password revision code	FFFE <sub>H</sub>
93	Hardware reset value	xxxx <sub>H</sub>
95–99	ATA-reserved	0000 <sub>H</sub>
100–103	Total number of user-addressable LBA sectors available (see Section 2.1 for related information). These words are required for drives that support the 48-bit addressing feature. Maximum value: 0000FFFFFFFFFFFh.	ST32000542AS = 3,907,029,168 ST31500541AS = 2,930,277,168 ST31000520AS = 1,953,525,168
104–107	ATA-reserved	0000 <sub>H</sub>

Word	Description	Value
108–111	The mandatory value of the world wide name (WWN) for the drive. NOTE: This field is valid if word 84, bit 8 is set to 1 indicating 64-bit WWN support.	Each drive will have a unique value.
112–127	ATA-reserved	0000 <sub>H</sub>
128	Security status	0001 <sub>H</sub>
129–159	Seagate-reserved	xxxx <sub>H</sub>
160–254	ATA-reserved	0000 <sub>H</sub>
255	Integrity word	xxA5 <sub>H</sub>

Note. Advanced Power Management (APM) and Automatic Acoustic Management (AAM) features are not supported

Note. See the bit descriptions below for words 63, 84, and 88 of the Identify Drive data.

Description (if bit is set to 1)				
Bit	Word 63			
0	Multiword DMA mode 0 is supported.			
1	Multiword DMA mode 1 is supported.			
2	Multiword DMA mode 2 is supported.			
8	Multiword DMA mode 0 is currently active.			
9	Multiword DMA mode 1 is currently active.			
10	Multiword DMA mode 2 is currently active.			
Bit	Word 84			
0	SMART error login is supported.			
1	SMART self-test is supported.			
2	Media serial number is supported.			
3	Media Card Pass Through Command feature set is supported.			
4	Streaming feature set is supported.			
5	GPL feature set is supported.			
6	WRITE DMA FUA EXT and WRITE MULTIPLE FUA EXT commands are supported.			
7	WRITE DMA QUEUED FUA EXT command is supported.			
8	64-bit World Wide Name is supported.			
9-10	Obsolete.			
11-1	2 Reserved for TLC.			
13	IDLE IMMEDIATE command with IUNLOAD feature is supported.			
14	Shall be set to 1.			
15	Shall be cleared to 0.			

	Bit	Word 88
(	0	Ultra DMA mode 0 is supported.
	1	Ultra DMA mode 1 is supported.
	2	Ultra DMA mode 2 is supported.
:	3	Ultra DMA mode 3 is supported.
	4	Ultra DMA mode 4 is supported.
4	5	Ultra DMA mode 5 is supported.
(	6	Ultra DMA mode 6 is supported.
	8	Ultra DMA mode 0 is currently active.
9	9	Ultra DMA mode 1 is currently active.
	10	Ultra DMA mode 2 is currently active.
	11	Ultra DMA mode 3 is currently active.
	12	Ultra DMA mode 4 is currently active.
	13	Ultra DMA mode 5 is currently active.
	14	Ultra DMA mode 6 is currently active.

#### 4.3.2 Set Features command

This command controls the implementation of various features that the drive supports. When the drive receives this command, it sets BSY, checks the contents of the Features register, clears BSY and generates an interrupt. If the value in the register does not represent a feature that the drive supports, the command is aborted. Power-on default has the read look-ahead and write caching features enabled. The acceptable values for the Features register are defined as follows:

#### Table 8: Set Features command values

- 02<sub>H</sub> Enable write cache (*default*).
- 03<sub>H</sub> Set transfer mode (based on value in Sector Count register). Sector Count register values:
  - 00<sub>H</sub> Set PIO mode to default (PIO mode 2).
  - $01_{H}$  Set PIO mode to default and disable IORDY (PIO mode 2).
  - 08<sub>H</sub> PIO mode 0
  - 09<sub>H</sub> PIO mode 1
  - 0A<sub>H</sub> PIO mode 2
  - 0B<sub>H</sub> PIO mode 3
  - 0C<sub>H</sub> PIO mode 4 (default)
  - 20<sub>H</sub> Multiword DMA mode 0
  - 21<sub>H</sub> Multiword DMA mode 1
  - 22<sub>H</sub> Multiword DMA mode 2
  - 40<sub>H</sub> Ultra DMA mode 0
  - 41<sub>H</sub> Ultra DMA mode 1
  - 42<sub>H</sub> Ultra DMA mode 2
  - 43<sub>H</sub> Ultra DMA mode 3
  - 44<sub>H</sub> Ultra DMA mode 4
  - 45<sub>H</sub> Ultra DMA mode 5
  - 46<sub>H</sub> Ultra DMA mode 6
- 10<sub>H</sub> Enable use of SATA features
- 55<sub>H</sub> Disable read look-ahead (read cache) feature.
- 82<sub>H</sub> Disable write cache
- 90<sub>H</sub> Disable use of SATA features
- AA<sub>H</sub> Enable read look-ahead (read cache) feature (default).
- F1<sub>H</sub> Report full capacity available
- Note. At power-on, or after a hardware or software reset, the default values of the features are as indicated above.

### 4.3.3 S.M.A.R.T. commands

S.M.A.R.T. provides near-term failure prediction for disk drives. When S.M.A.R.T. is enabled, the drive monitors predetermined drive attributes that are susceptible to degradation over time. If self-monitoring determines that a failure is likely, S.M.A.R.T. makes a status report available to the host. Not all failures are predictable. S.M.A.R.T. predictability is limited to the attributes the drive can monitor. For more information on S.M.A.R.T. commands and implementation, see the *Draft ATA-5 Standard*.

SeaTools diagnostic software activates a built-in drive self-test (DST S.M.A.R.T. command for D4<sub>H</sub>) that eliminates unnecessary drive returns. The diagnostic software ships with all new drives and is also available at: <u>http://seatools.seagate.com</u>.

This drive is shipped with S.M.A.R.T. features disabled. You must have a recent BIOS or software package that supports S.M.A.R.T. to enable this feature. The table below shows the S.M.A.R.T. command codes that the drive uses.

Code in features register	S.M.A.R.T. command
D0 <sub>H</sub>	S.M.A.R.T. Read Data
D2 <sub>H</sub>	S.M.A.R.T. Enable/Disable Attribute Autosave
D3 <sub>H</sub>	S.M.A.R.T. Save Attribute Values
D4 <sub>H</sub>	S.M.A.R.T. Execute Off-line Immediate (runs DST)
D5 <sub>H</sub>	S.M.A.R.T. Read Log Sector
D6 <sub>H</sub>	S.M.A.R.T. Write Log Sector
D8 <sub>H</sub>	S.M.A.R.T. Enable Operations
D9 <sub>H</sub>	S.M.A.R.T. Disable Operations
DA <sub>H</sub>	S.M.A.R.T. Return Status

Table 9:S.M.A.R.T. commands

Note. If an appropriate code is not written to the Features Register, the command is aborted and 0x04 (abort) is written to the Error register.

## 5.0 Seagate Technology support services

#### Internet

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Seagate offers worldwide customer support for Seagate products. Seagate distributors, OEMs and other direct customers should contact their Seagate Customer Service Operations (CSO) representative for warranty-related issues. Resellers or end users of drive products should contact their place of purchase or Seagate warranty service for assistance. Have your serial number and model or part number available.

#### **Data Recovery Services**

Seagate offers data recovery services for all formats and all brands of storage media. Our data recovery services labs are currently located throughout the world. Additional information, including an online request form and data loss prevention resources, is available at <a href="http://services.seagate.com/index.aspx">http://services.seagate.com/index.aspx</a>.

#### **Authorized Service Centers**

Seagate Service Centers are available on a global basis for the return of defective products. See <u>www.seagate.com</u> for the service center near you.

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Presales, Technical, and Warranty Support							
Call Center	Toll-free	Direct dial					
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and Mexico	1-800-SEAGATE	+1-405-324-4700					

Data Recovery Services						
Call Center	Toll-free	Direct dial	FAX			
USA, Canada, and Mexico	1-800-475-0143	+1-905-474-2162	1-800-475-0158 +1-905-474-2459			

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Seagate Technology LLC 920 Disc Drive, Scotts Valley, California 95066-4544, USA Publication Number: 100564361, Rev. D, Printed in U.S.A.