



FIRST PASS SYSTEM SUCCESS

APPLICATION WORKSHOPS FOR HIGH-PERFORMANCE ELECTRONIC DESIGN



LVDS Network Camera System

Panasonic Electronic Devices
Panasonic Communications
Company
Molex Japan
Ansoft



Seminar Contents

- Introducing Panasonic
- Defining the Project
- Approaching to the Project
- Improving the Design
- EYE Design Tools from Ansoft
- Activity Introduction with Molex Japan
- Conclusion



Main Domains of Panasonic Group

Matsushita Electric Industrial Co., Ltd Head Office (Group & Global)

AVC Networks

Panasonic AVC Networks Company
Panasonic Communications Co., Ltd.
Panasonic Mobile Communications Co., Ltd.
Panasonic Automotive Systems Company
Panasonic System Solutions Company
Panasonic Shikoku Electronics Co., Ltd.

Appliances

Matsushita Home Appliances Company
Healthcare Business Company
Lighting Company
Matsushita Ecology Systems Co., Ltd.

Devices

Semiconductor Company
Panasonic Electronic Devices Co., Ltd.
Matsushita Battery Industrial Co., Ltd.
Motor Company

Matsushita Electric Works, Ltd.

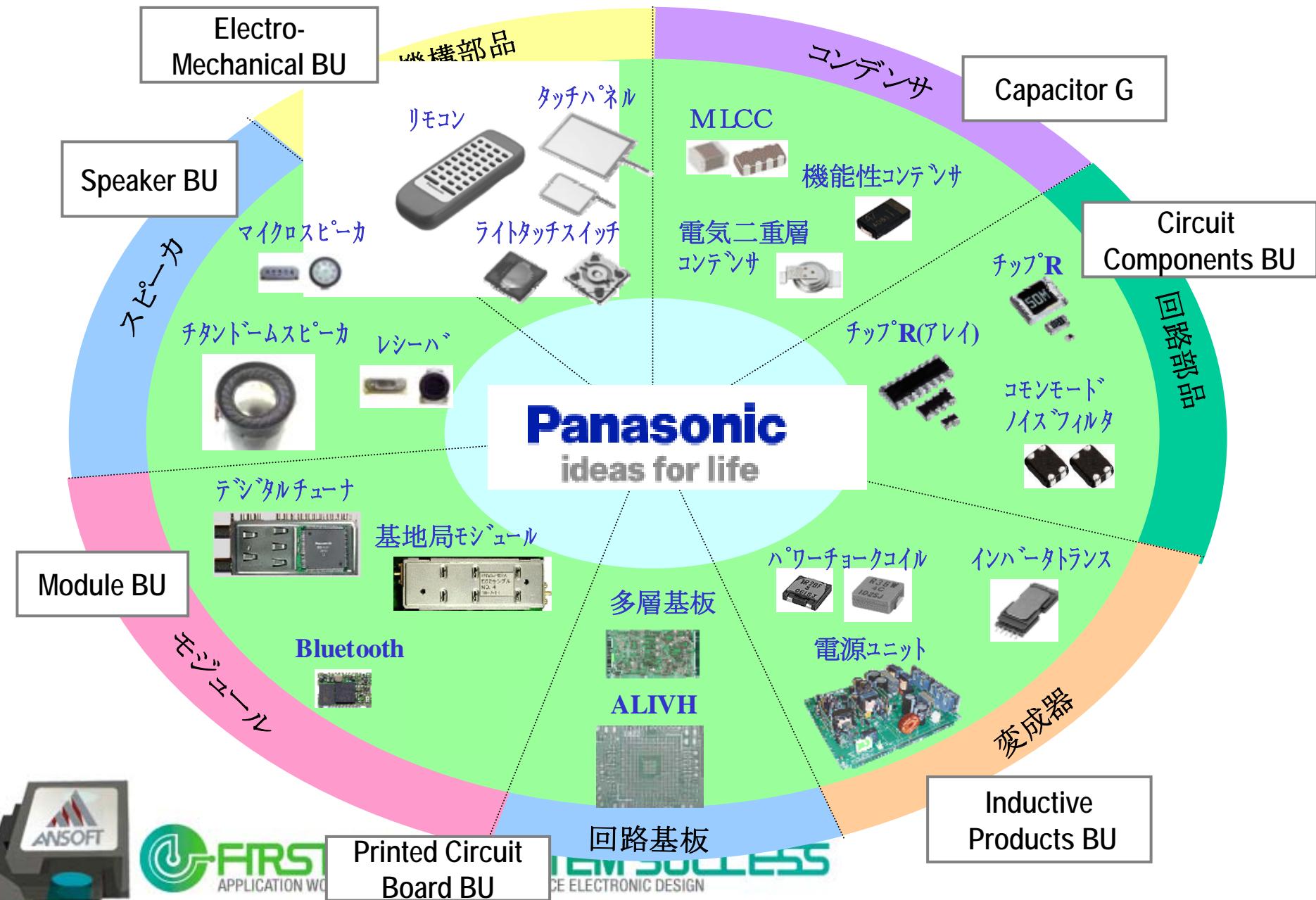
PanaHome Corporation

Victor Company of Japan, Ltd.

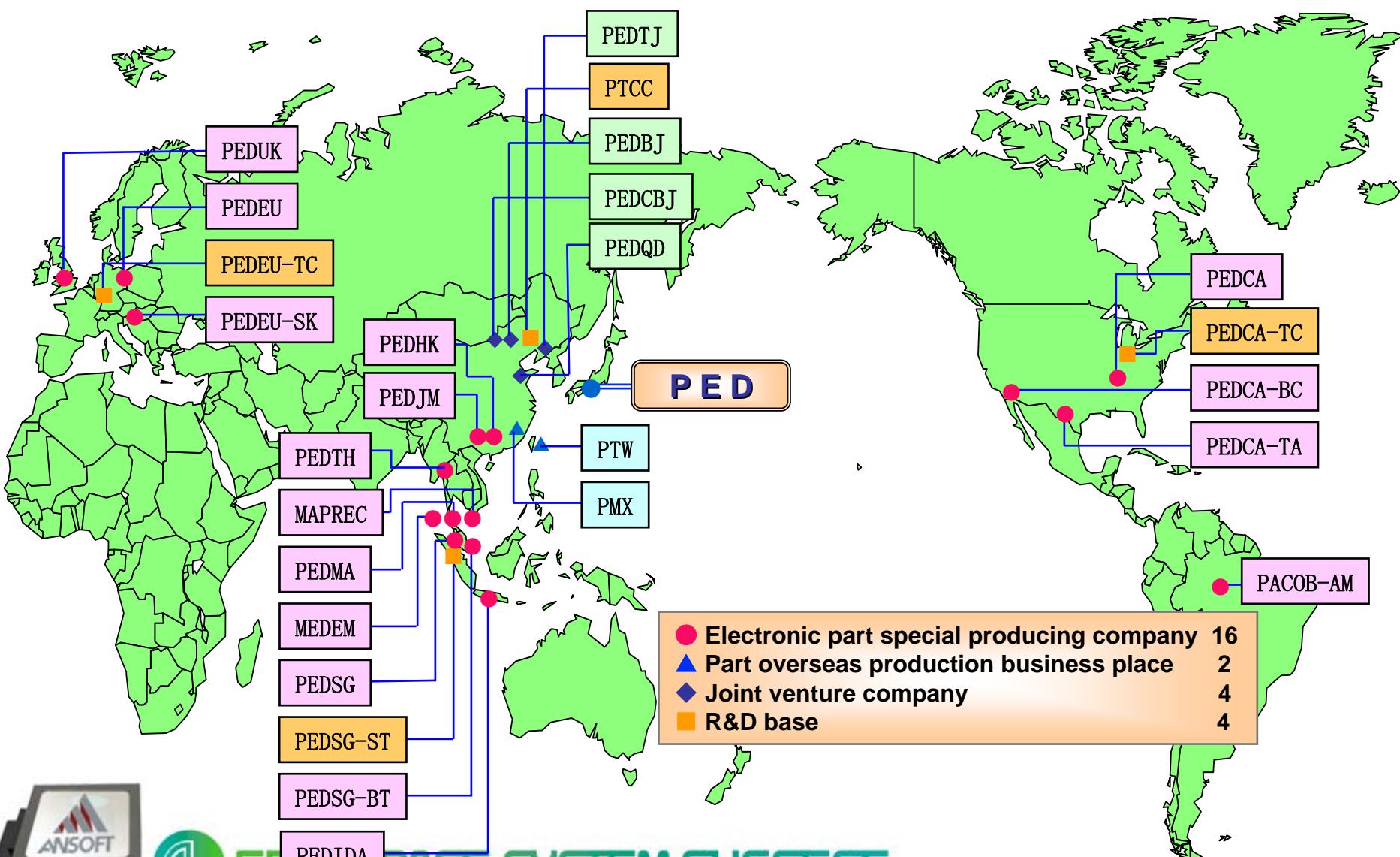
Others

Panasonic Factory Solutions Co., Ltd.
Matsushita Welding Systems Co., Ltd.
Others

Main Products in PED



Global production and R&D base list



CIRCUIT WORKS SYSTEM SUCCESS
APPLICATION WORKSHOPS FOR HIGH-PERFORMANCE ELECTRONIC DESIGN

LCR Solution on PED WEB Site

<http://panasonic.co.jp/ped/>



... Data Library
for simulation



... Device selection guide



... Device selection guide

A screenshot of the Panasonic Electronic Devices Co., Ltd. website. At the top, there is a search bar with "Search Keyword" and "Search" buttons. The Panasonic logo with the tagline "ideas for life" is displayed. Below the header, there is a navigation menu with links to "Corporate Profile", "Products Information", "News", and "Environmental Activities". A "Products" link is also present. The main content area features a background image of electronic components like capacitors and resistors floating over a binary code grid, with the slogan "Innovate Together" overlaid. There are several call-to-action buttons: "Device Library as components for design tool of Agilent, Ansoft and AWR", "Noise Suppression Solutions", and "SMD Capacitor Selection Guide".

Panasonic
ideas for life

> Search Keyword

Panasonic Electronic Devices Co.,Ltd. Products

Corporate Profile | Products Information | News | Environmental Activities

TOP

• Products Information

• News Releases

• Corporate Profile

• Environmental Activities

• Contact Us

Device Library as components for design tool of Agilent, Ansoft and AWR

Noise Suppression Solutions

SMD Capacitor Selection Guide

Innovate Together



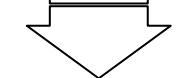
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Purpose of Device Library

Customer/Set

[Conventional]

Catalog retrieval



Sample order



Circuit design and Experimental evaluation

Device selection

Experimental evaluation

[Current]

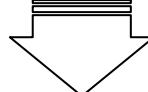
Catalog retrieval on WEB



Circuit design(simulation)

Device selection

Sample order on WEB



Experimental evaluation

Circuit design process

PED/Device

<http://industrial.panasonic.com/i/library.html>

Simulator

High frequency circuit & system
design tool



Please download from Web
and register it as a component
for the simulator.



Improvement of
design perfection

Efficiency
improvement of
design

Device Parameter

S Parameter
Equivalent circuit model

例) Capacitor



C



- Chip multi-layer
ceramic capacitors
< MLCC's >
(270 parts numbers)

R



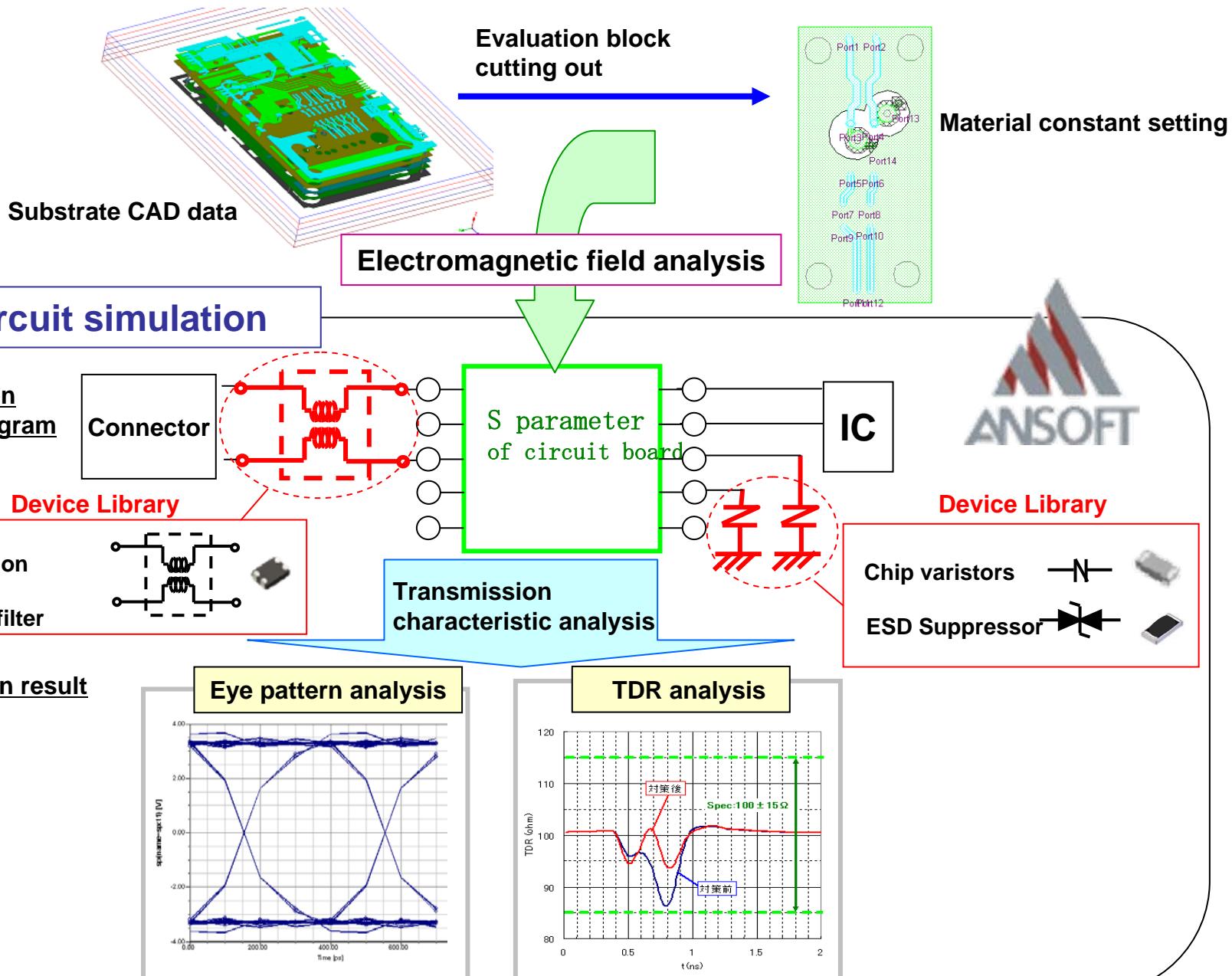
- Chip resistors
(186 parts numbers)

L

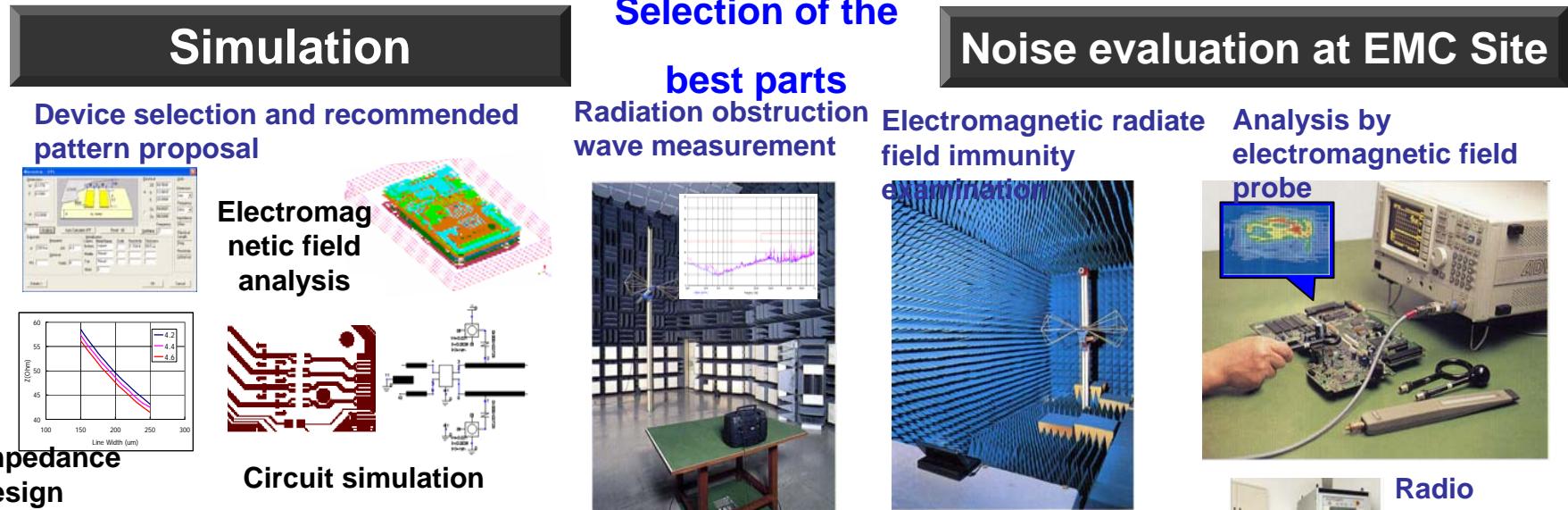
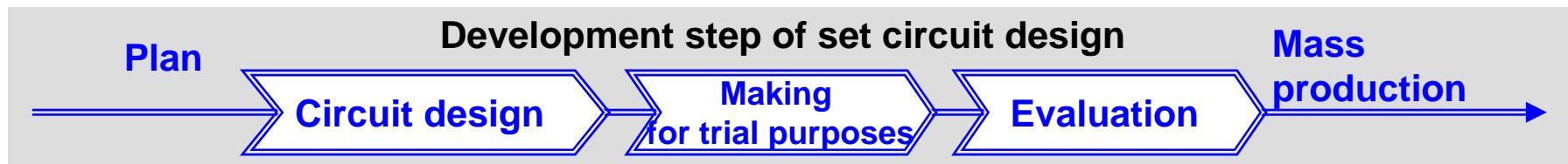


- Chip inductors
(45 parts
numbers)

Simulation analysis with substrate CAD data and Device Library



LCR solution activity flow



- ① Solution corresponding to design phase
- ② Total solution with a lot of devices



Demand of Remote IP Monitoring



I want to monitor my branch shops at any time, any place.
(Franchise retail store owner)



I want to install Security system, but expensive!
I need something at reasonable cost .



I want to check the present Condition or change for large facility.
(Parking lot, warehouse)



I want to check branches to make sure if everything is fine
(Branch, factory, school)



I want to make HP more attractive.
(Live monitoring)

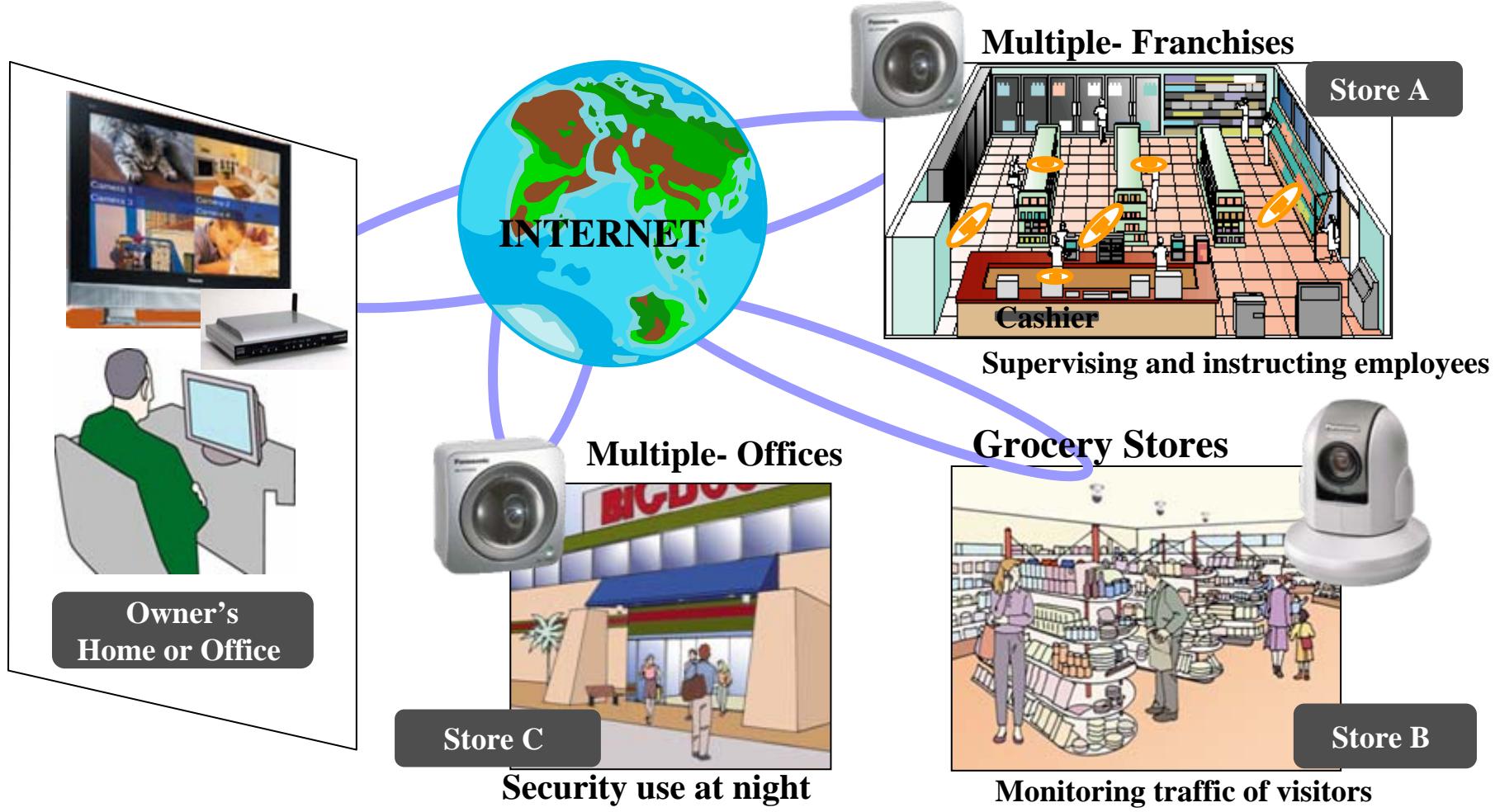


I want to improve customer satisfaction, energy saving
(Building, apartment, hotel)



Remote IP Monitoring corresponds to such needs!!

ZDM [Zero Distance Management]

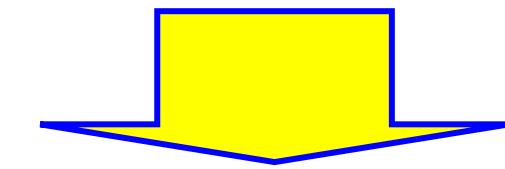


Improve the **Quality of Management** by IP Monitoring

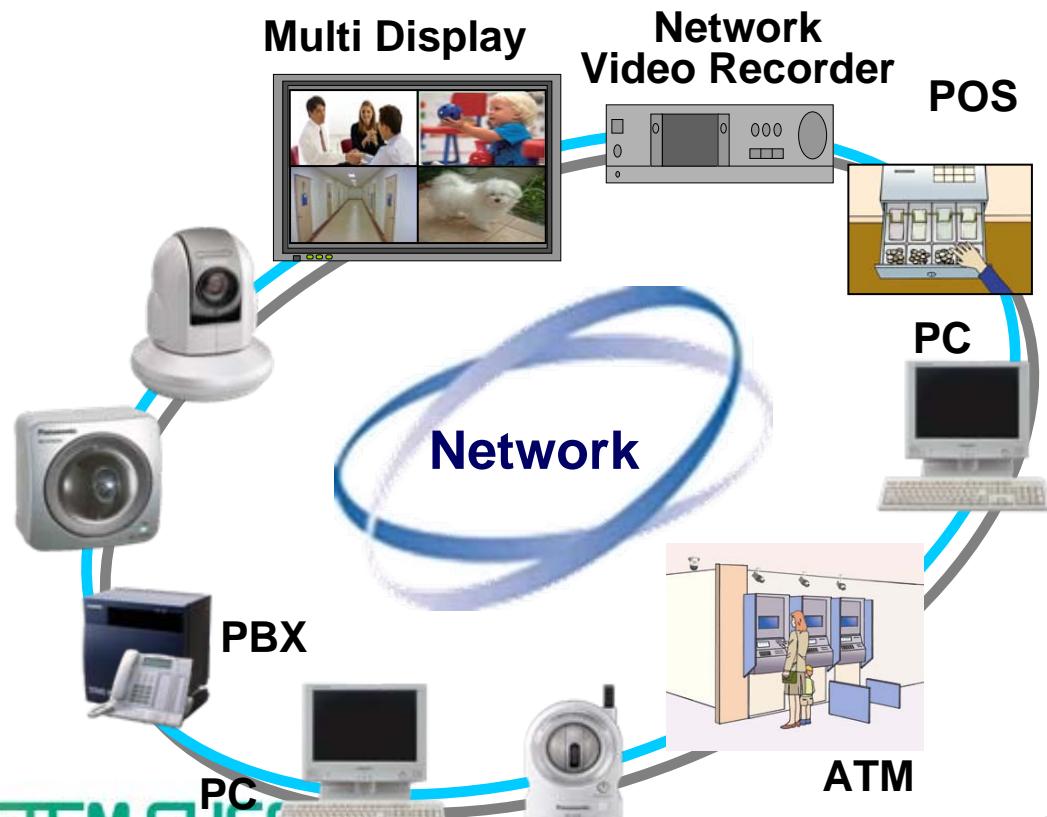


Remote IP Monitoring

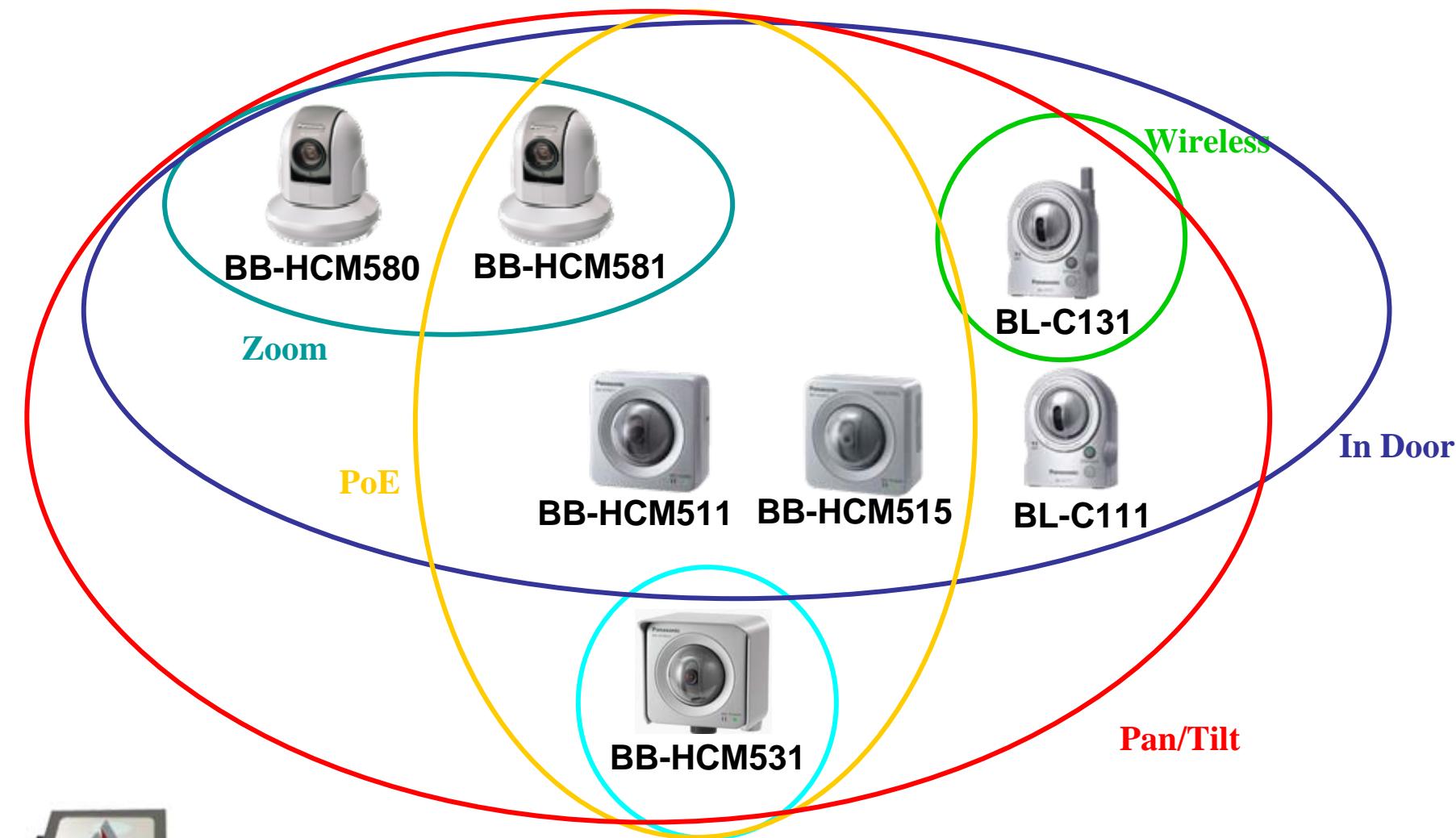
Provides effective solution and benefit
to improve the Quality of Management



- Distance Free!
- High Reliability!
- Save Time & Cost!
- Integration with
IP-PBX, POS, ATM!

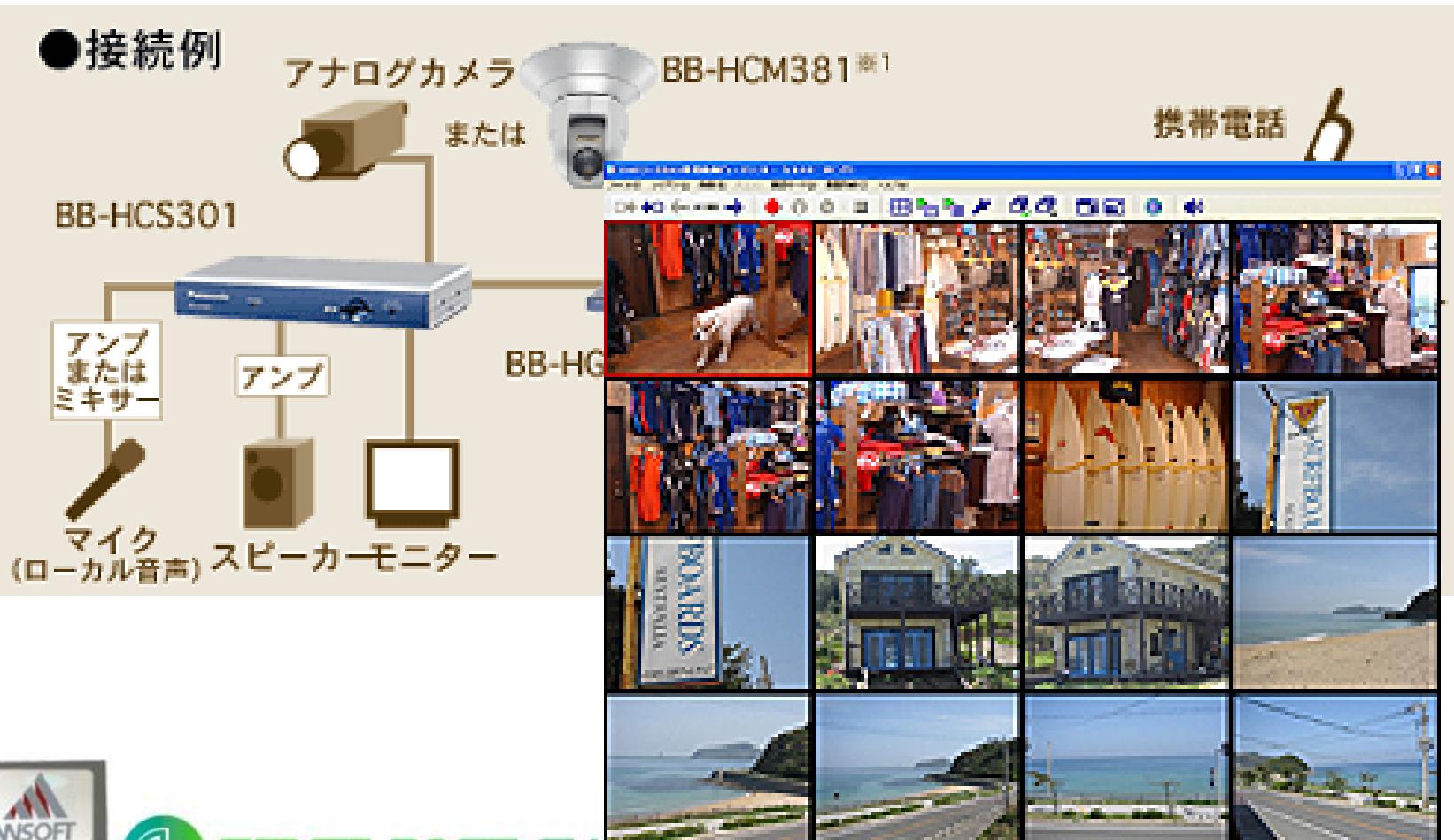


Line up



Introducing Project Definitions

●接続例



Motivation of the Project

- Panasonic to employ Computer Aided Engineering in their System.
 - Time to Market...Initial Design to Product
 - Robust Design
 - EYE Design
 - EMI/EMC Regulation
- Establish the methodology of designing products using Simulator instead of Cut and Try method.



Why not Cut and Try?

- If Engineer is lucky, then it will probably find solution quickly modifying the design.
- It all depends on the engineer's skill and experiences.
- Just making the prototype board takes 2 months and measurements takes additional one month.
- **CAE based approach** not just reveal all the problems but also gives path to **greatly enhance their products**.



Ansoft Tools ready to Solve their requirements?

- **LVDS system in Network Camera is huge.** No Electromagnetic Simulator can possibly solve the entire problem.
- Ansoft has suggested to use **combination of Electromagnetic Simulator** as well as **Circuit Simulator** to find the causes of their prototype board.
- **PED/PCC/Molex Japan** together with **Ansoft** formed project to establish design flow using prototype boards. (System and Boards which failed.)
- **Knowledge and experiences** obtained through this project has been implemented in **Panasonic** for future products.



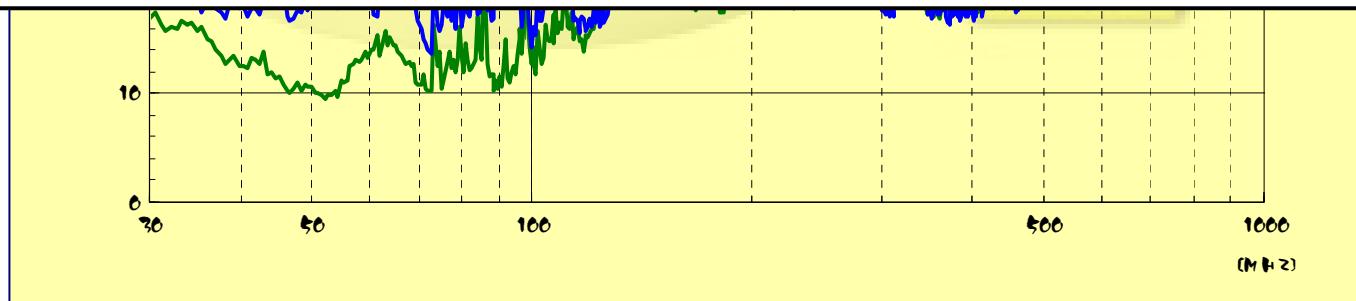
Project Scope



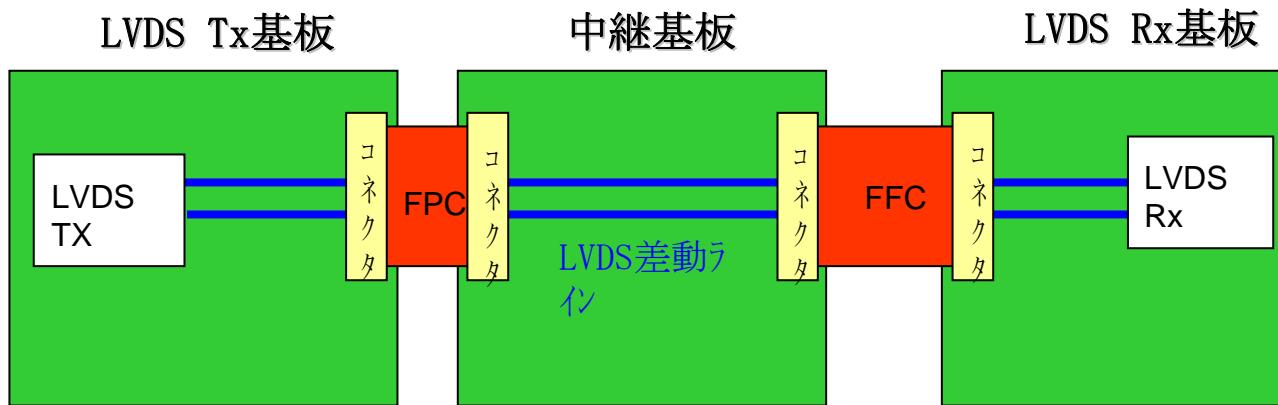
Maintain Quality Eye Diagram

Suppress Radiation Noise (EMI/EMC)

Implication is to reduce Common Mode Degeneration
through out the system



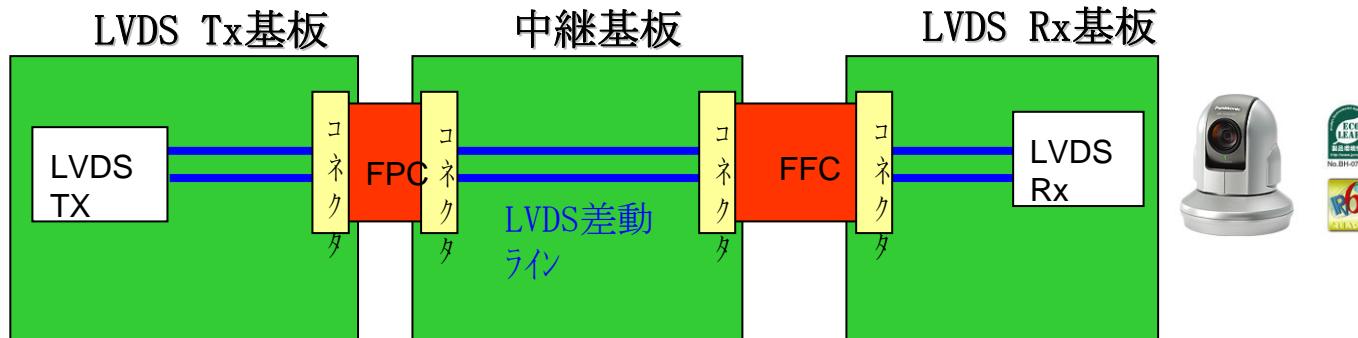
Approaches to the Project



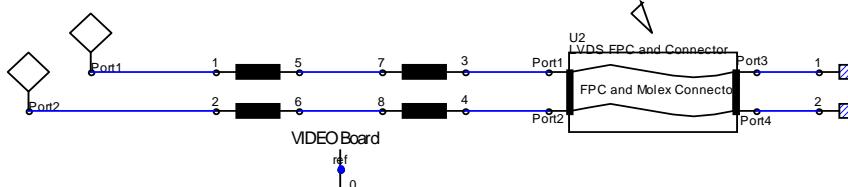
**Duplicate the System in Simulation
Match to the Measurements if available**

Use the system to improve the performance



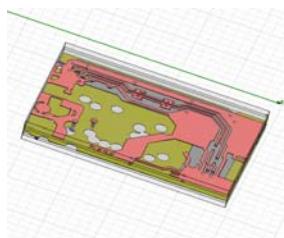


FPC and Molex Connectors

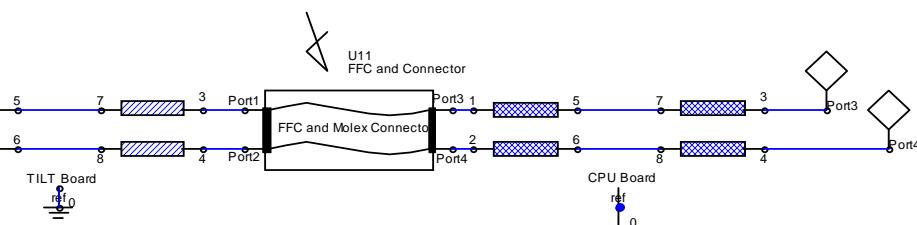


LVDS Tx基板

SiWave Analysis HFSS Analysis



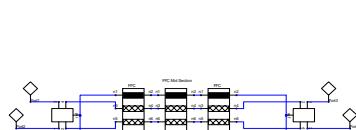
FFC and Molex Connectors



LVDS Rx基板

PlanarEM

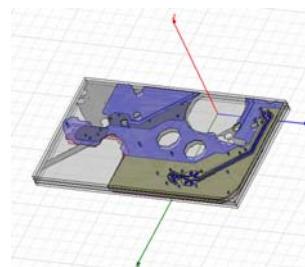
Nexxim W-element



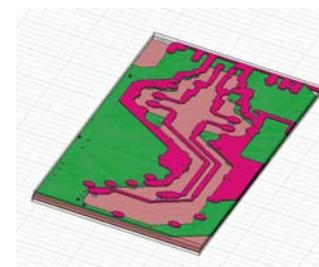
中継基板

SiWave Analysis

HFSS Analysis



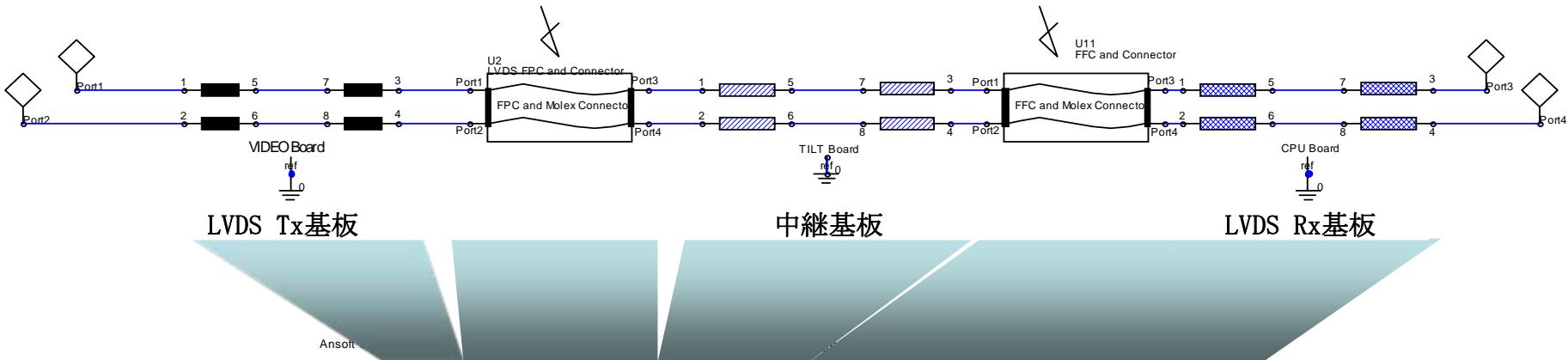
SiWave Analysis
HFSS Analysis

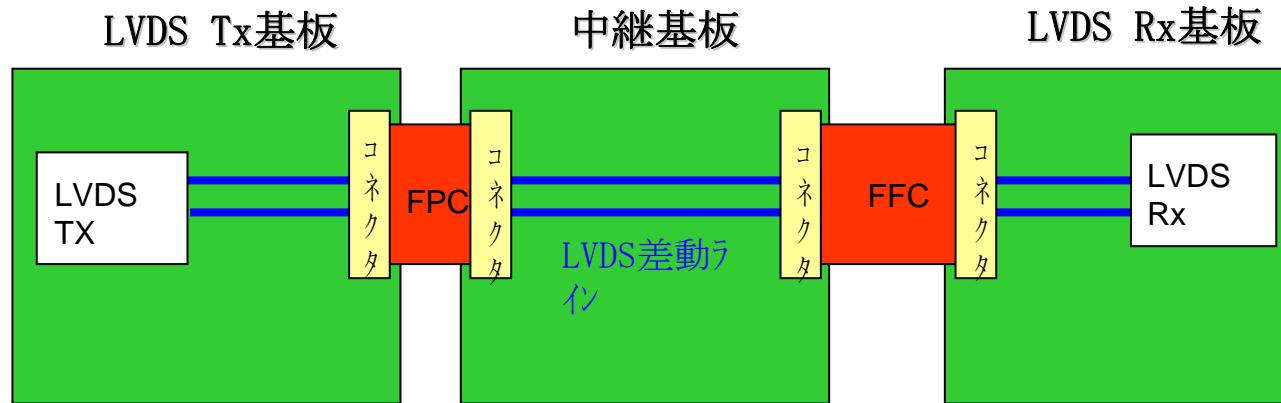


Examine Total System

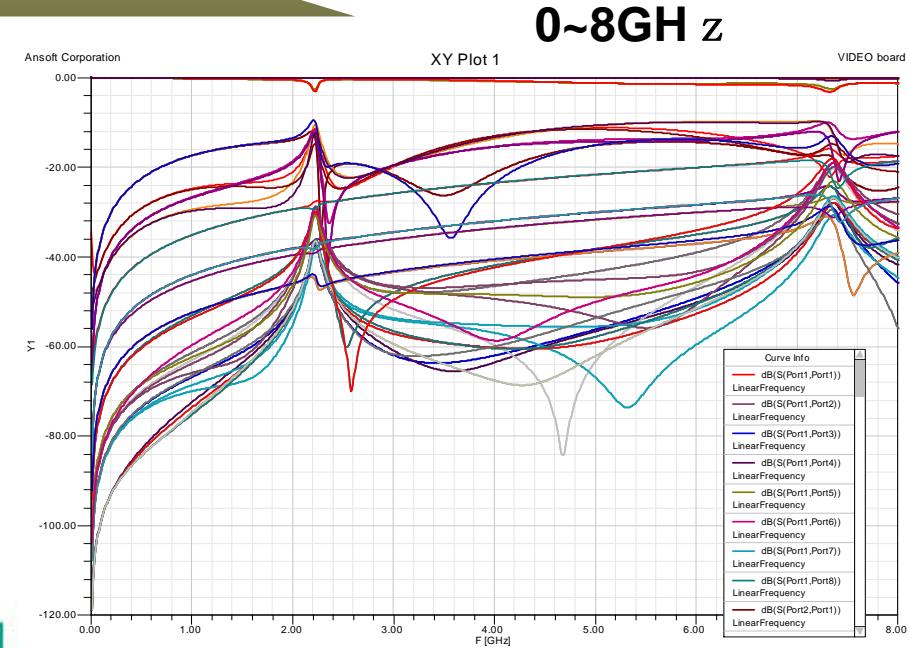
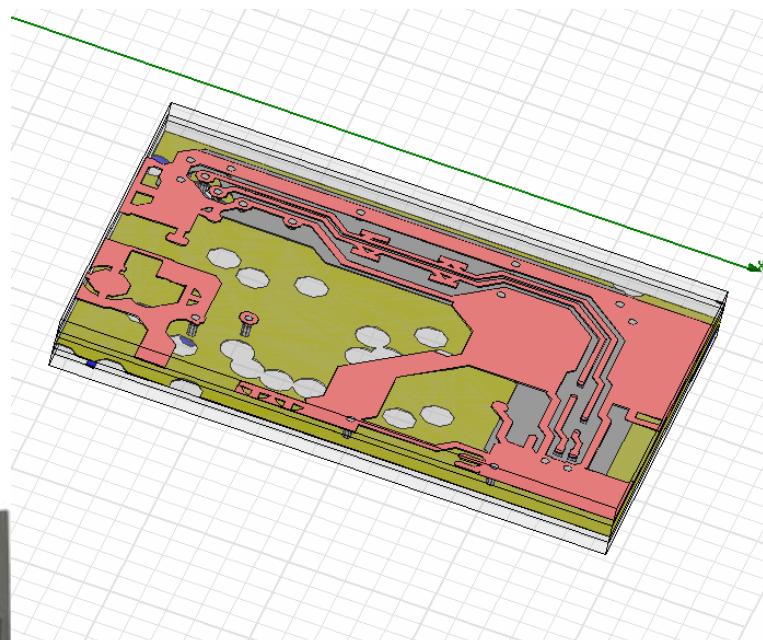
FPC and Molex Connectors

FFC and Molex Connectors





LVDX Tx Board

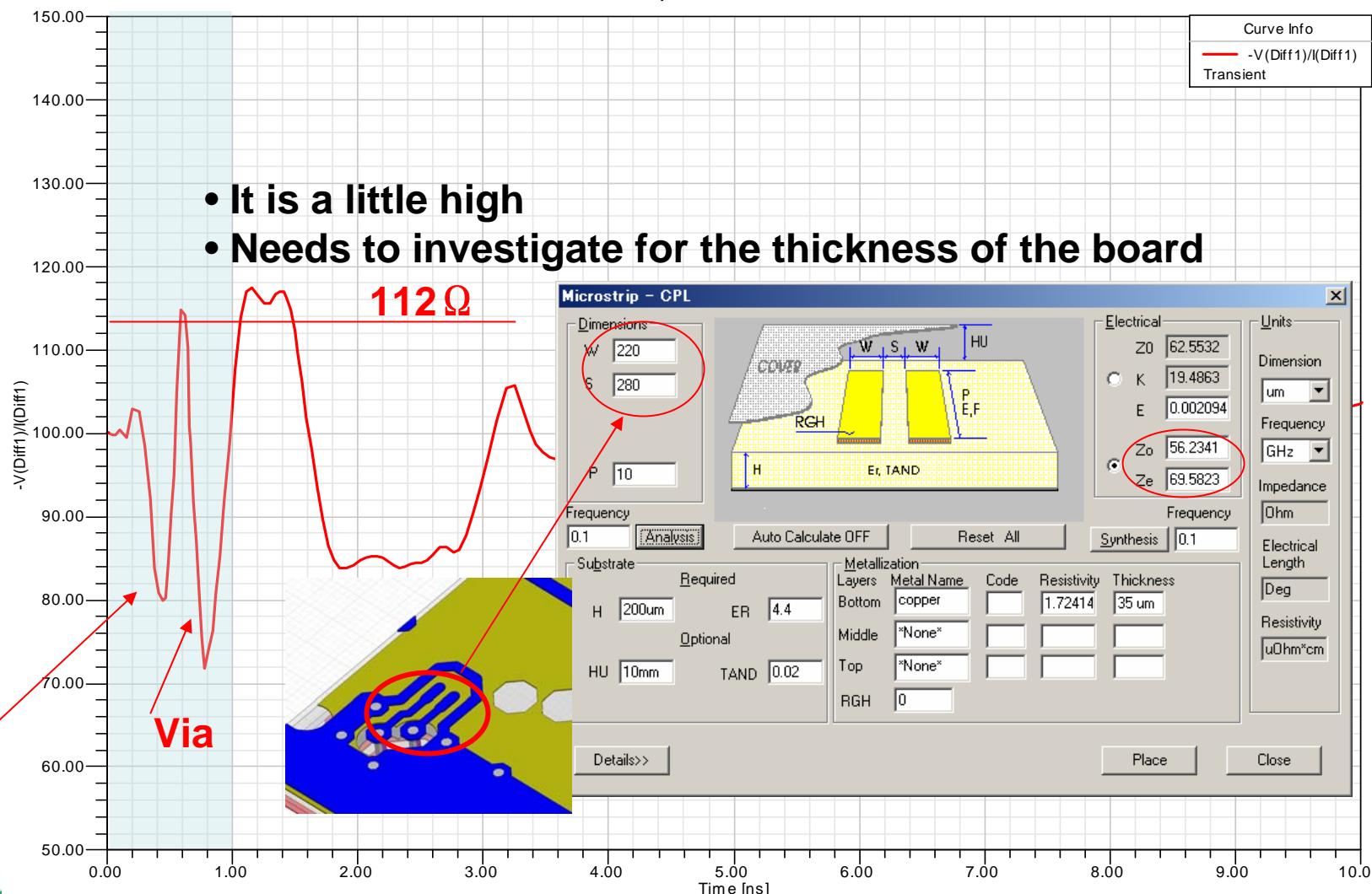


Video Board Time Domain

Ansoft Corporation

Impedance

LVDS TDR folded



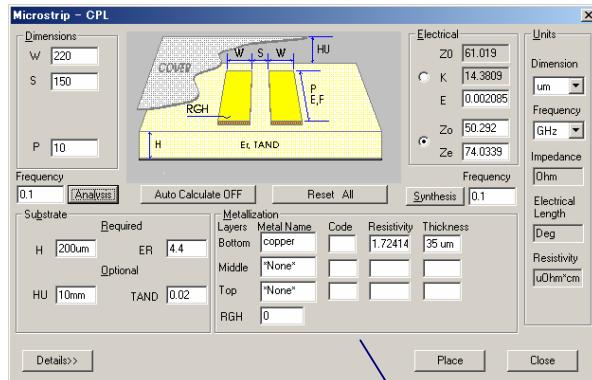
Pad

Via



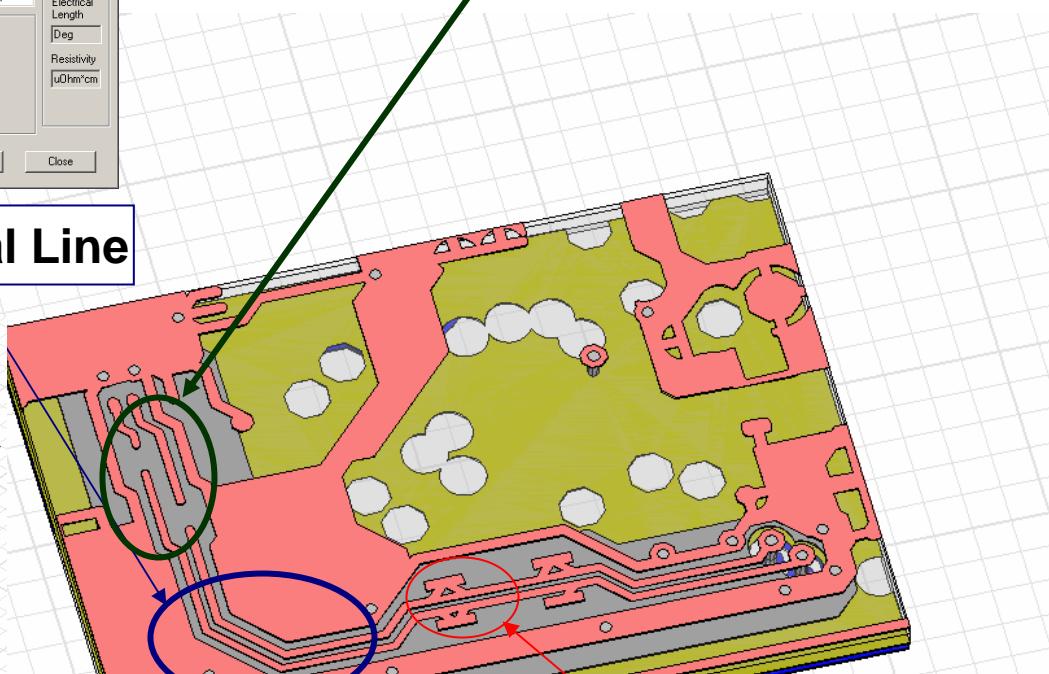
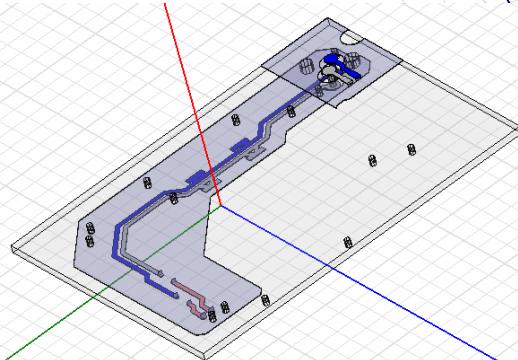
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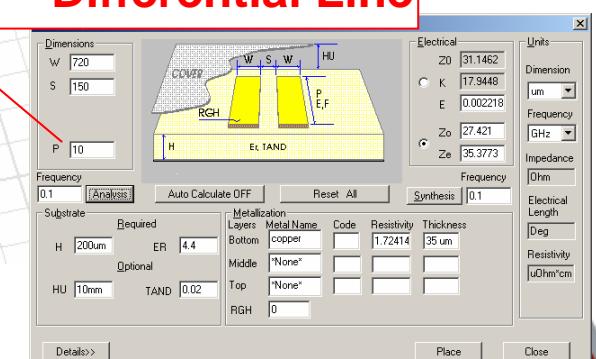
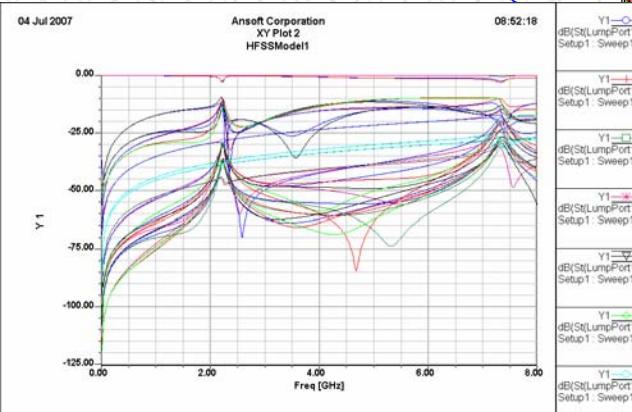


May cause Skew if the components are placed.

~100Ω Differential Line



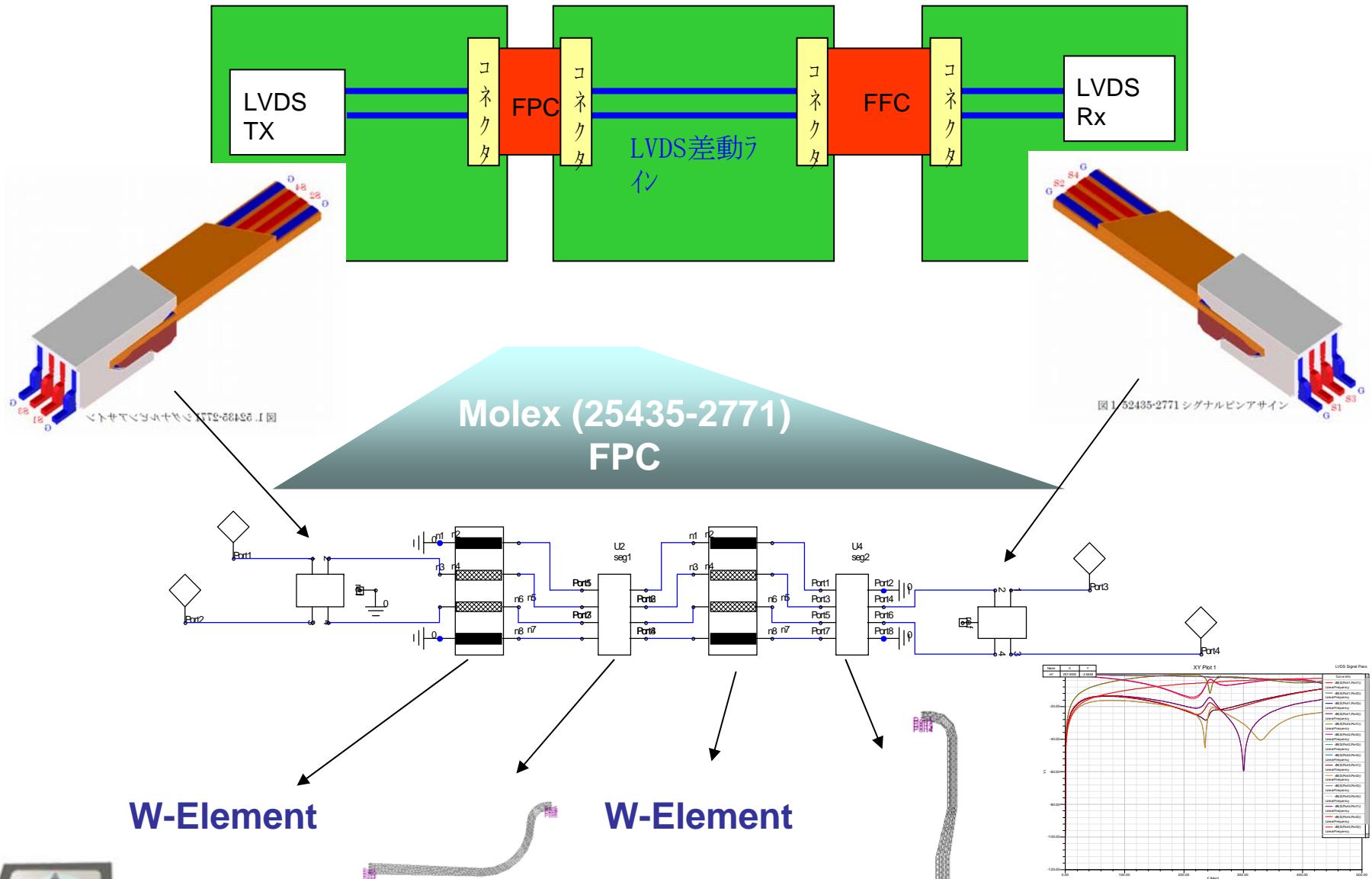
~54Ω Differential Line



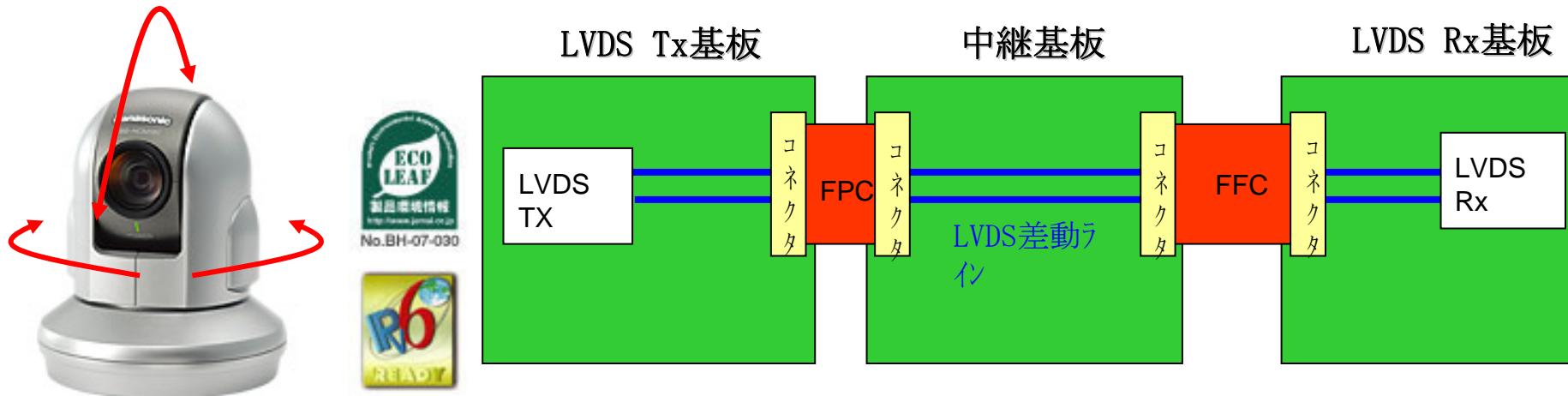
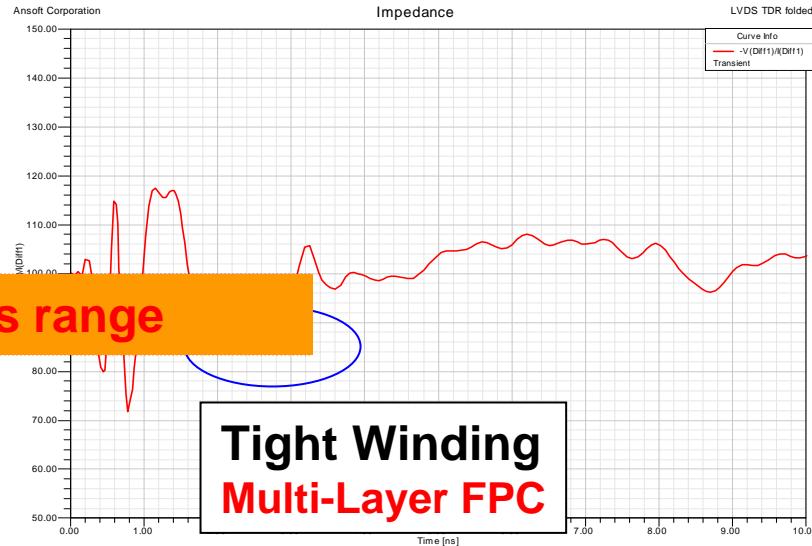
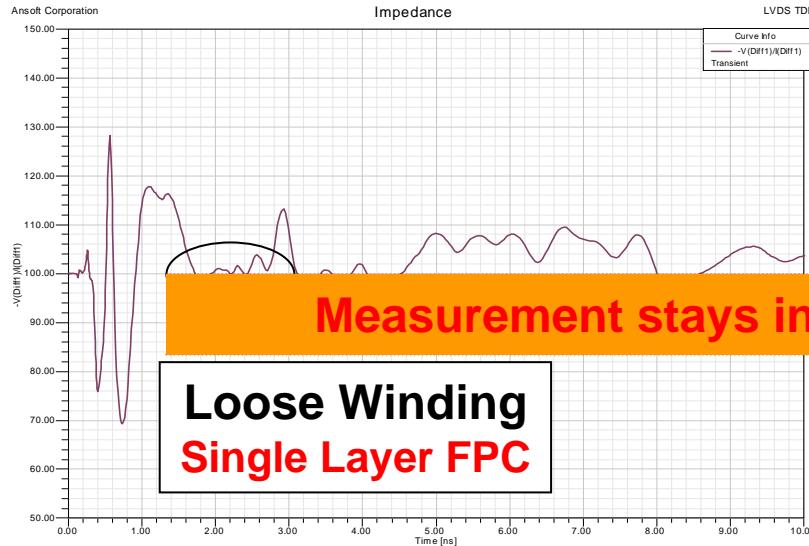
LVDS Tx基板

中継基板

LVDS Rx基板



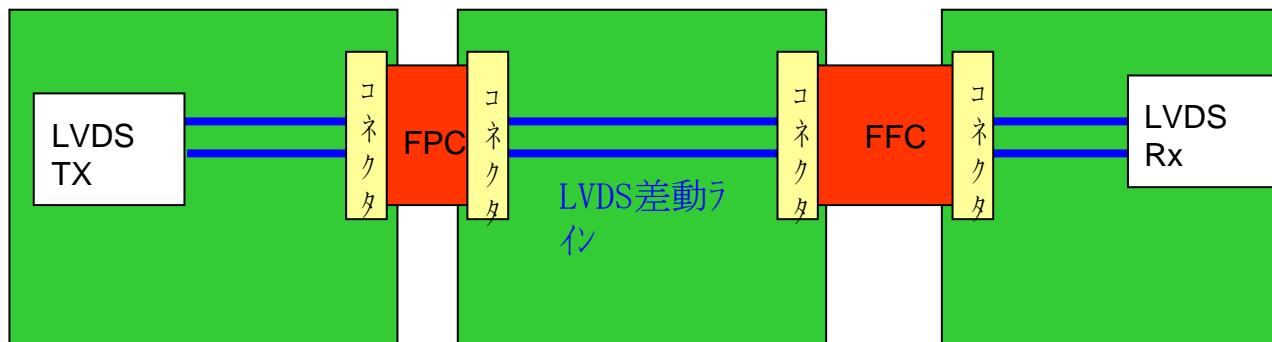
FPC Straight and Bobbin effect



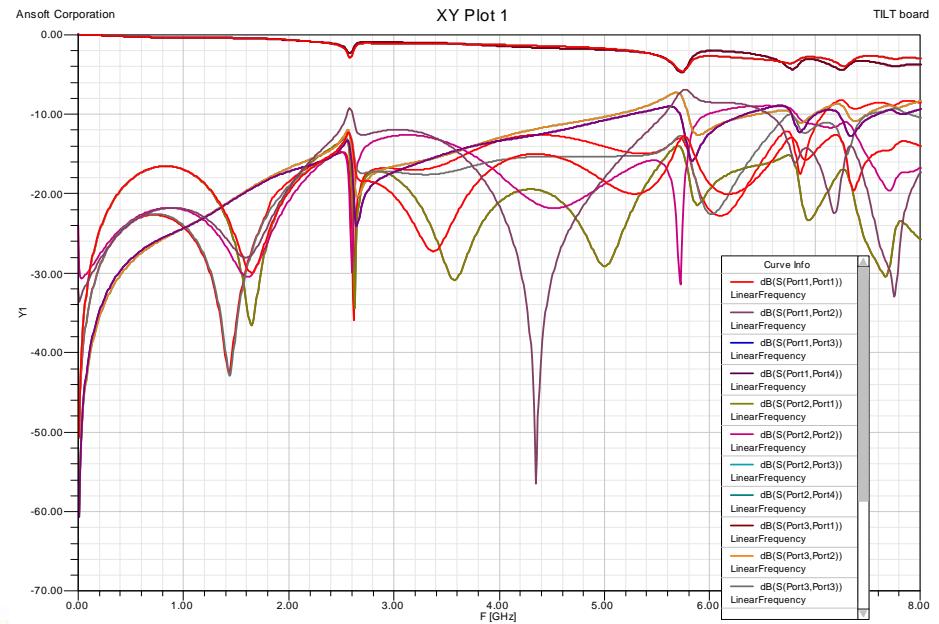
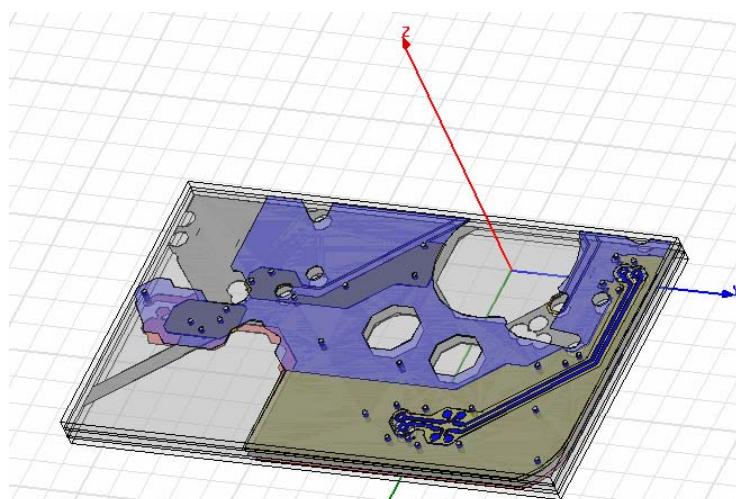
LVDS Tx基板

中継基板

LVDS Rx基板

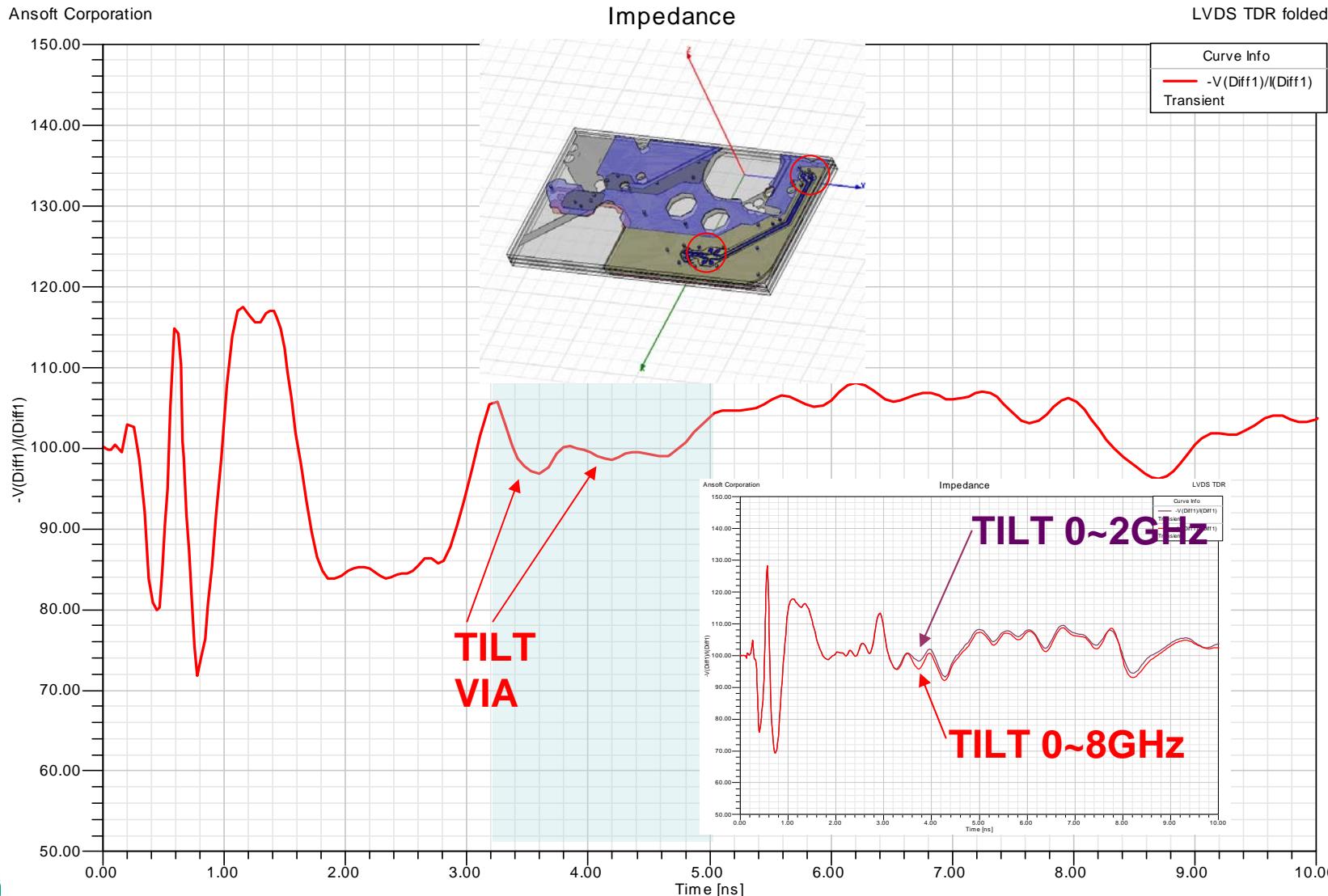


TILT Board



TILT Board Time Domain

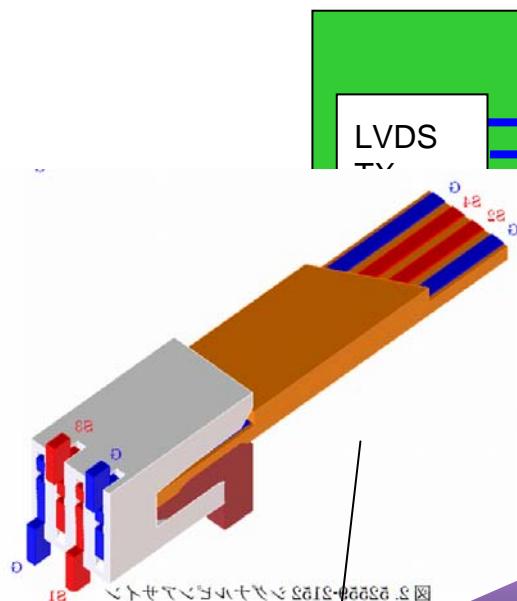
Ansoft Corporation



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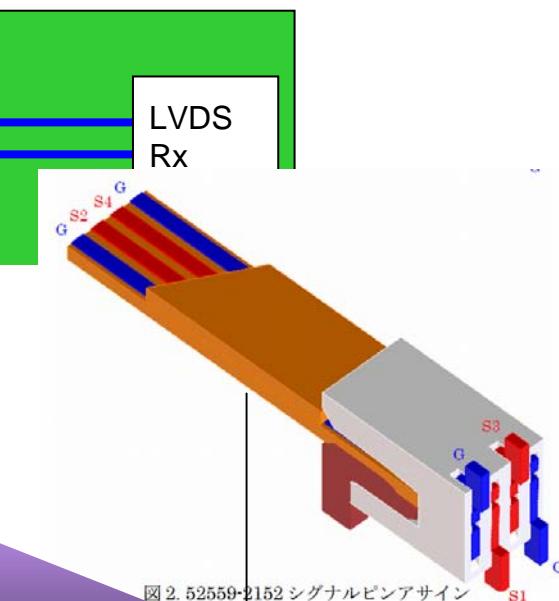
LVDS Tx基板



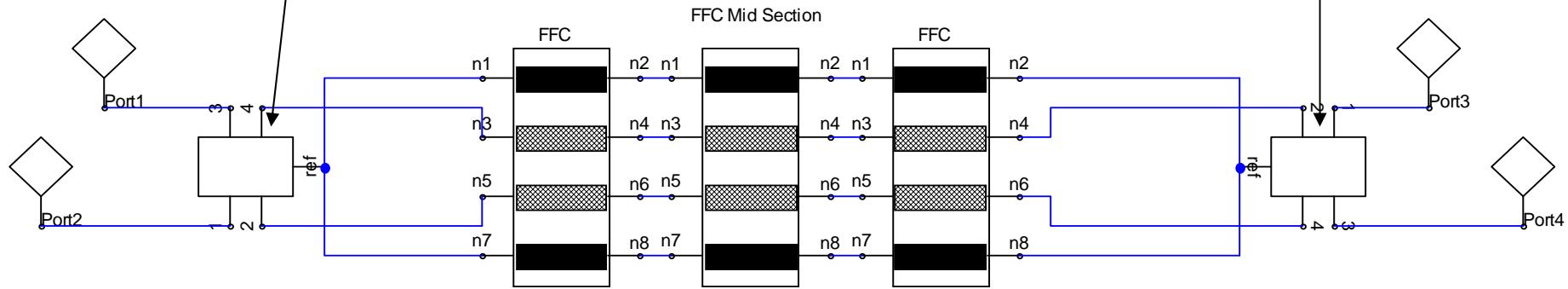
中継基板



LVDS Rx基板



Molex Connector FFC

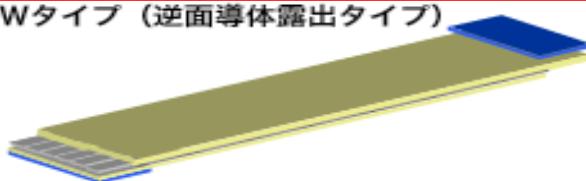


FFC Geometry

Sタイプ (同一面導体露出タイプ)



Wタイプ (逆面導体露出タイプ)



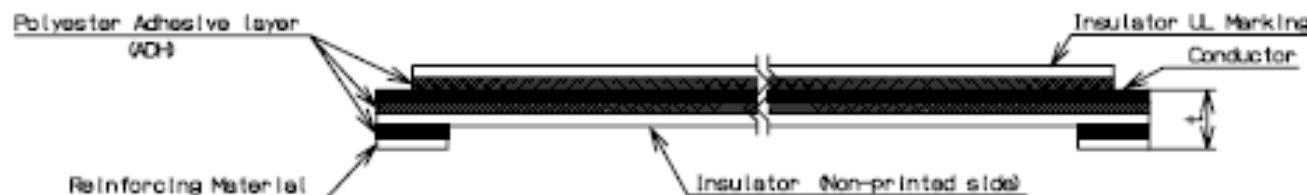
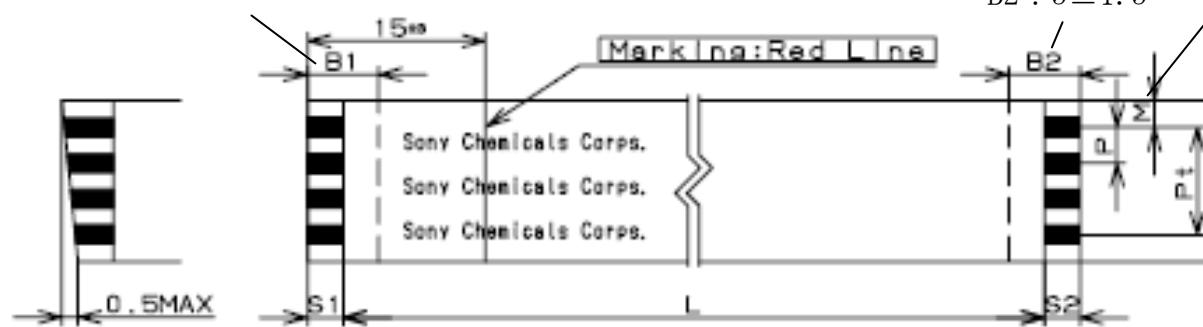
Bタイプ (片側導体露出タイプ)



Dタイプ (両側導体露出タイプ)

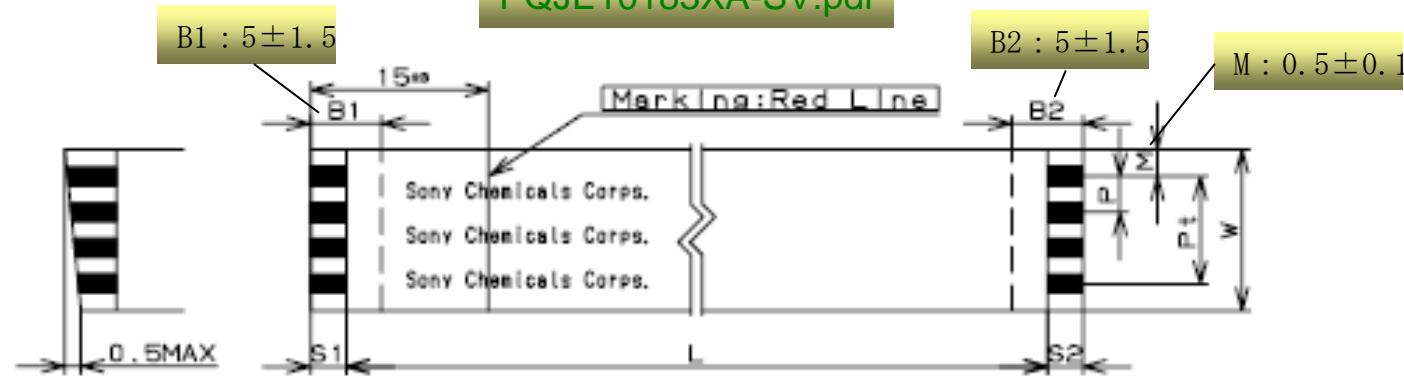


B1 : 5 ± 1.5



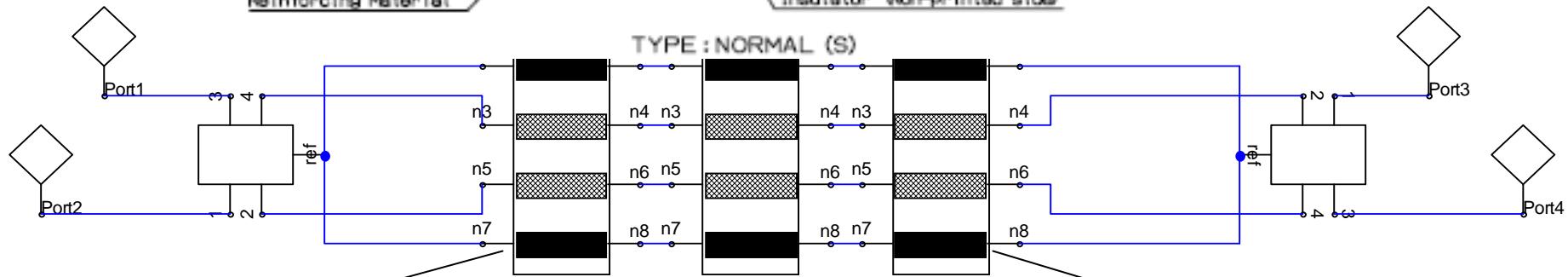
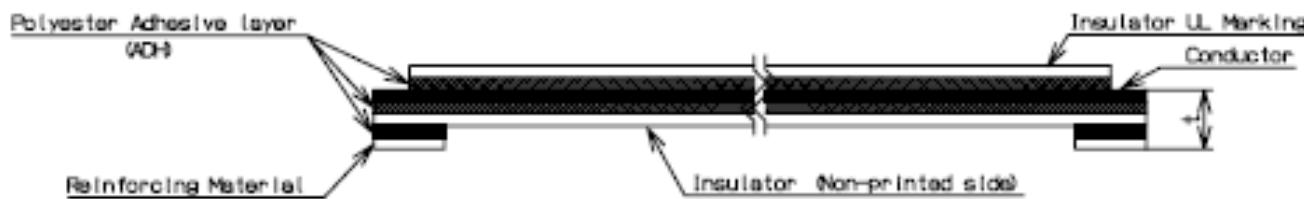
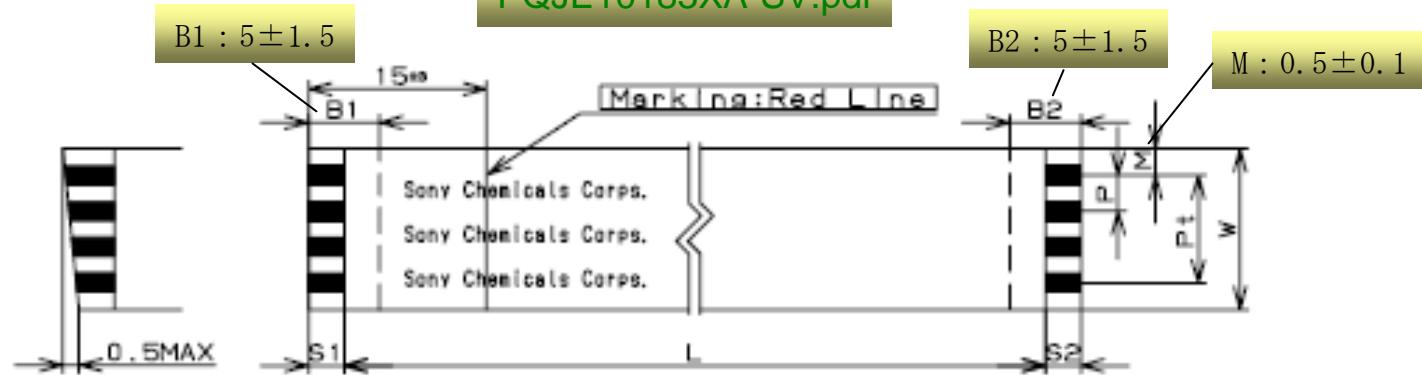
TYPE : NORMAL (S)





```
.layerstack FFC_edge
+ layer = (air, 'Air_thickness'),
+ layer = (Base, 188um),
+ layer = (Polyester, 40um),
+ layer = (Base, 'FFC_Base_Thickness'),
+ layer = (Polyester, 'FFC_polyester_Thickness+FFC_Conductor_Thickness'),
+ layer = (air, 'Air_thickness+FFC_polyester_Thickness+FFC_Base_Thickness')
```

```
.layerstack FFC_mid
+ layer = (air, 'Air_thickness+188um+40um'),
+ layer = (Base, 'FFC_Base_Thickness'),
+ layer = (Polyester, '2*FFC_polyester_Thickness+FFC_Conductor_Thickness'),
+ layer = (Base, 'FFC_Base_Thickness'),
+ layer = (air, 'Air_thickness')
```



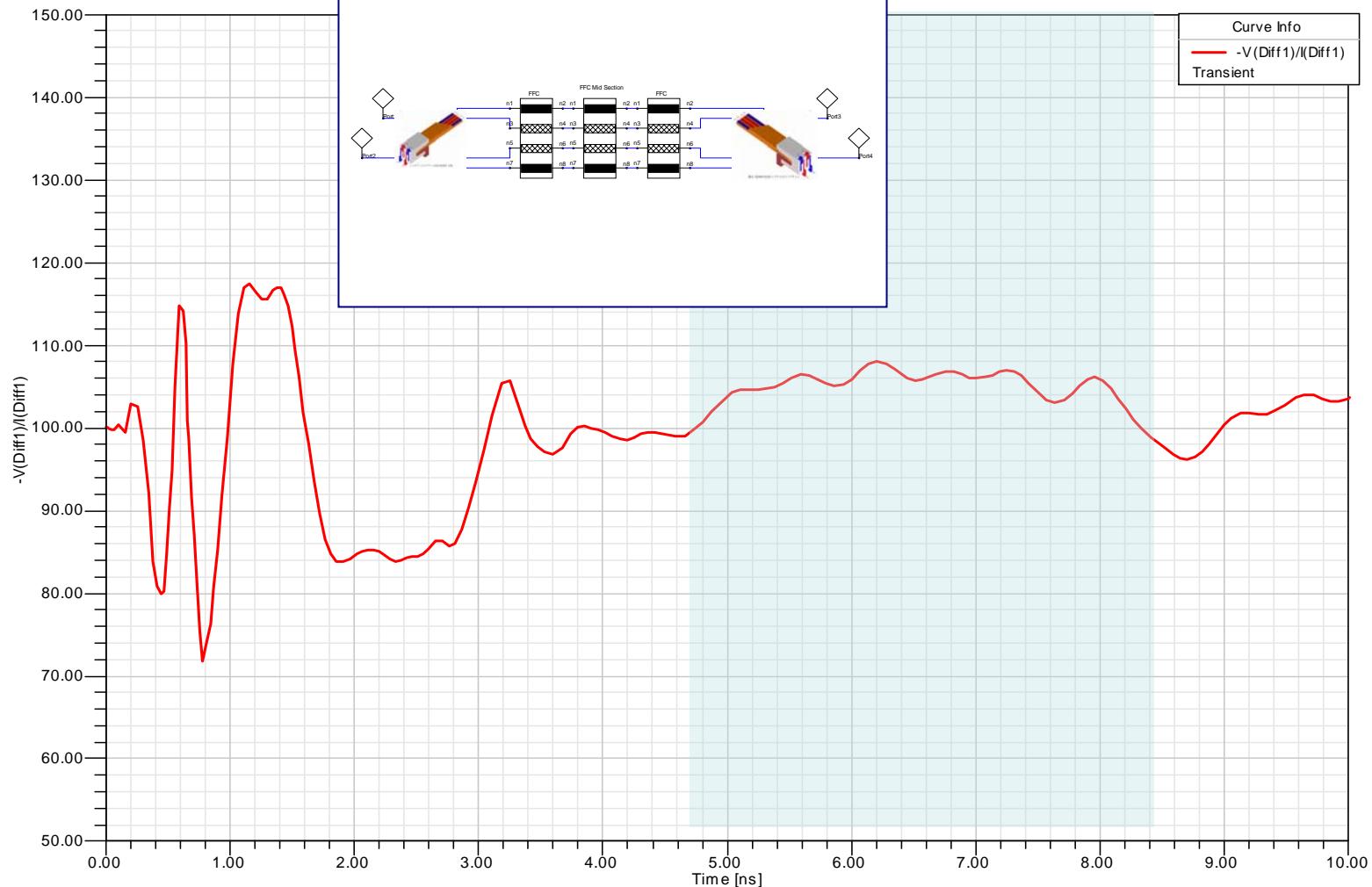
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ModelName	x				-	<input type="checkbox"/>
P	5	mm	5mm		-	<input checked="" type="checkbox"/>
W1	300	um	300um		-	<input type="checkbox"/>
W2	300	um	300um		-	<input type="checkbox"/>
W3	300	um	300um		-	<input type="checkbox"/>
W4	300	um	300um		-	<input type="checkbox"/>
S12	200	um	200um		-	<input type="checkbox"/>
S23	200	um	200um		-	<input type="checkbox"/>
S34	200	um	200um		-	<input type="checkbox"/>

Name	Value	Unit	Evaluated Value	Description	Callback	Override
ModelName	x				-	<input type="checkbox"/>
P	5	mm	5mm		-	<input checked="" type="checkbox"/>
W1	300	um	300um		-	<input type="checkbox"/>
W2	300	um	300um		-	<input type="checkbox"/>
W3	300	um	300um		-	<input type="checkbox"/>
W4	300	um	300um		-	<input type="checkbox"/>
S12	200	um	200um		-	<input type="checkbox"/>
S23	200	um	200um		-	<input type="checkbox"/>
S34	200	um	200um		-	<input type="checkbox"/>

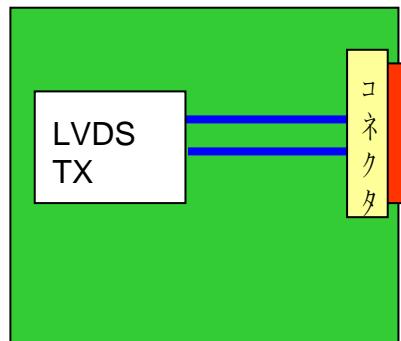
Name	Value	Unit	Evaluated Value	Description	Callback	Override
ModelName	x				-	<input type="checkbox"/>
P	319	mm	319mm		-	<input checked="" type="checkbox"/>
W1	300	um	300um		-	<input checked="" type="checkbox"/>
W2	300	um	300um		-	<input checked="" type="checkbox"/>
W3	300	um	300um		-	<input checked="" type="checkbox"/>
W4	300	um	300um		-	<input checked="" type="checkbox"/>
S12	200	um	200um		-	<input type="checkbox"/>

FFC Time Domain

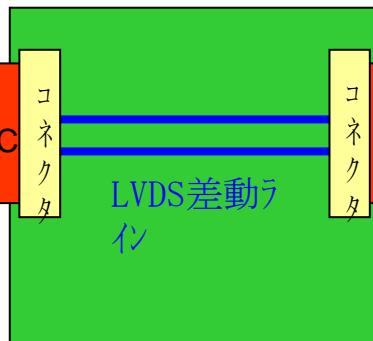
Ansoft Corporation



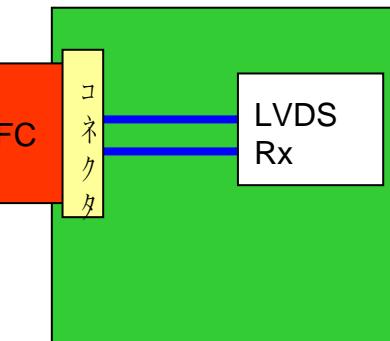
LVDS Tx基板



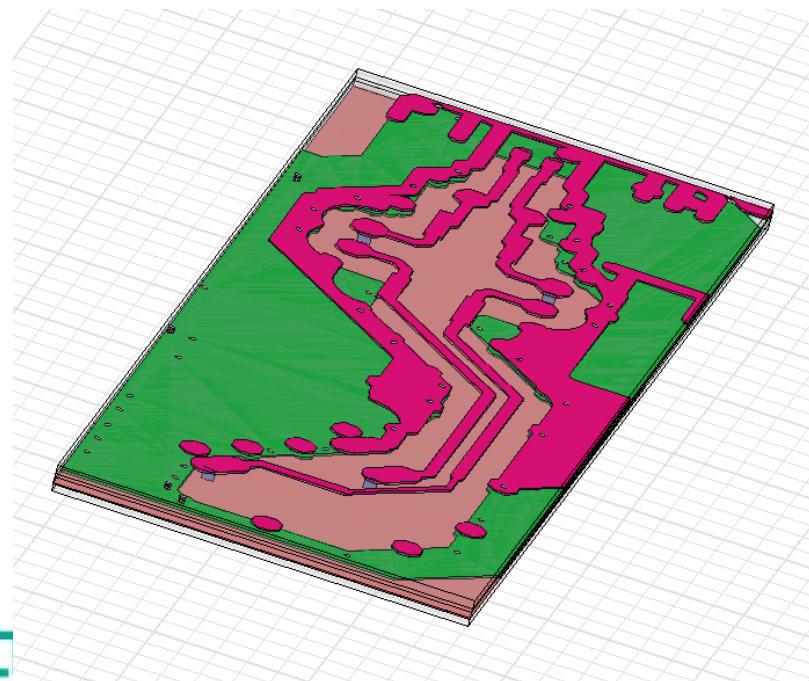
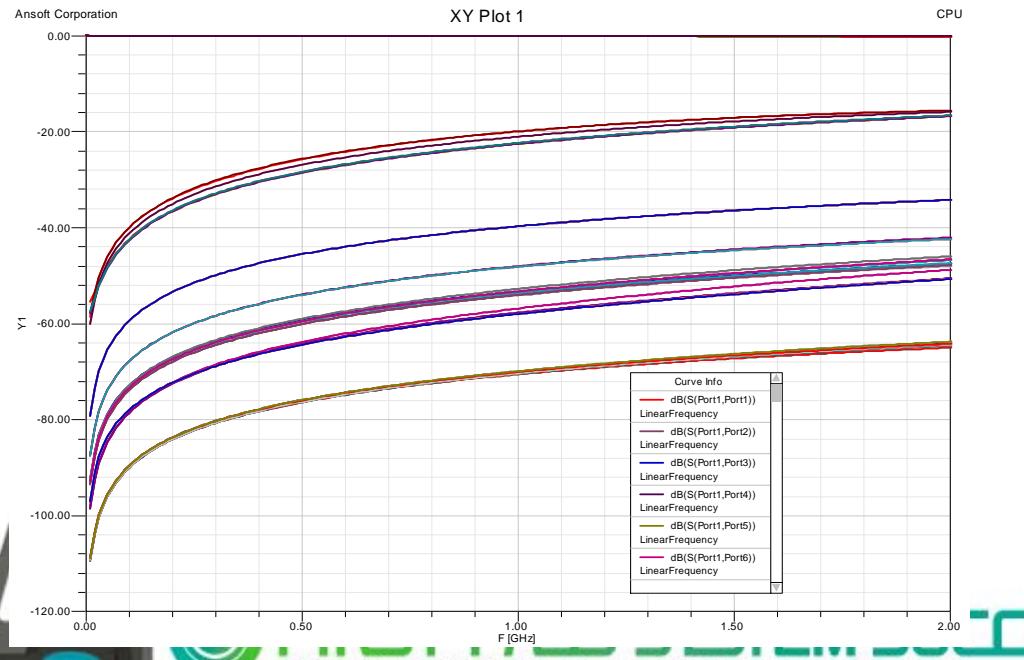
中継基板



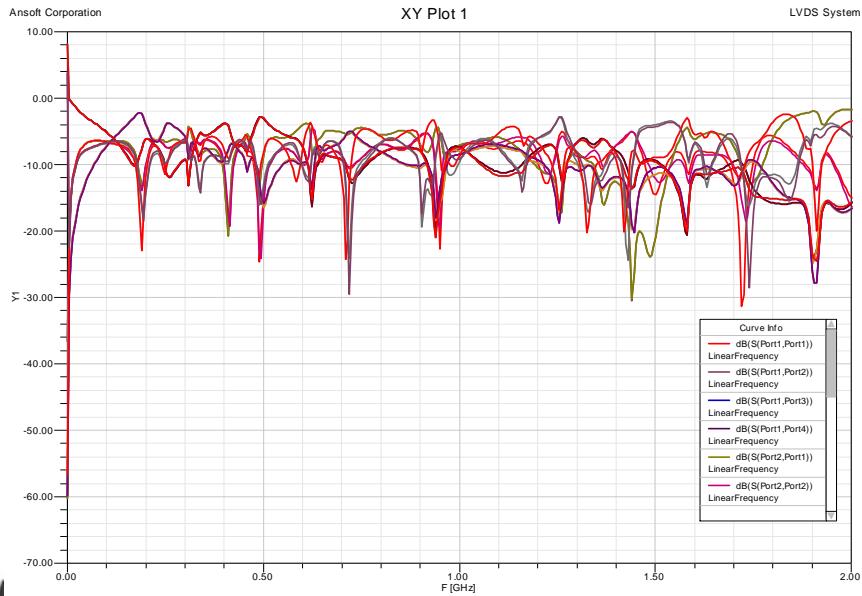
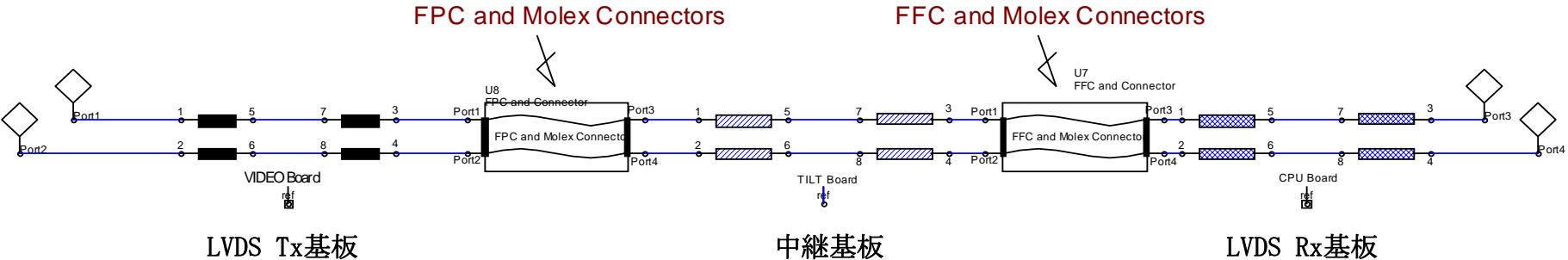
LVDS Rx基板



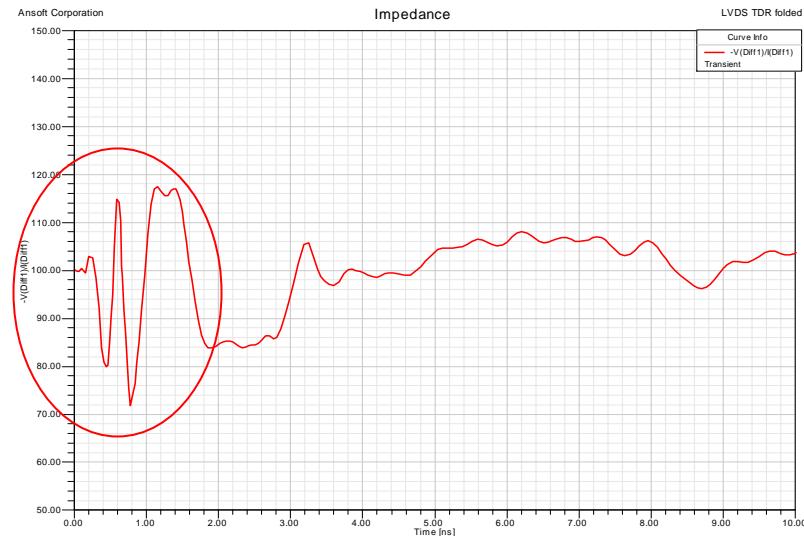
LVDS Rx Board



LVDS System Review



Improving the Design

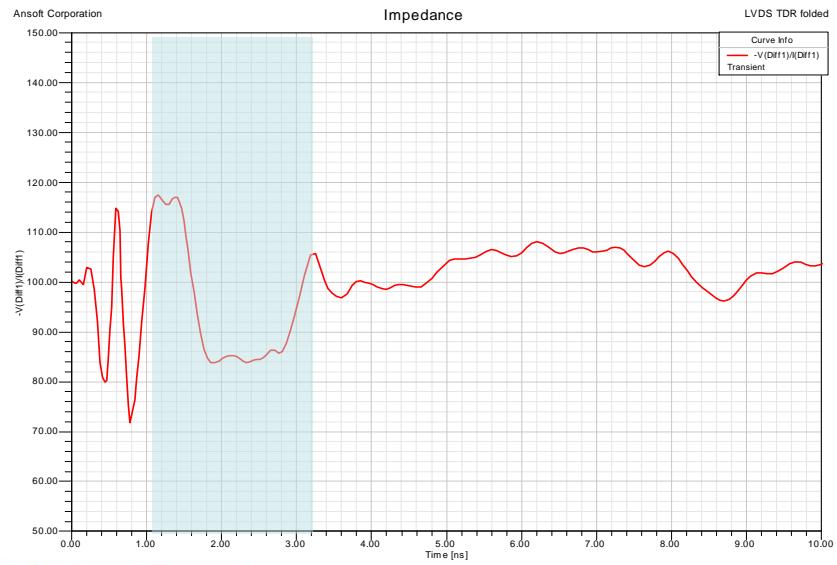
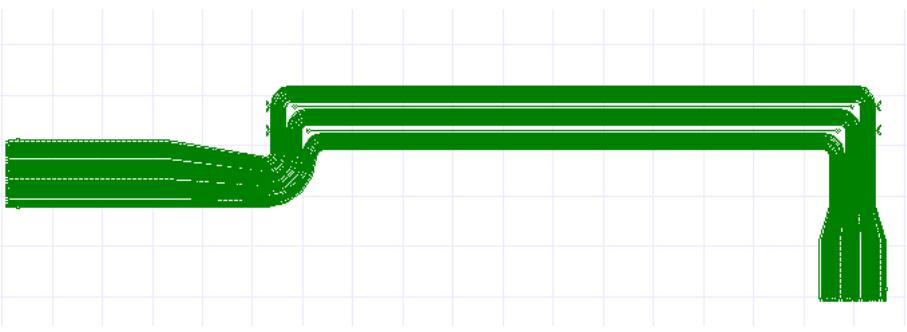


- VIDEO Board
- FPC Board

- Impedance matching through the FPC line
- Impedance matching and Skew Reduction in VIDEO Board
 - EYE Design and Reduction of Differential to Common Mode



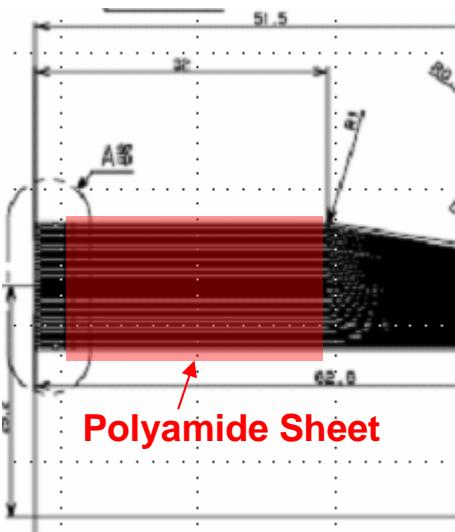
Improving Impedance Matching on the FPC



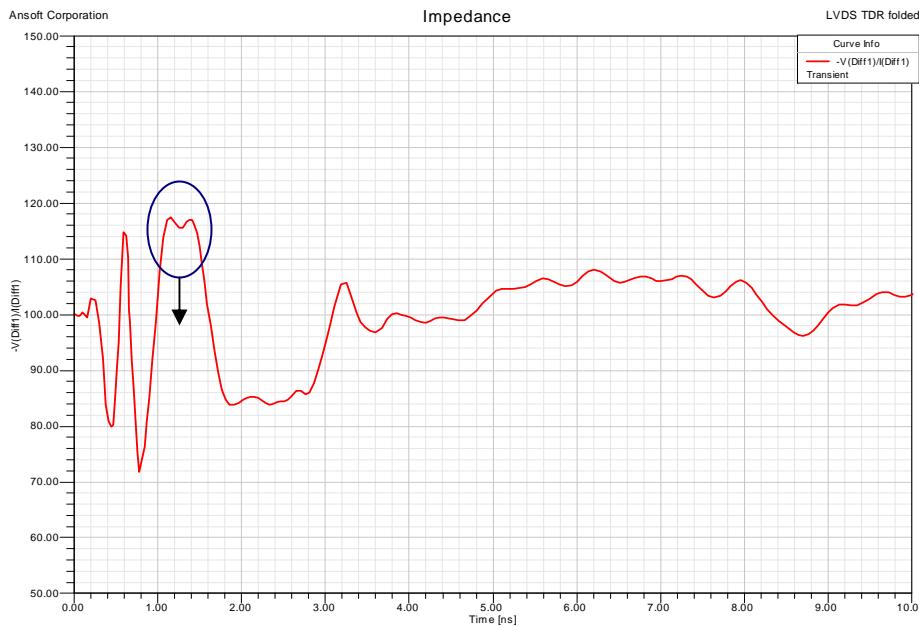
 **FIRST-PASS SYSTEM SUCCESS**
APPLICATION WORKSHOPS FOR HIGH-PERFORMANCE ELECTRONIC DESIGN



Matching FPC



By placing Polyamide sheet on top of FPC, Electromagnetic fields are captured in the vicinity of the Differential Line. Hence the Capacitor increase resulting to decrease the Characteristic impedances

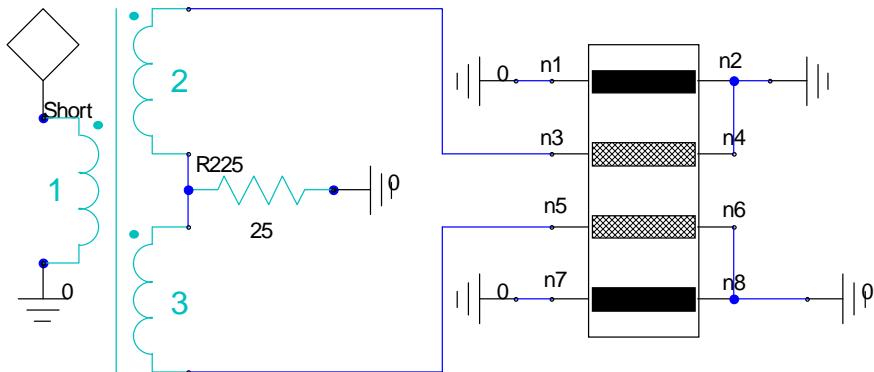
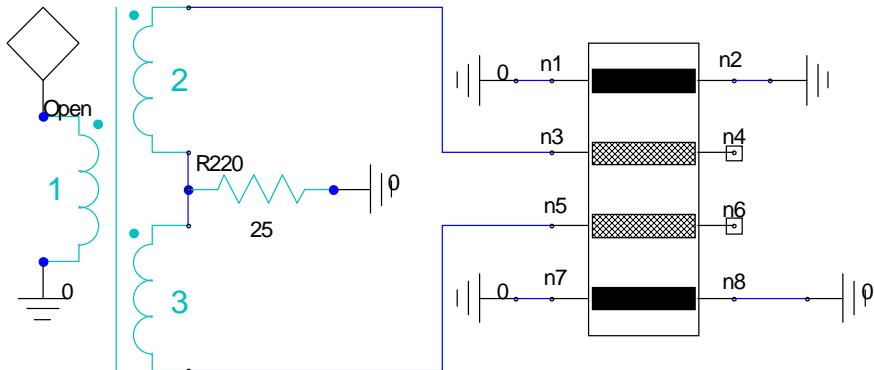


Finding Characteristic Impedance in Frequency Domain

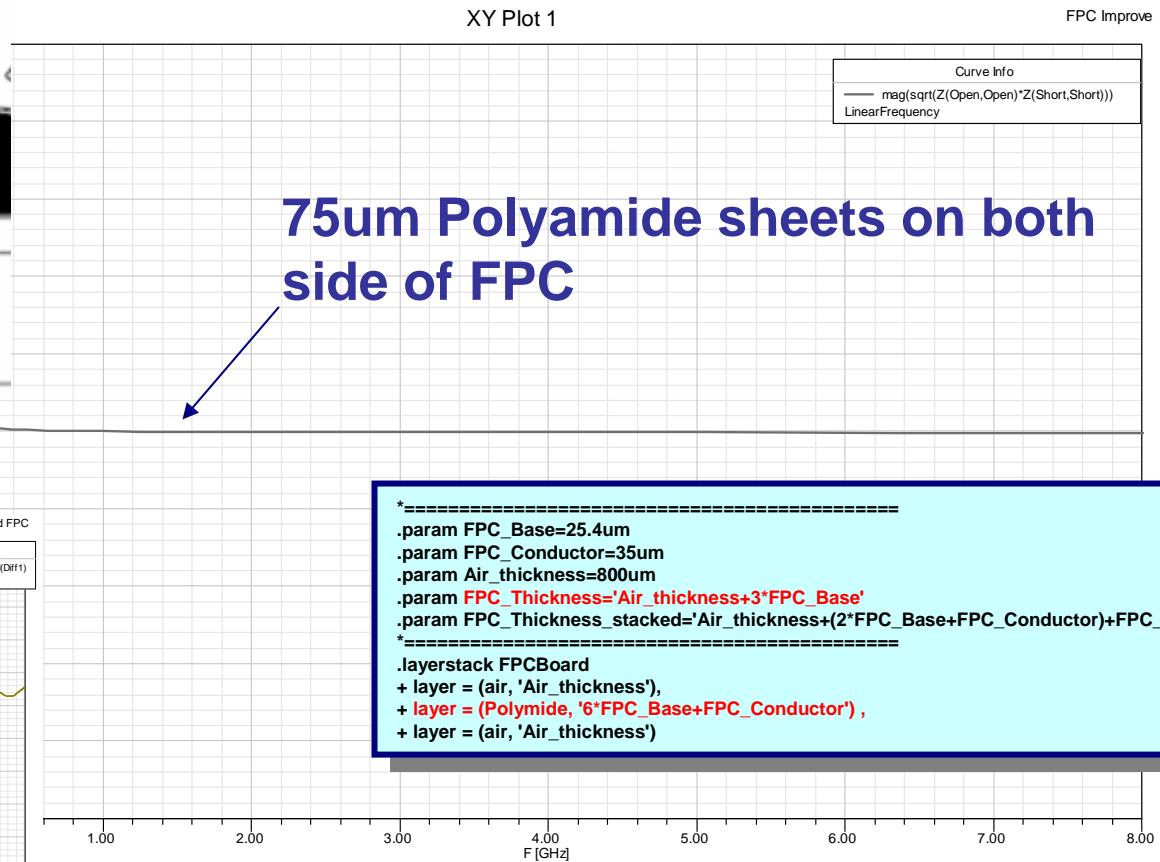
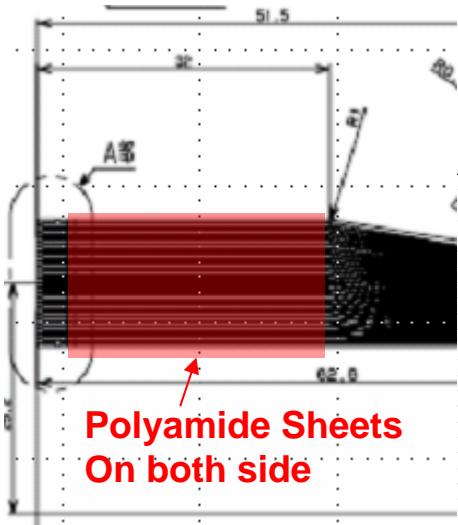
Use *Open/Short conditions* to compute the Impedances

By selecting the transformer ratio, differential impedance can be computed by

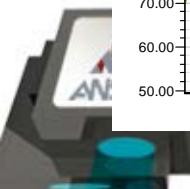
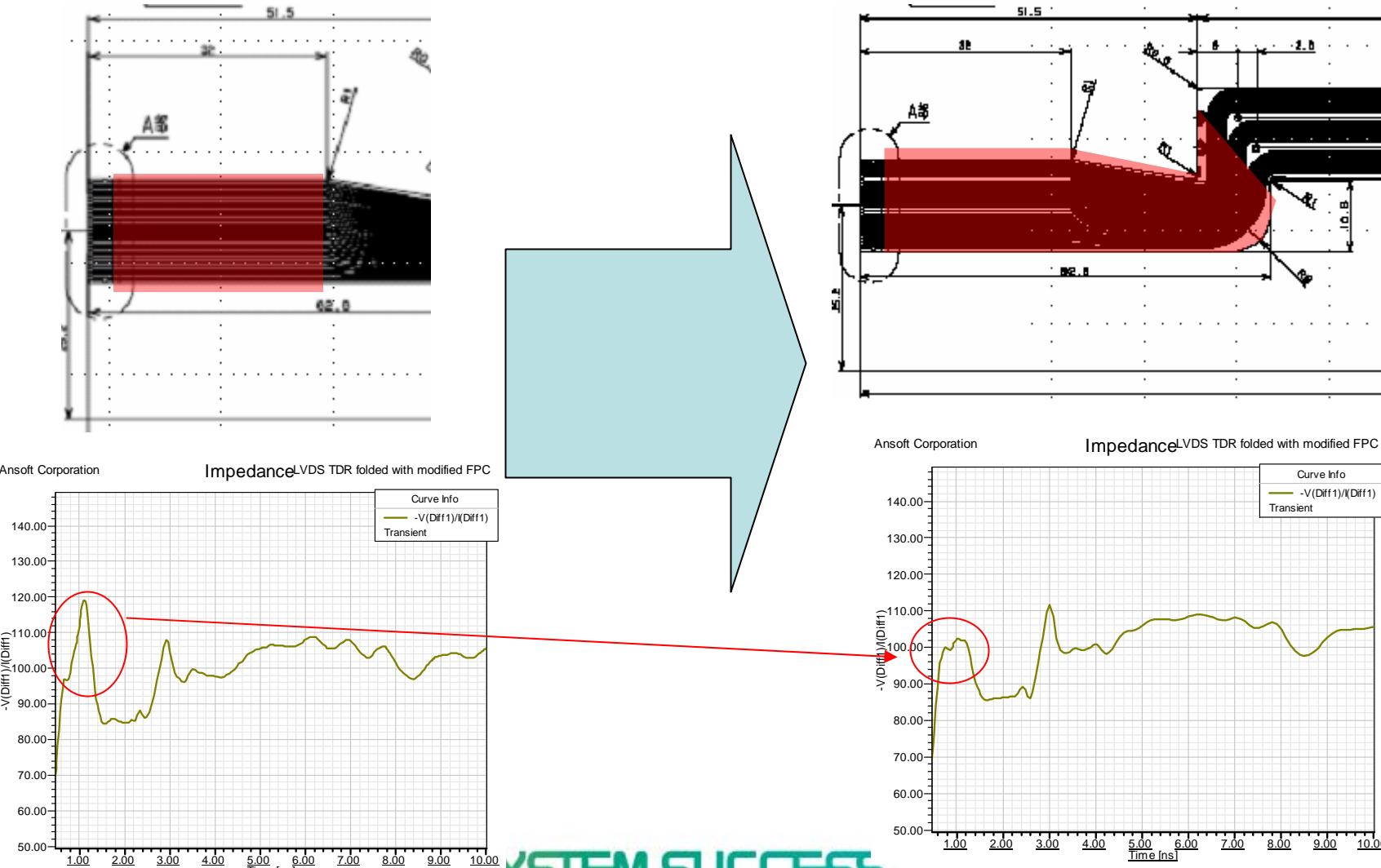
$$Z_o = \sqrt{Z_{open} * Z_{short}}$$



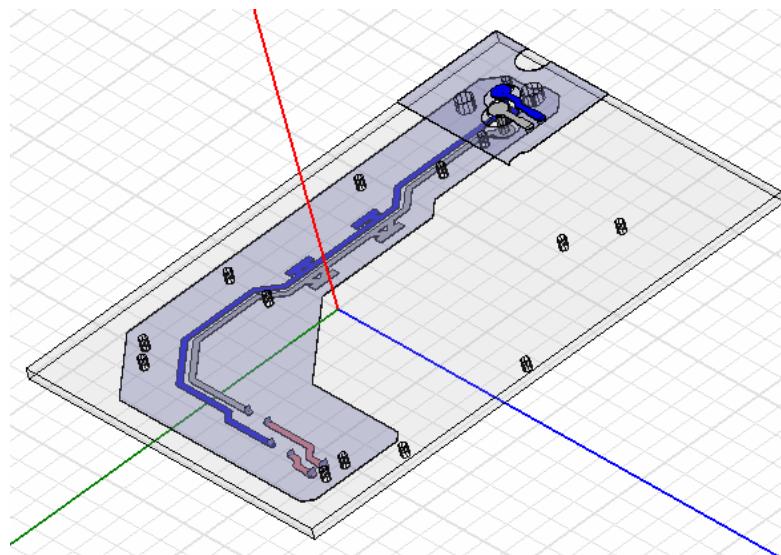
Matching FPC Impedance



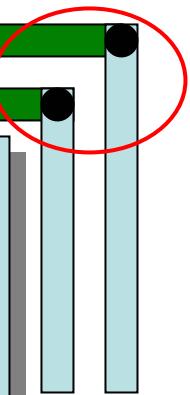
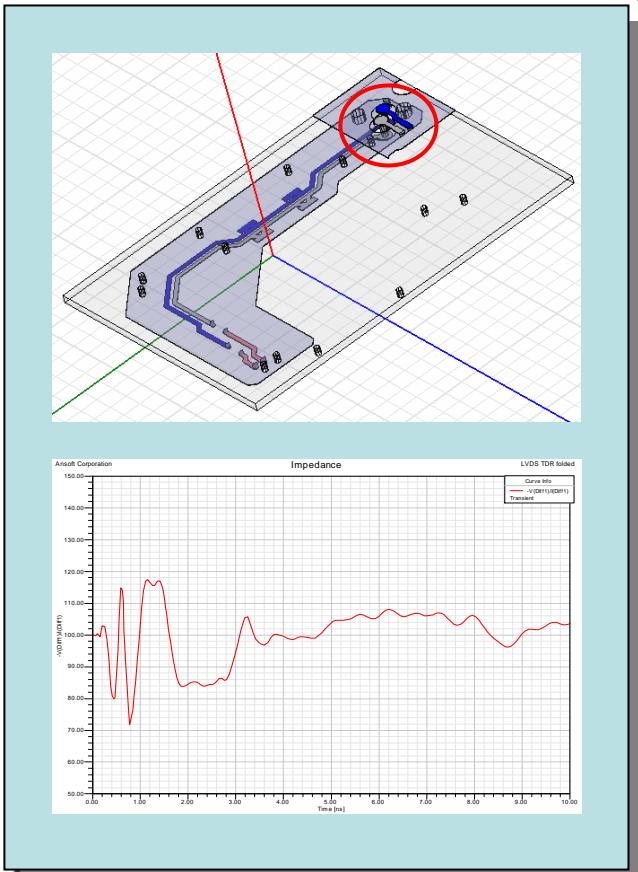
Covering with Polyamide Sheets



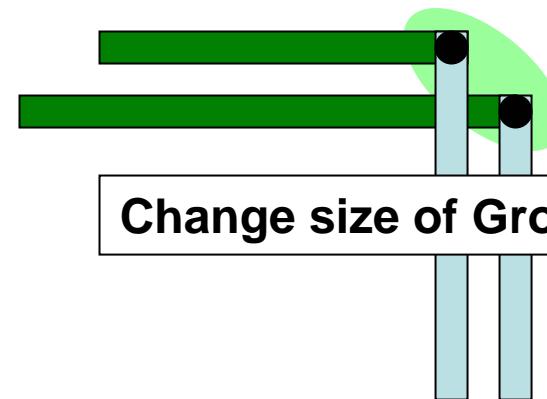
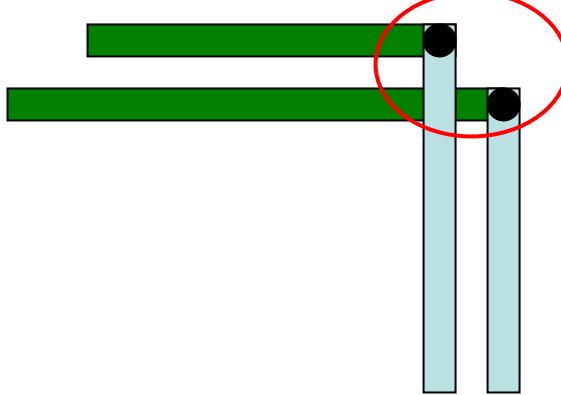
Improving Impedance Matching and Skew on the LVDS Tx board



Skew and Impedance Matching on LVDS Tx Board



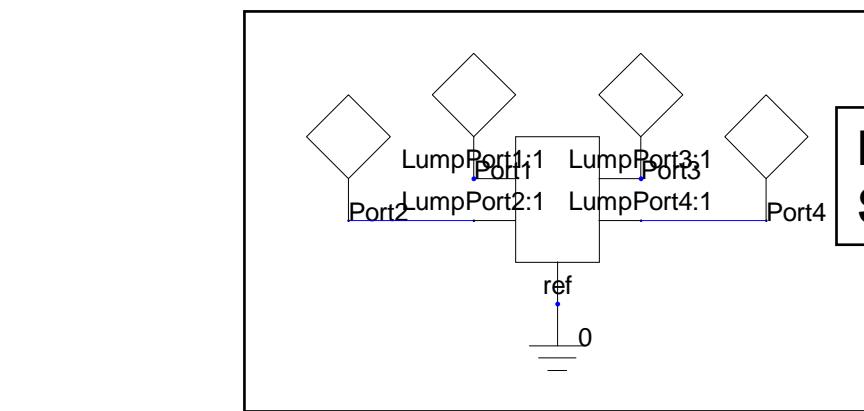
Skew
Matching
Strategy



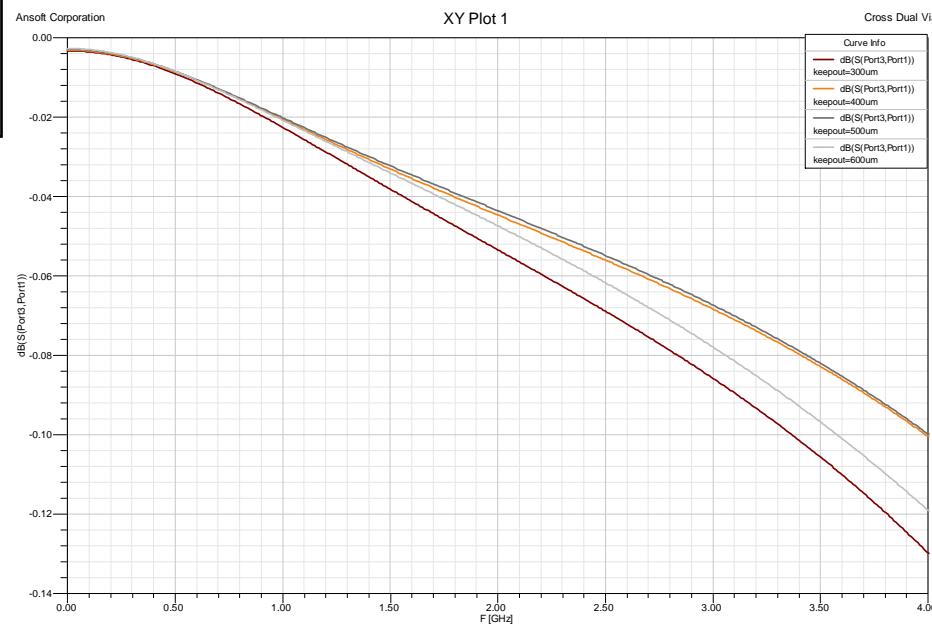
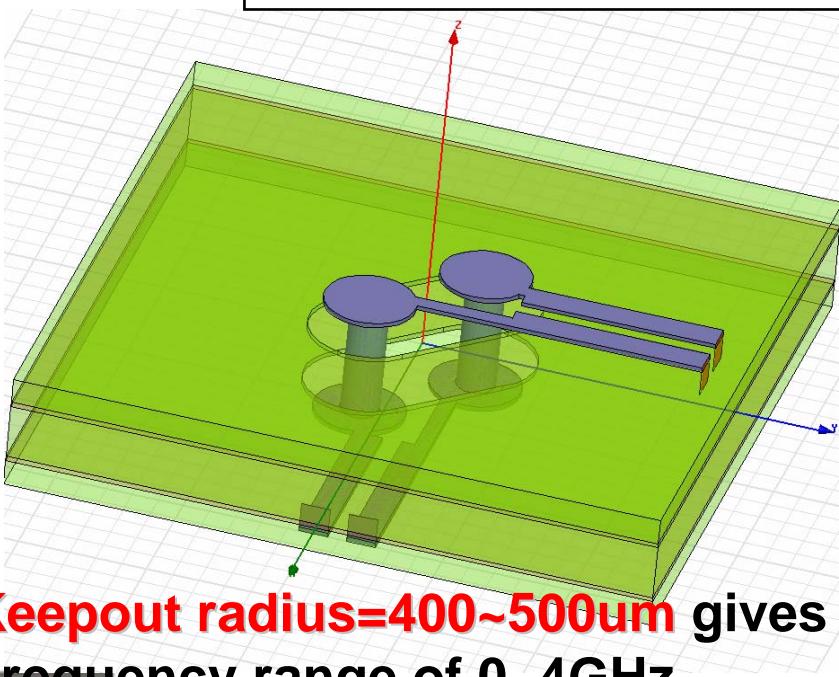
Change size of Ground Hall

Impedance Matching Strategy

Crossed Dual Via Model in HFSS

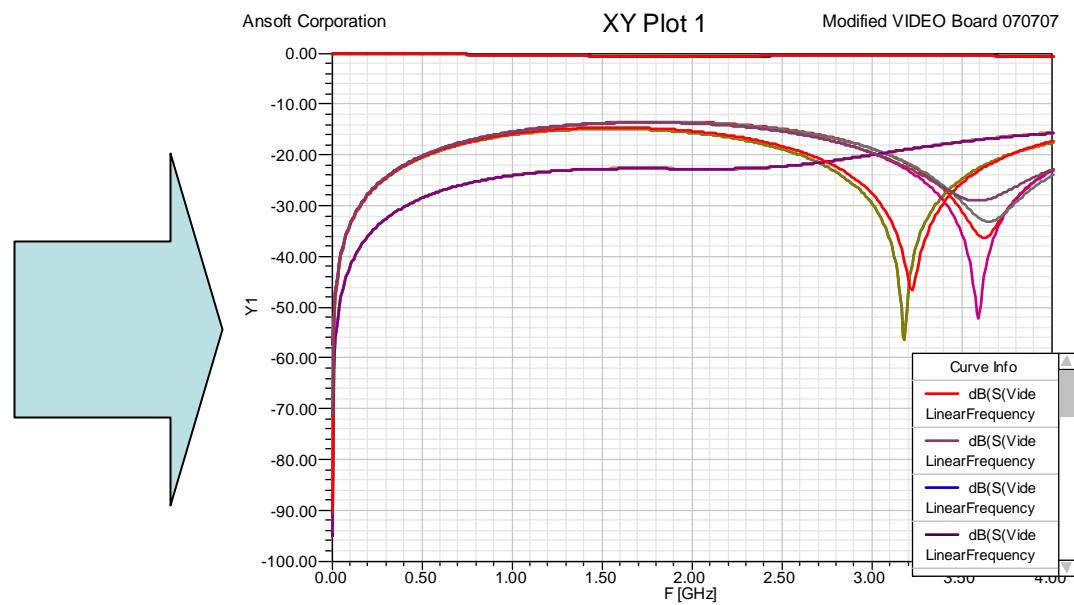
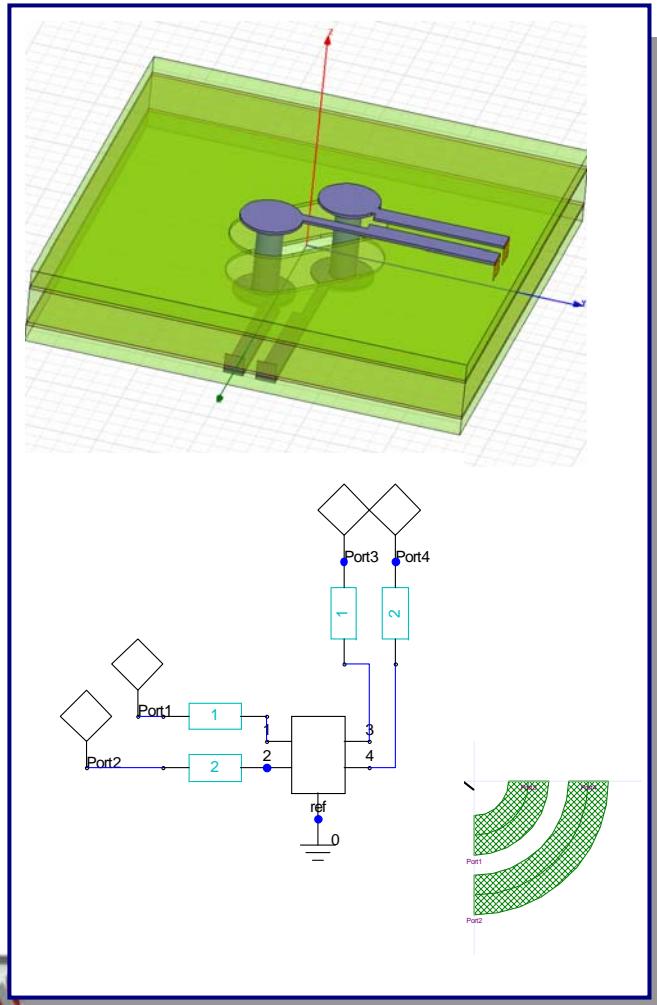


**Nexxim Dynamic Link to HFSS
Sweep on the parameters on the HFSS**

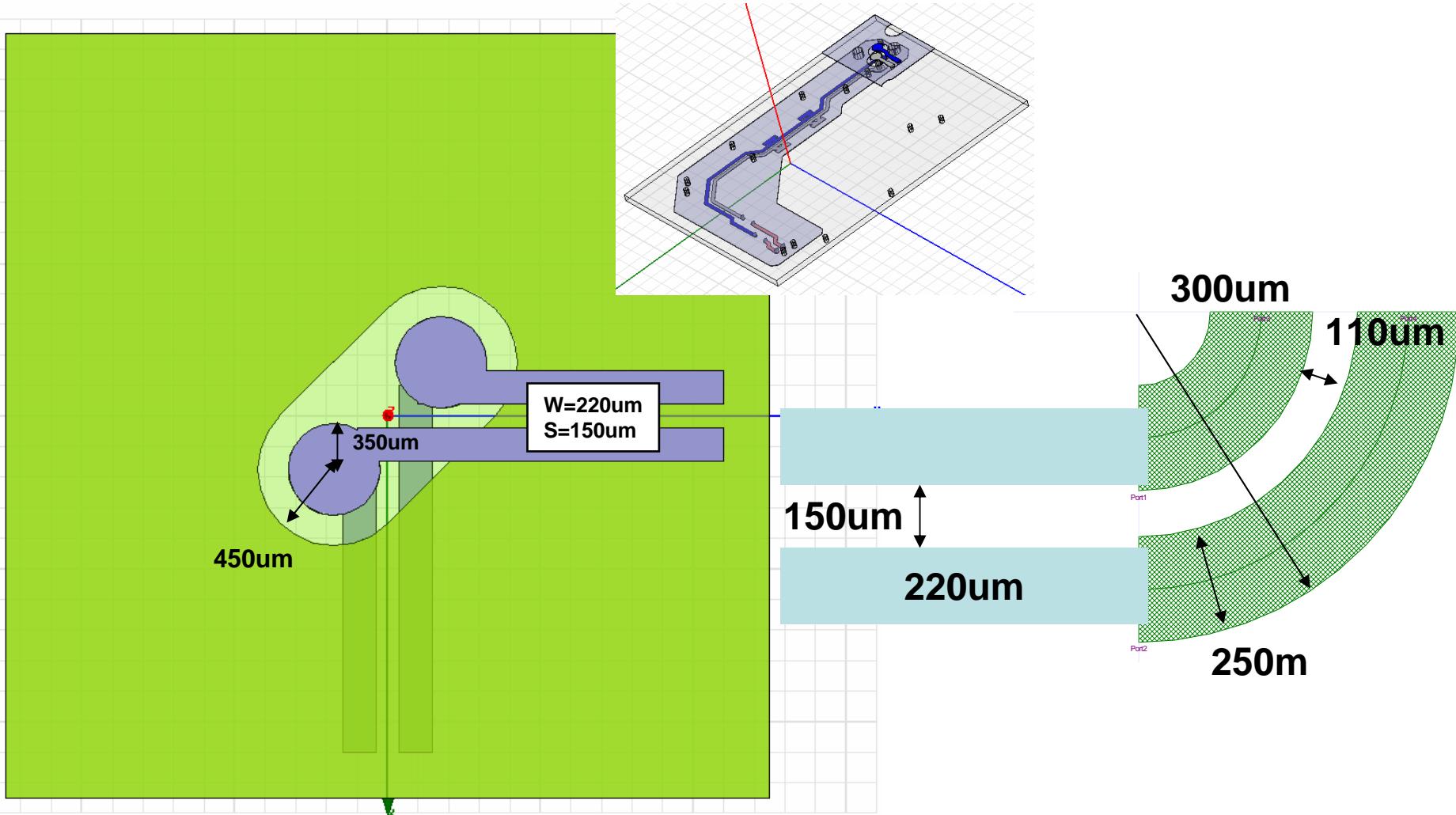


Keepout radius=400~500um gives the maximum transfer through the Frequency range of 0~4GHz

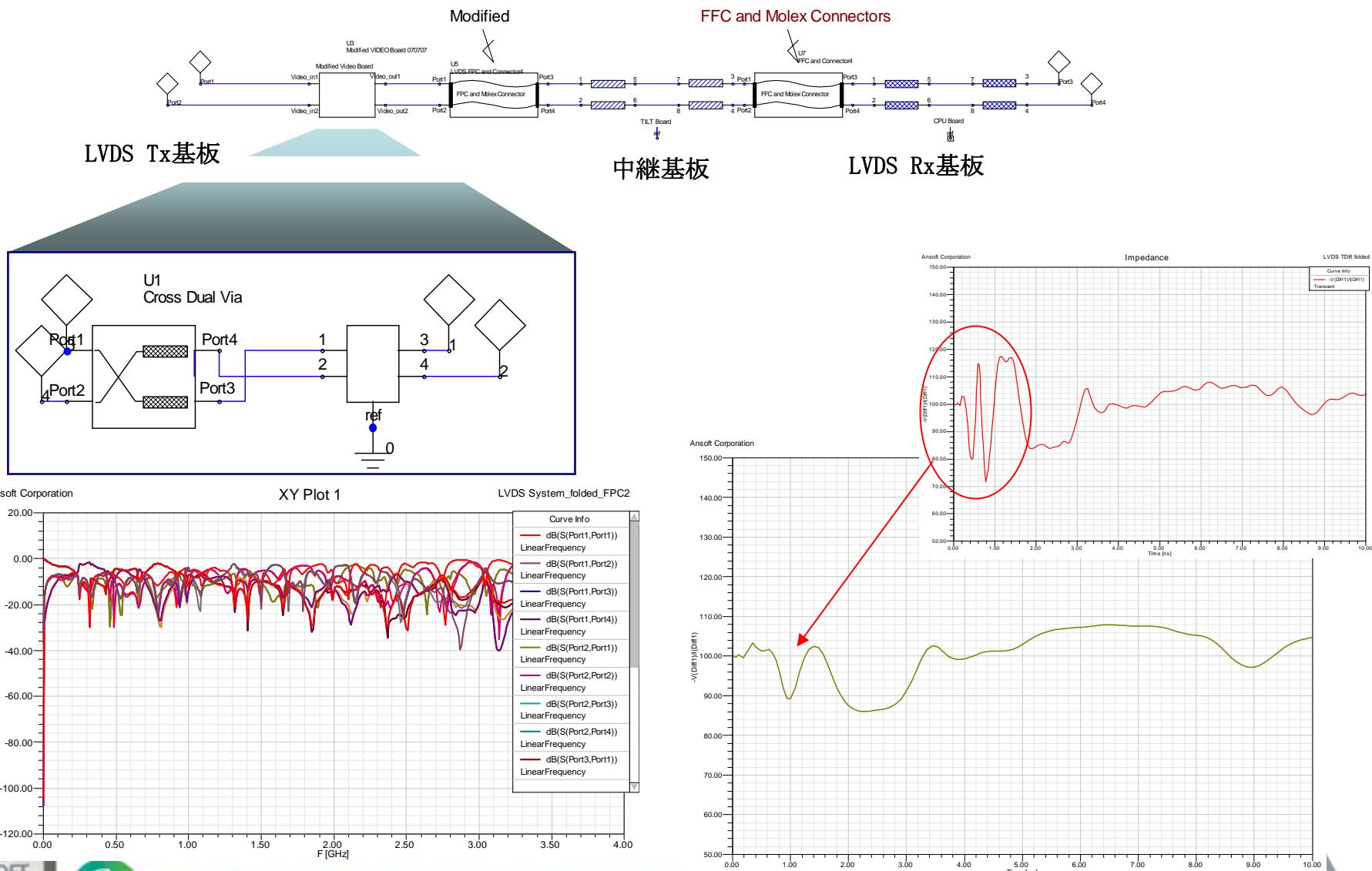
Layout Design using PlanarEM in Ansoft Designer 4.0



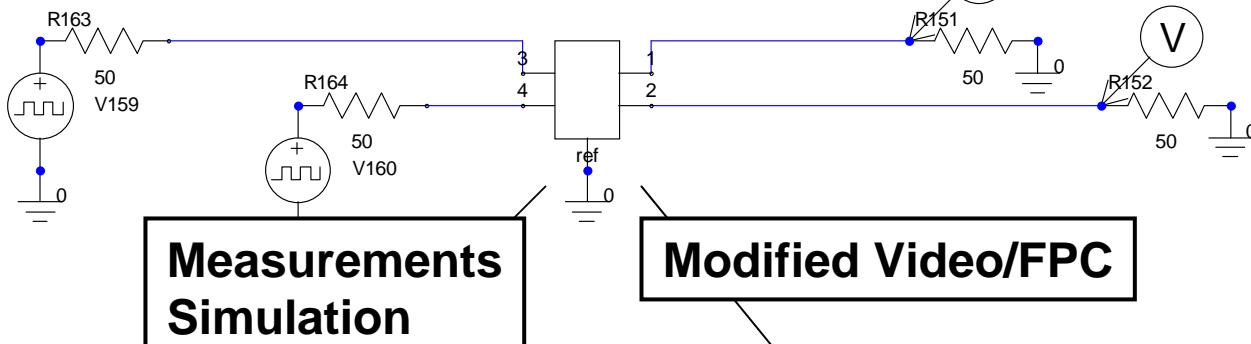
Final Design Modification



Modified Total System

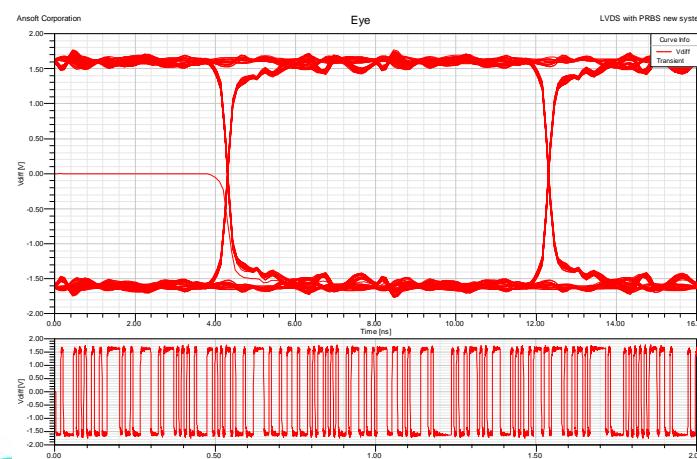
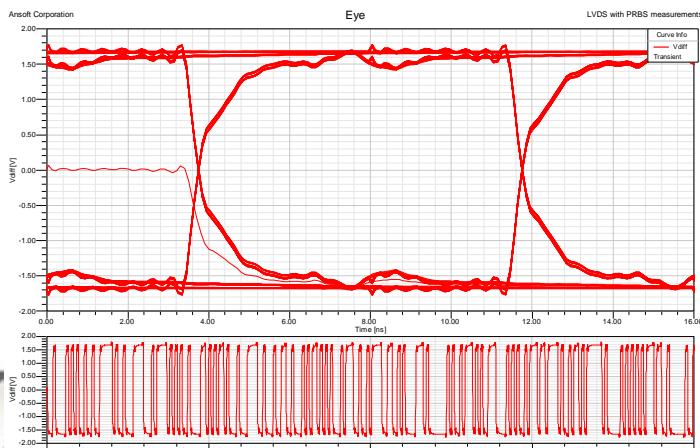
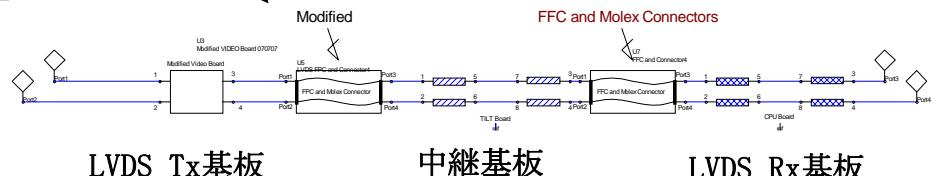
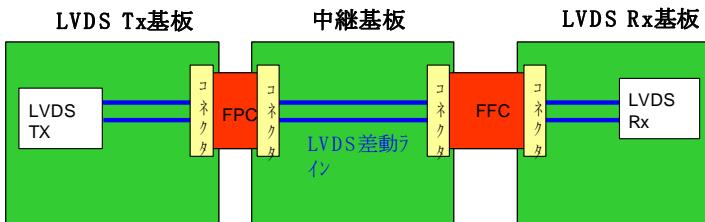


PRBS Simulation

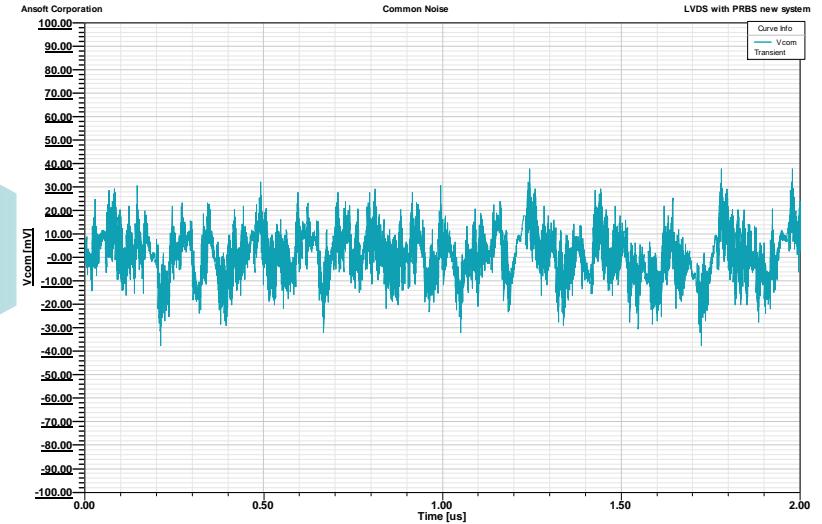
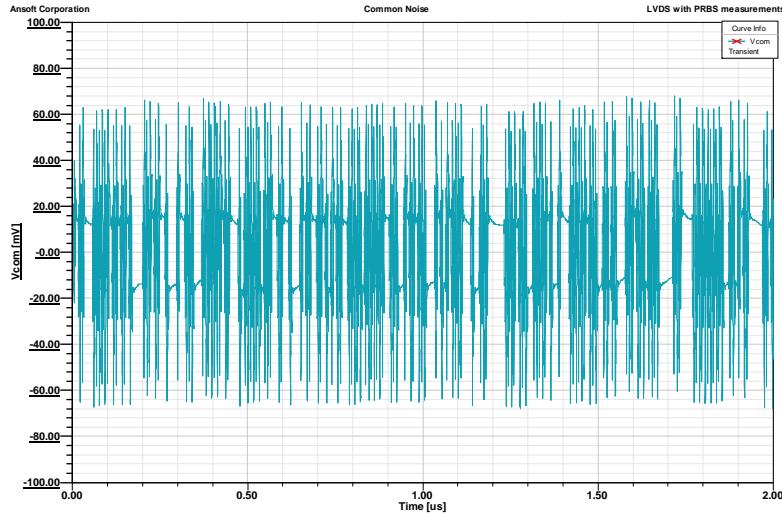


**Measurements
Simulation**

Modified Video/FPC



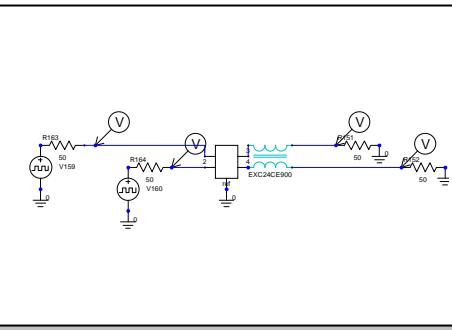
Output Common Noise



Name	X	Y
m1	20.0000	-55.6890
m2	30.0000	-55.3825
m3	40.0000	-62.7354
m4	50.0000	-58.8138
m5	60.0000	-56.4834
m6	70.0000	-54.9845
m7	180.0000	-64.7677
m8	190.0000	-65.2964
m9	200.0000	-64.2190

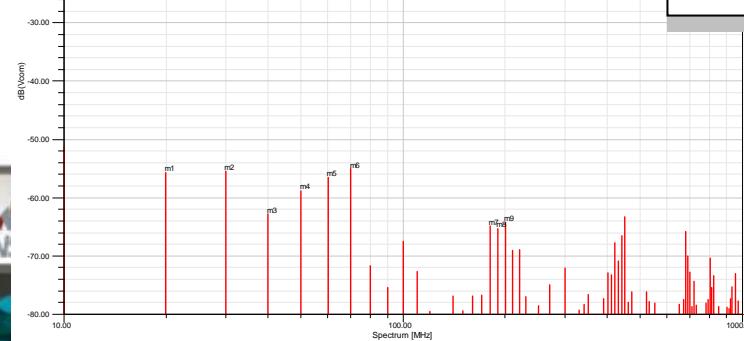
XY Plot 1

LVDS w

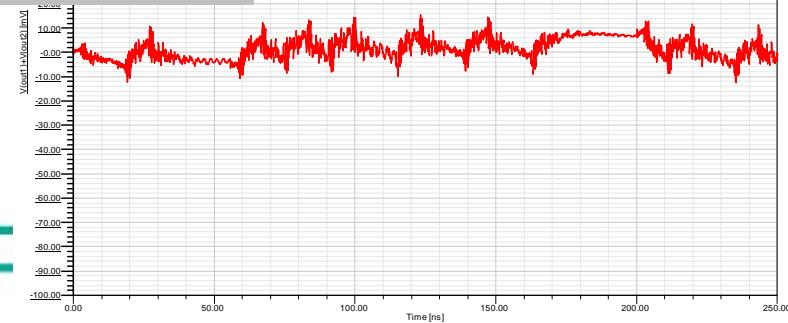


ith CMNF

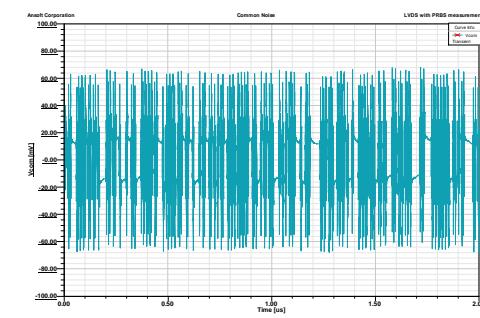
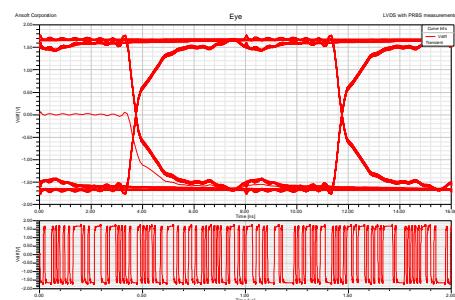
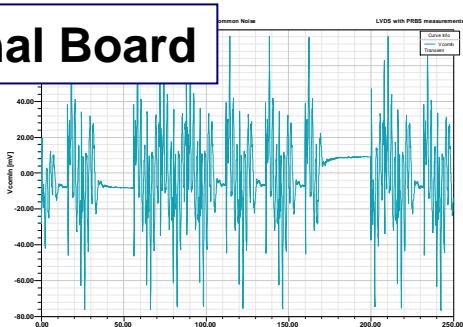
LVDS with PRBS new system



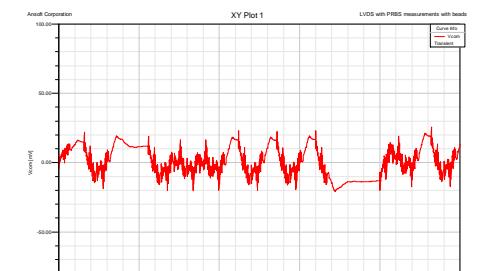
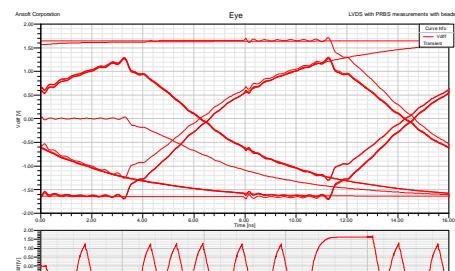
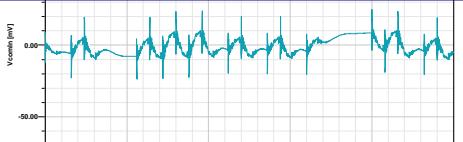
M SUC
TRONIC DESIGN



Original Board

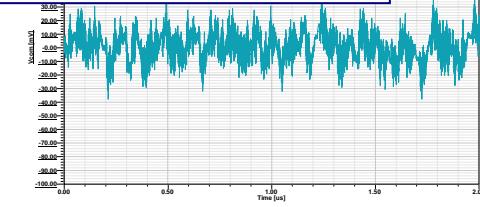
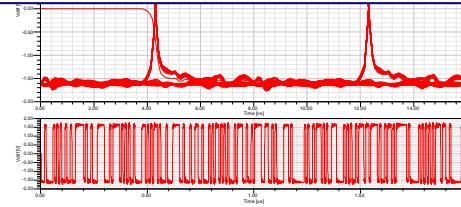
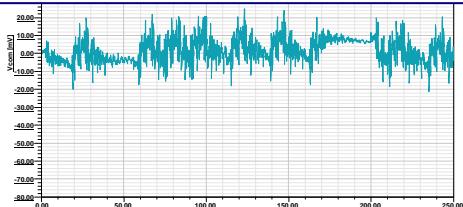


Ferrite Beads+Original Board

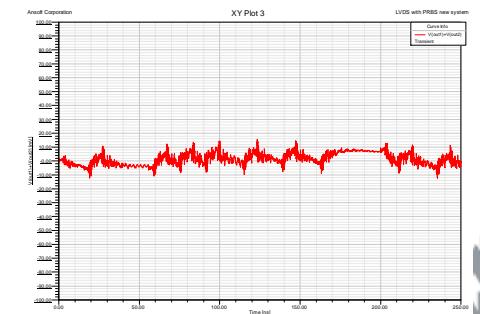
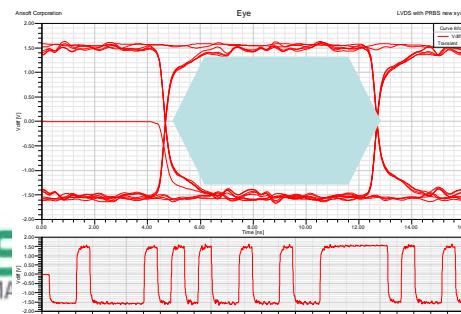
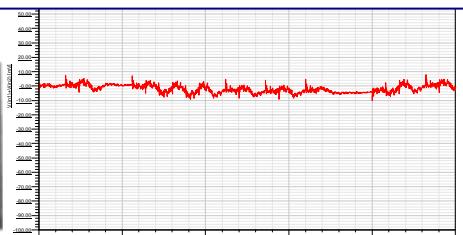


Mod

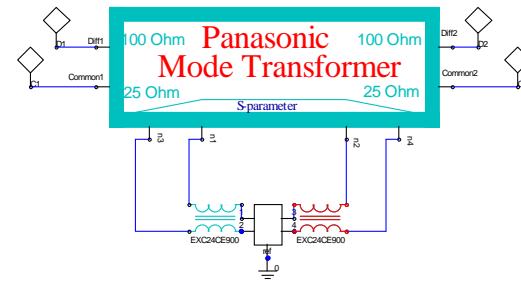
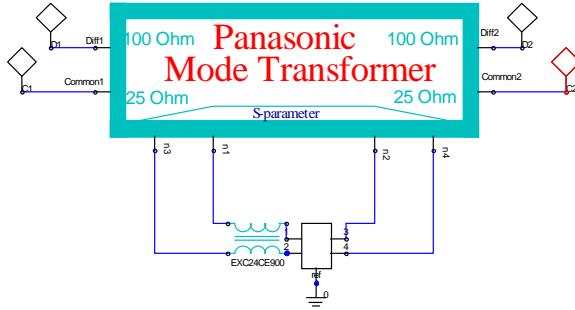
Can achieve Much Quality EYE pattern with Lower Common Mode Degeneration!!



CMNF+Modified FPC/VIDEO



Using Scc21 and EMI?

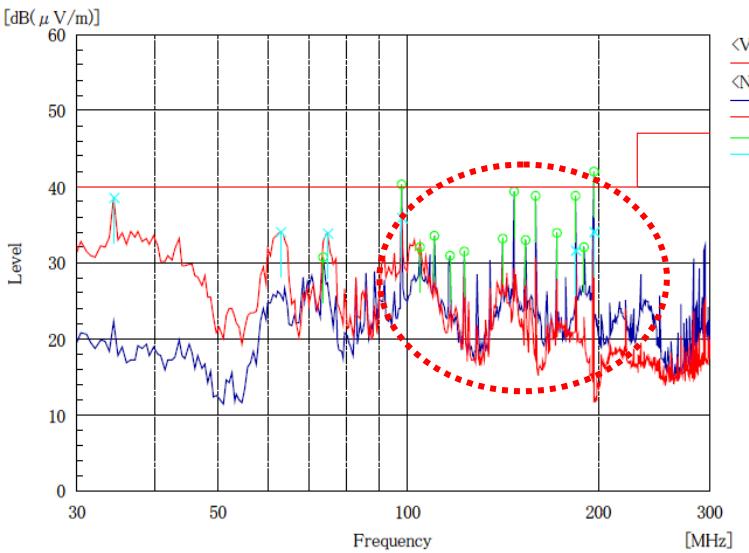


We are currently evaluating the theory and measurements in correlate the Scc21 and EMI measurements.
Qualitatively we observed if Scc21 is small, it will make the EMI small.

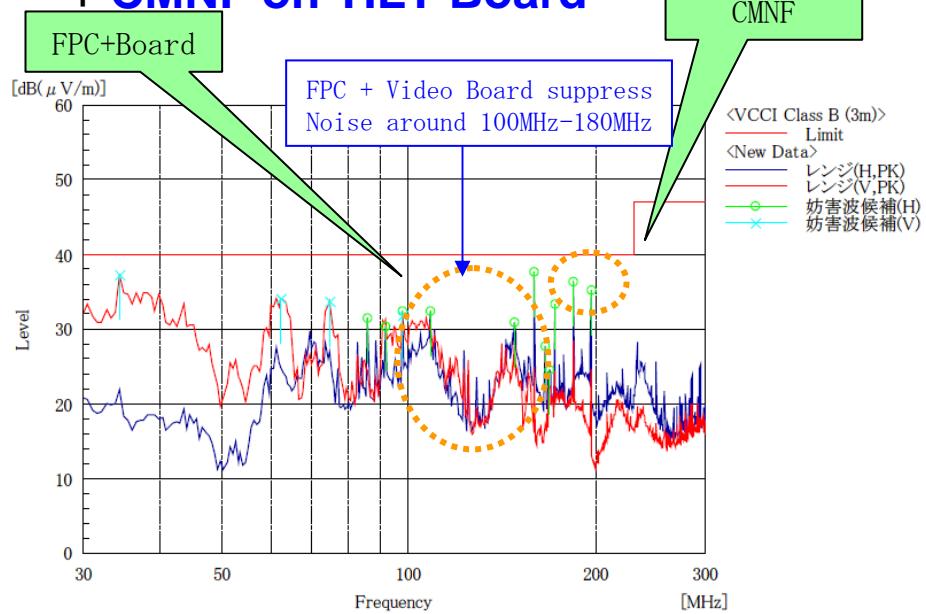


Measurement on EMI

OLD VIDEO BOARD+FPC-A



NEW VIDEO BOARD+**FPC-B**
+ **CMMF on TILT Board**



By suppressing the common mode degeneration, we have suppressed the EMI Noise. Confirmed on the LAB.



Designing quality signal line with Less Common Mode Degeneration

- **Impedance matching** (either from frequency domain and TDR) will result in keeping the quality signal through out the system.
- **Skew Reduction** through out the System will reduce Common Mode Degeneration.
- CMNF achieves additional reduction of the **Common Mode Noise**.
- **EMI measurements are done by Panasonic and new Board+FPC+CMNF reduced all the unnecessary noise from the system.**
- **New System maintain good Eye Pattern and Low EMI.**
- **Electromagnetic Simulator** together with **Accurate Circuit Simulator** are the key of pin pointing the cause of the problems



Acknowledgements

- We like to express deep acknowledgements to **Mr. Higashtiani (PED)** for providing all the precious data to confirm on the Measurements and Simulation comparison.
- **Mr. Kihara and Mr. Suto (PCC)** for providing us prototype board as well as new board. Also all the effort of making EMI measurements.
- **Mr. Noda (Molex Japan)** for providing us the connector models and S-parameters with helpful discussions.

