

NPN SILICON RF POWER TRANSISTOR

DESCRIPTION:

The **ASI AVD350** is Designed for

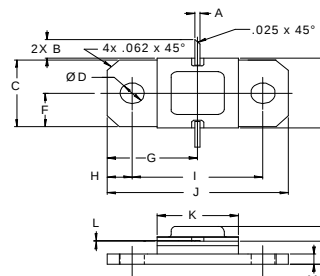
FEATURES:

- Input Matching Network
- **Omnigold™** Metalization System

MAXIMUM RATINGS

I_C	19.8 A
V_{CC}	55 V
P_{DISS}	720 W @ $T_C = 25^\circ\text{C}$
T_J	-65°C to $+250^\circ\text{C}$
T_{STG}	-65°C to $+200^\circ\text{C}$
θ_{JC}	0.17°C/W

PACKAGE STYLE .400 2NL FLG



DIM	MINIMUM inches / mm	MAXIMUM inches / mm
A	.020 / 0.51	.030 / 0.76
B	.100 / 2.54	
C	.376 / 9.55	.396 / 10.06
D	.110 / 2.79	.130 / 3.30
E	.395 / 10.03	.407 / 10.34
F	.193 / 4.90	
G	.450 / 11.43	
H	.125 / 3.18	
I	.640 / 16.26	.660 / 16.76
J	.890 / 22.61	.910 / 23.11
K	.395 / 10.03	.415 / 10.54
L	.004 / 0.10	.007 / 0.18
M	.052 / 1.32	.072 / 1.83
N	.118 / 3.00	.131 / 3.33
P		.230 / 5.84

ORDER CODE: ASI10566

CHARACTERISTICS $T_C = 25^\circ\text{C}$

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
BV_{CBO}	$I_C = 10\text{ mA}$	65			V
BV_{CER}	$I_C = 25\text{ mA}$ $R_{BE} = 10\ \Omega$	65			V
BV_{EBO}	$I_E = 1.0\text{ mA}$	3.5			V
I_{CES}	$V_{CE} = 50\text{ V}$			25	mA
h_{FE}	$V_{CE} = 5.0\text{ V}$ $I_C = 1.0\text{ A}$	15		120	---
P_G η_c	$V_{CC} = 50\text{ V}$ $P_{OUT} = 350\text{ W}$ $f = 1090\text{ MHz}$	6.7 40	7.1 44	---	dB %