ANALOG DEVICES

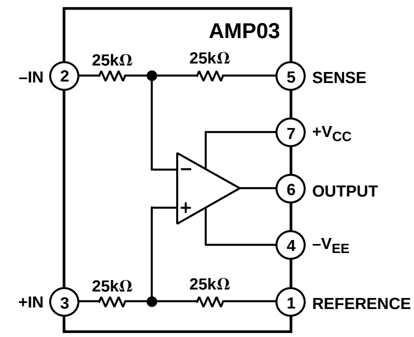
Precision, Unity-Gain Differential Amplifier

AMP03

FEATURES
High CMRR: 100 dB Typ
Low Nonlinearity: 0.001% Max
Low Distortion: 0.001% Typ
Wide Bandwidth: 3 MHz Typ
Fast Slew Rate: 9.5 V/μs Typ
Fast Settling (0.01%): 1 μs Typ
Low Cost

APPLICATIONS
Summing Amplifiers
Instrumentation Amplifiers
Balanced Line Receivers
Current-Voltage Conversion
Absolute Value Amplifier
4 mA–20 mA Current Transmitter
Precision Voltage Reference Applications
Lower Cost and Higher Speed Version of INA105

FUNCTIONAL BLOCK DIAGRAM



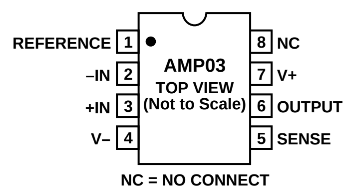
GENERAL DESCRIPTION
The AMP03 is a monolithic unity-gain, high speed differential amplifier. Incorporating a matched thin-film resistor network, the AMP03 features stable operation over temperature without requiring expensive external matched components. The AMP03 is a basic analog building block for differential amplifier and instrumentation applications.

The differential amplifier topology of the AMP03 serves to both amplify the difference between two signals and provide extremely high rejection of the common-mode input voltage. By providing common-mode rejection (CMR) of 100 dB typical, the AMP03 solves common problems encountered in instrumentation design. As an example, the AMP03 is ideal for performing either addition or subtraction of two signals without using expensive externally-matched precision resistors. The large common-mode rejection is made possible by matching the internal resistors to better than 0.002% and maintaining a thermally symmetric layout. Additionally, due to high CMR over frequency, the AMP03 is an ideal general amplifier for buffering signals in a noisy environment into data acquisition systems.

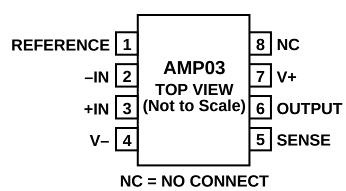
The AMP03 is a higher speed alternative to the INA105. Featuring slew rates of 9.5 V/μs, and a bandwidth of 3 MHz, the AMP03 offers superior performance for high speed current sources, absolute value amplifiers and summing amplifiers than the INA105.

PIN CONNECTIONS

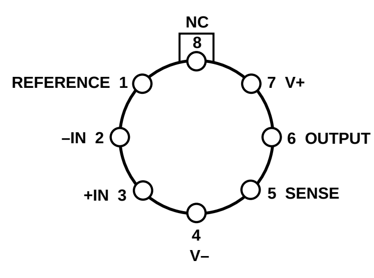
8-Lead Plastic DIP (P Suffix)



8-Lead SOIC (S Suffix)



Header (J Suffix)



REV. E

One Technology Way, P.O. Box 9106, Norwood, MA 02062-9106, U.S.A.
Tel: 781/329-4700 World Wide Web Site: http://www.analog.com
Fax: 781/326-8703

AMP03–SPECIFICATIONS

ELECTRICAL CHARACTERISTICS (@ V_S = ±15 V, T_A = +25°C, unless otherwise noted)

Parameter	Symbol	Conditions	AMP03F Min	AMP03F Typ	AMP03F Max	AMP03B Min	AMP03B Typ	AMP03B Max	AMP03G Min	AMP03G Typ	AMP03G Max	Units
Offset Voltage	V_{OS}	V_{CM} = 0 V	–400	10	400	–700	20	700	–750	25	750	µV
Gain Error		No Load, V_{IN} = ±10 V, R_S = 0 Ω		0.00004	0.008		0.00004	0.008		0.001	0.008	%
Input Voltage Range	IVR	(Note 1)	±10			±10			±10			V
Common-Mode Rejection	CMR	V_{CM} = ±10 V	85	100		80	95		80	95		dB
Power Supply Rejection Ratio	PSRR	V_S = ±6 V to ±18 V		0.6	10		0.6	10		0.7	10	µV/V
Output Swing	V_O	R_L = 2 kΩ	±12	±13.7		±12	±13.7		±12	±13.7		V
Short-Circuit Current Limit	I_{SC}	Output Shorted to Ground	+45/–15			+45/–15			+45/–15			mA
Small-Signal Bandwidth (–3 dB)	BW	R_L = 2 kΩ		3			3			3		MHz
Slew Rate	SR	R_L = 2 kΩ	6	9.5		6	9.5		6	9.5		V/µs
Capacitive Load Drive Capability	C_L	No Oscillation		300			300			300		pF
Supply Current	I_{SY}	No Load		2.5	3.5		2.5	3.5		2.5	3.5	mA

NOTES

[1]Input voltage range guaranteed by CMR test.

Specifications subject to change without notice.

ELECTRICAL CHARACTERISTICS (@ V_S = ±15 V, –55°C ≤ T_A ≤ +125°C for B Grade)

Parameter	Symbol	Conditions	AMP03B Min	AMP03B Typ	AMP03B Max	Units
Offset Voltage	V_{OS}	V_{CM} = 0 V	–1500	150	1500	µV
Gain Error		No Load, V_{IN} = ±10 V, R_S = 0 Ω		0.0014	0.02	%
Input Voltage Range	IVR		±20			V
Common-Mode Rejection	CMR	V_{CM} = ±10 V	75	95		dB
Power Supply Rejection Ratio	PSRR	V_S = ±6 V to ±18 V		0.7	20	µV/V
Output Swing	V_O	R_L = 2 kΩ	±12	±13.7		V
Slew Rate	SR	R_L = 2 kΩ		9.5		V/µs
Supply Current	I_{SY}	No Load		3.0	4.0	mA

Specifications subject to change without notice.

ELECTRICAL CHARACTERISTICS (@ V_S = ±15 V, –40°C ≤ T_A ≤ +85°C for F and G Grades)

Parameter	Symbol	Conditions	AMP03F Min	AMP03F Typ	AMP03F Max	AMP03G Min	AMP03G Typ	AMP03G Max	Units
Offset Voltage	V_{OS}	V_{CM} = 0 V	–1000	100	1000	–2000	200	2000	µV
Gain Error		No Load, V_{IN} = ±10 V, R_S = 0 Ω		0.0008	0.015		0.002	0.02	%
Input Voltage Range	IVR		±20			±20			V
Common-Mode Rejection	CMR	V_{CM} = ±10 V	80	95		75	90		dB
Power Supply Rejection Ratio	PSRR	V_S = ±6 V to ±18 V		0.7	15		1.0	15	µV/V
Output Swing	V_O	R_L = 2 kΩ	±12	±13.7		±12	±13.7		V
Slew Rate	SR	R_L = 2 kΩ		9.5			9.5		V/µs
Supply Current	I_{SY}	No Load		2.6	4.0		2.6	4.0	mA

Specifications subject to change without notice.

WAFER TEST LIMITS (@ V_S = ±15 V, T_A = +25°C, unless otherwise noted)

Parameter	Symbol	Conditions	AMP03BC Limit	Units
Offset Voltage	V_{OS}	V_S = ±18 V	0.5	mV max
Gain Error		No Load, V_{IN} = ±10 V, R_S = 0 Ω	0.008	% max
Input Voltage Range	IVR		±10	V min
Common-Mode Rejection	CMR	V_{CM} = ±10 V	80	dB min
Power Supply Rejection Ratio	PSRR	V_S = ±6 V to ±18 V	8	μV/V max
Output Swing	V_O	R_L = 2 kΩ	±12	V max
Short-Circuit Current Limit	I_{SC}	Output Shorted to Ground	+45/–15	mA min
Supply Current	I_{SY}	No Load	3.5	mA max

Electrical tests are performed at wafer probe to the limits shown. Due to variations in assembly methods and normal yield loss, yield after packaging is not guaranteed for standard product dice. Consult factory to negotiate specifications based on dice lot qualifications through sample lot assembly and testing.

ABSOLUTE MAXIMUM RATINGS[1]

Supply Voltage . . . . ±18 V
Input Voltage[2] . . . . Supply Voltage
Output Short-Circuit Duration . . . . Continuous
Storage Temperature Range
  P, J Package . . . . –65°C to +150°C
Lead Temperature (Soldering, 60 sec) . . . . +300°C
Junction Temperature . . . . +150°C
Operating Temperature Range
  AMP03B . . . . –55°C to +125°C
  AMP03F, AMP03G . . . . –40°C to +85°C

Package Type	θ_{JA}[3]	θ_{JC}	Units
Header (J)	150	18	°C/W
8-Lead Plastic DIP (P)	103	43	°C/W
8-Lead SOIC (S)	155	40	°C/W

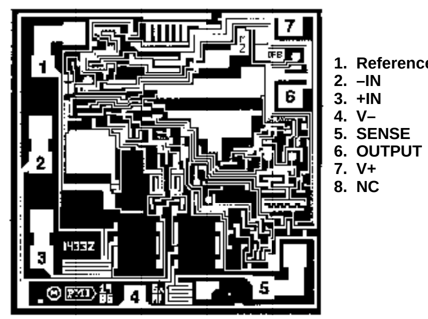
NOTES
[1]Absolute maximum ratings apply to both DICE and packaged parts, unless otherwise noted.
[2]For supply voltages less than ±18 V, the absolute maximum input voltage is equal to the supply voltage.
[3]θ_{JA} is specified for worst case mounting conditions, i.e., θ_{JA} is specified for device in socket for header and plastic DIP packages and for device soldered to printed circuit board for SOIC package.

ORDERING GUIDE[1]

Model	Temperature Range	Package Description	Package Option[2]
AMP03GP	–40°C to +85°C	8-Lead Plastic DIP	N-8
AMP03BJ	–40°C to +85°C	Header	H-08B
AMP03FJ	–40°C to +85°C	Header	H-08B
AMP03BJ/883C	–55°C to +125°C	Header	H-08B
AMP03GS	–40°C to +85°C	8-Lead SOIC	SO-8
AMP03GS-REEL	–40°C to +85°C	8-Lead SOIC	SO-8
5962-9563901MGA	–55°C to +125°C	Header	H-08B
AMP03GBC		Die	

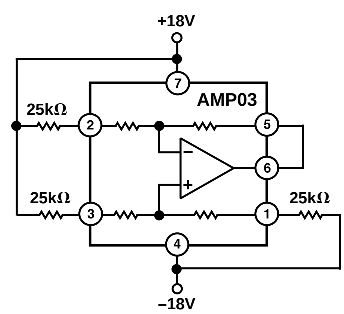
NOTES
[1]Burn-in is available on commercial and industrial temperature range parts in plastic DIP and header packages.
[2]For devices processed in total compliance to MIL-STD-883, add /883 after part number. Consult factory for /883 data sheet.

DICE CHARACTERISTICS

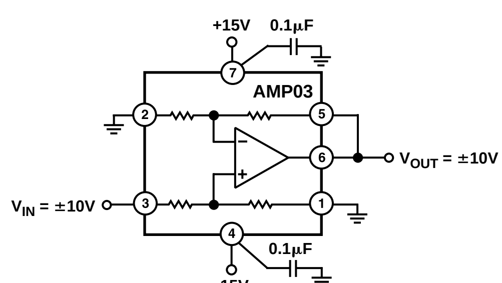


DIE SIZE 0.076 × 0.076 inch, 5,776 sq. mils
(1.93 × 1.93 mm, 3.73 sq. mm)

BURN-IN CIRCUIT



SLEW RATE TEST CIRCUIT



CAUTION

ESD (electrostatic discharge) sensitive device. Electrostatic charges as high as 4000 V readily accumulate on the human body and test equipment and can discharge without detection. Although the AMP03 features proprietary ESD protection circuitry, permanent damage may occur on devices subjected to high energy electrostatic discharges. Therefore, proper ESD precautions are recommended to avoid performance degradation or loss of functionality.

WARNING!
ESD SENSITIVE DEVICE

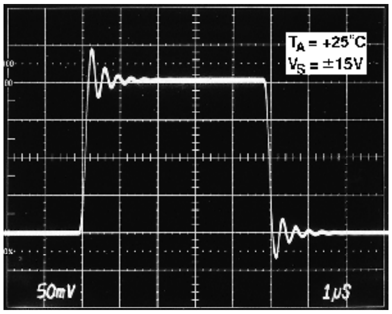


Figure 1. Small Signal Transient Response

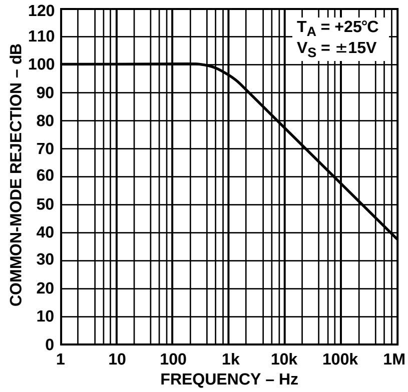


Figure 2. Common-Mode Rejection vs. Frequency

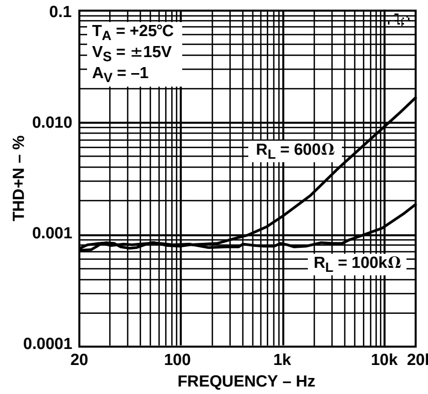


Figure 3. Total Harmonic Distortion vs. Frequency

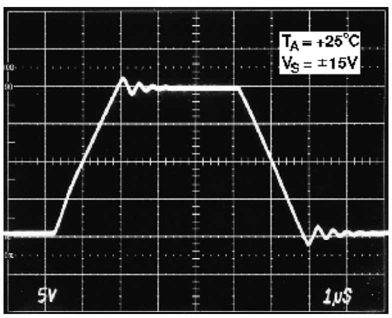


Figure 4. Large Signal Transient Response

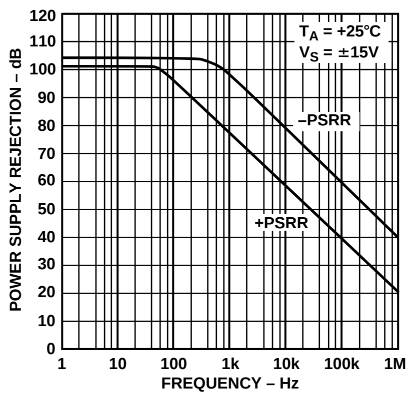


Figure 5. Power Supply Rejection vs. Frequency

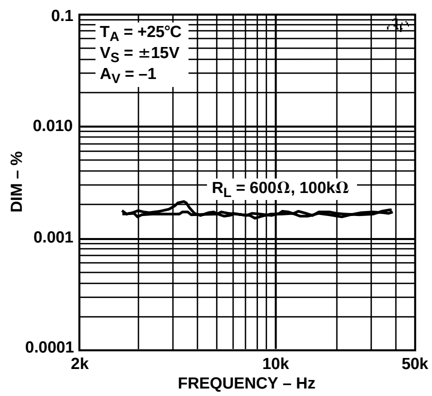


Figure 6. Dynamic Intermodulation Distortion vs. Frequency

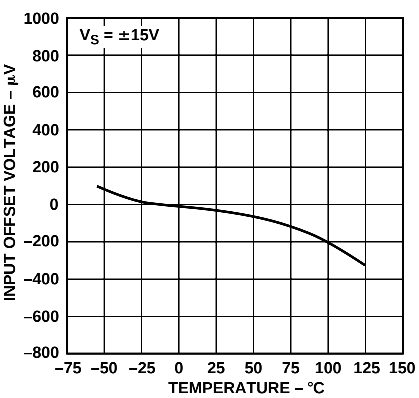


Figure 7. Input Offset Voltage vs. Temperature

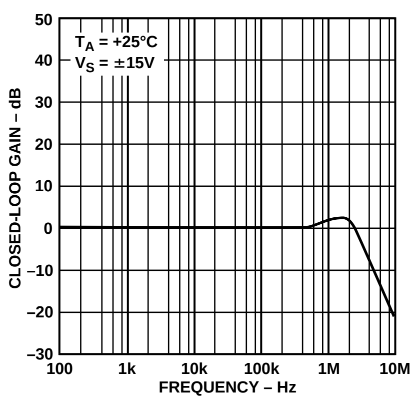


Figure 8. Closed-Loop Gain vs. Frequency

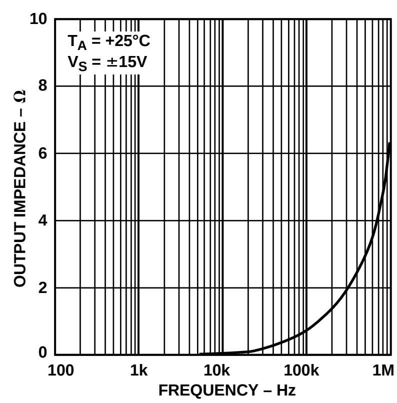


Figure 9. Closed-Loop Output Impedance vs. Frequency

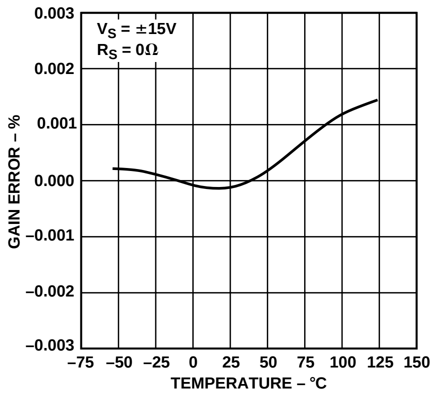


Figure 10. Gain Error vs. Temperature

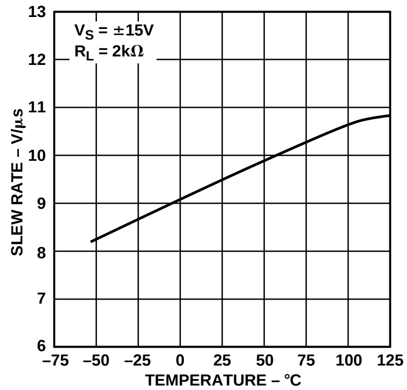


Figure 11. Slew Rate vs. Temperature

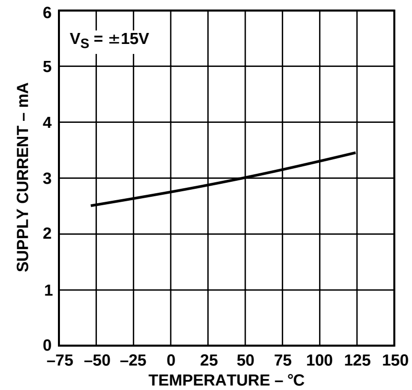


Figure 12. Supply Current vs. Temperature

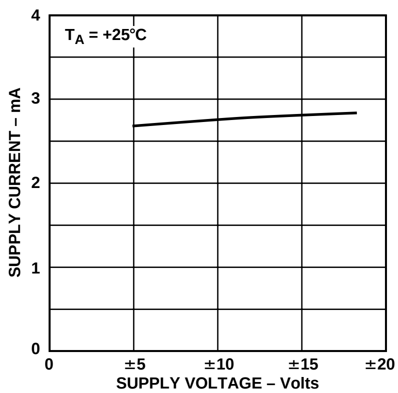


Figure 13. Supply Current vs. Supply Voltage

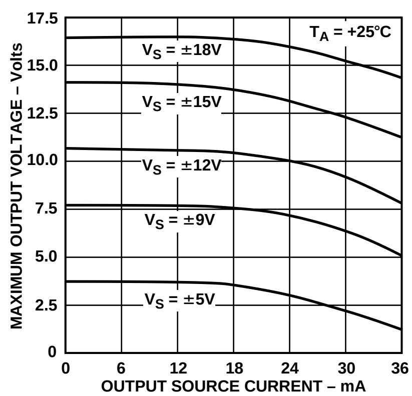


Figure 14. Maximum Output Voltage vs. Output Current (Source)

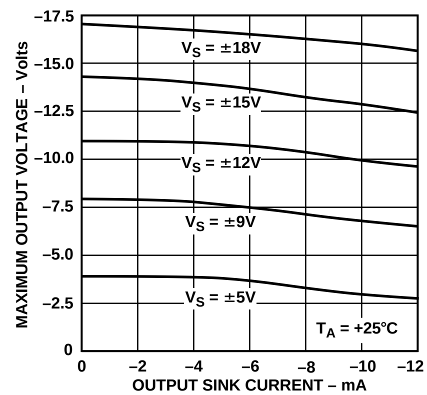


Figure 15. Maximum Output Voltage vs. Output Current (Sink)

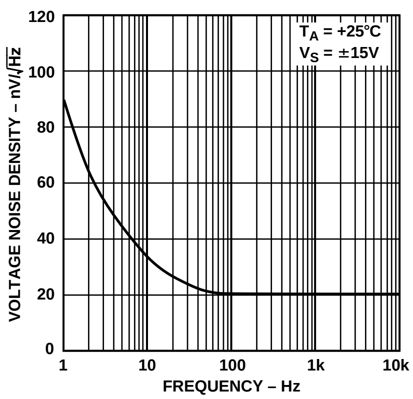


Figure 16. Voltage Noise Density vs. Frequency

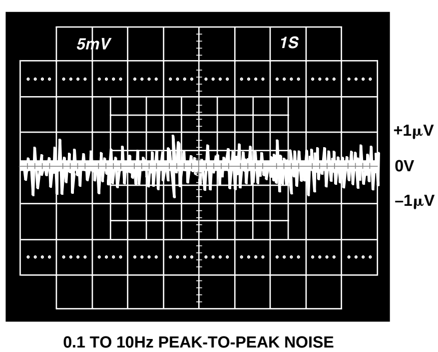


Figure 17. Low Frequency Voltage Noise

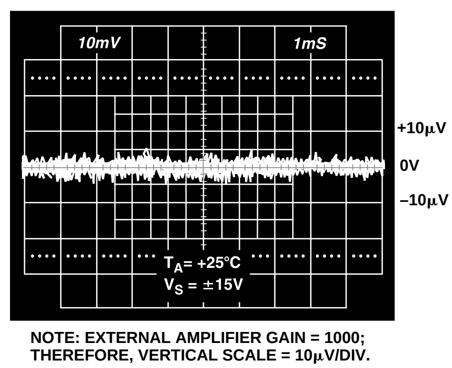


Figure 18. Voltage Noise from 0 kHz to 1 kHz

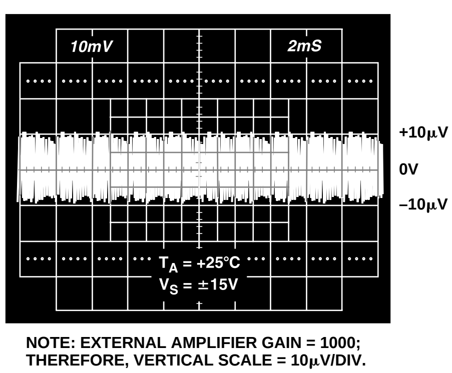


Figure 19. Voltage Noise from 0 kHz to 10 kHz

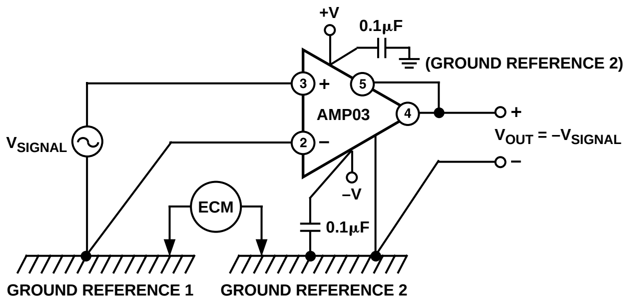


Figure 20. AMP03 Serves to Reject Common-Mode Voltages in Instrumentation Systems. Common-Mode Voltages Occur Due to Ground Current Returns. V_{SIGNAL} and E_{CM} Must Be Within the Common-Mode Range of AMP03.

APPLICATIONS INFORMATION

The AMP03 represents a versatile analog building block. In order to capitalize on fast settling time, high slew rate and high CMR, proper decoupling and grounding techniques must be employed. Figure 20 illustrates the use of 0.1 µF decoupling capacitors and proper ground connections.

MAINTAINING COMMON-MODE REJECTION

In order to achieve the full common-mode rejection capability of the AMP03, the source impedance must be carefully controlled. Slight imbalances of the source resistance will result in a degradation of DC CMR—even a 5 Ω imbalance will degrade CMR by 20 dB. Also, the matching of the reactive source impedance must be matched in order to preserve the CMRR over frequency.

APPLICATION CIRCUITS

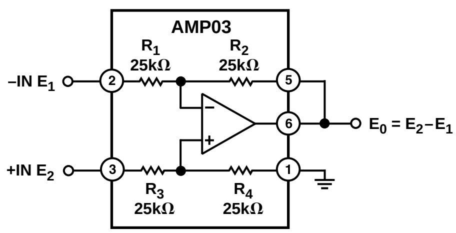


Figure 21. Precision Difference Amplifier. Rejects Common-Mode Signal = $(E_1 + E_2)/2$ by 100 dB

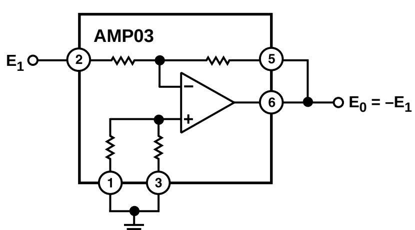


Figure 22. Precision Unity-Gain Inverting Amplifier

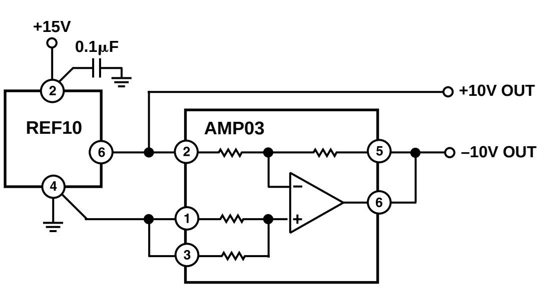


Figure 23. ±10 V Precision Voltage Reference

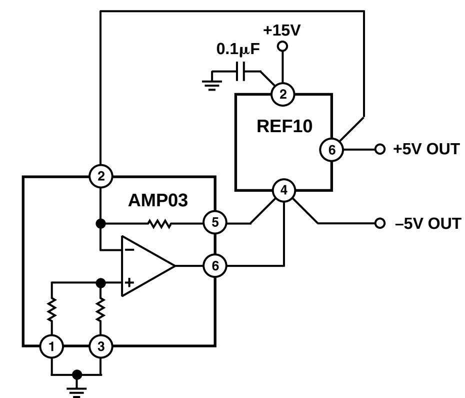


Figure 24. ±5 V Precision Voltage Reference

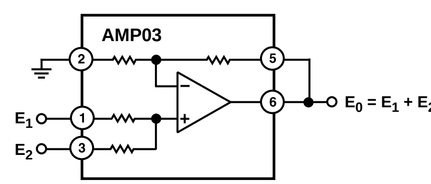


Figure 25. Precision Summing Amplifier

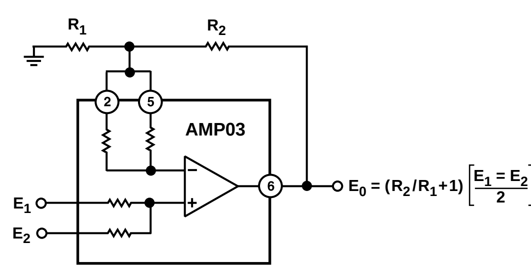


Figure 26. Precision Summing Amplifier with Gain

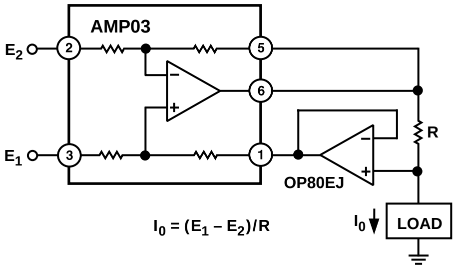


Figure 27. Differential Input Voltage-to-Current Converter for Low I_{OUT}. OP80EJ Maintains 250 fA Max Input Current, Allowing I_O to Be Less Than 1 pA

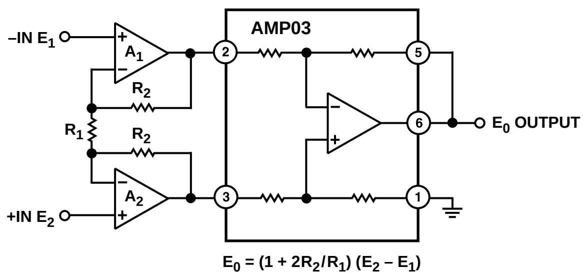


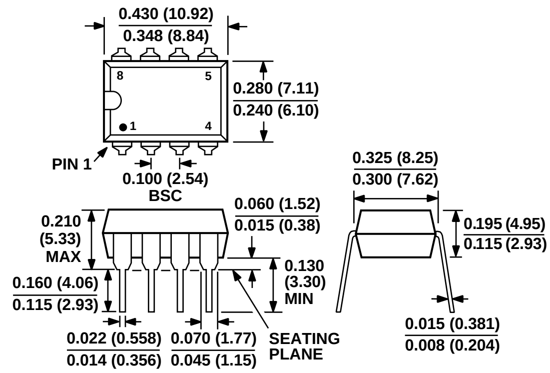
Figure 28. Suitable Instrumentation Amplifier Requirements Can Be Addressed by Using an Input Stage Consisting of A_1, A_2, R_1 and R_2. The Following Matrix Suggests a Suitable Amplifier.

System Design Requirement	Suggested Op Amp For A1 and A2
Source Impedance Low, Need Low Voltage Noise Performance	OP27, OP37 OP227 (Dual Matched) OP270 (Dual) OP271 OP470 OP471
Source Impedance High ($R_S \geq 15$ kΩ). Need Low Current Noise	OP80 OP41 OP43 OP249 OP97
Require Ultrahigh Input Impedance	OP80 OP97 OP41 OP43
Need Wider Bandwidth and High Speed	OP42 OP43 OP249

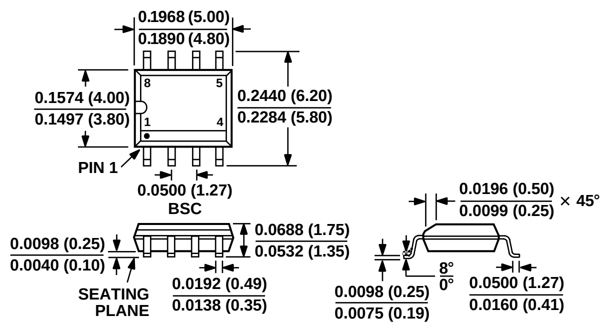
OUTLINE DIMENSIONS

Dimensions shown in inches and (mm).

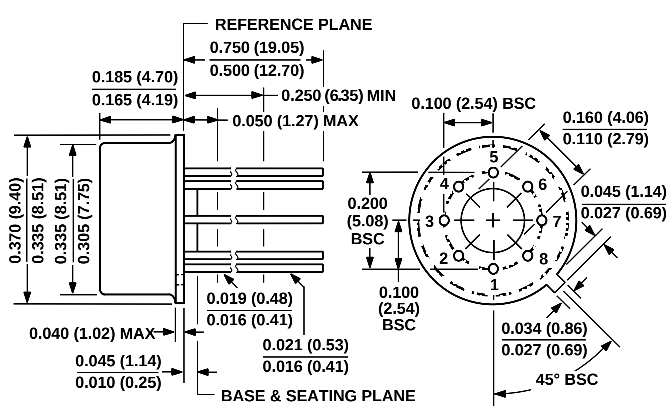
8-Lead Plastic DIP (N-8)



8-Lead SOIC (SO-8)



8-Lead Metal Can (H-08B)



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