3·1/2 DIGIT SINGLE CHIP A/D CONVERTER WITH DISPLAY HOLD

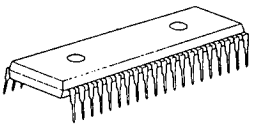
■ GENERAL DESCRIPTION

The NJU9203B/9204B are low-power-consumption, high-performance 3·1/2 digit single chip A/D converters with display hold containing a voltage reference, oscillator, 3·1/2 digits A/D converter,7-segment decoder, display driver and control circuits.

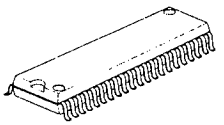
The NJU9203B is designed for direct LCD driving and the NJU9204B for LED direct driving.

The NJU9203B/9204B can be operated on simple application circuits as they require only few external components, therefore they are most suited for digital multimeter, digital thermometer and other likes.

■ PACKAGE OUTLINE



NJU9203BD/9204BD

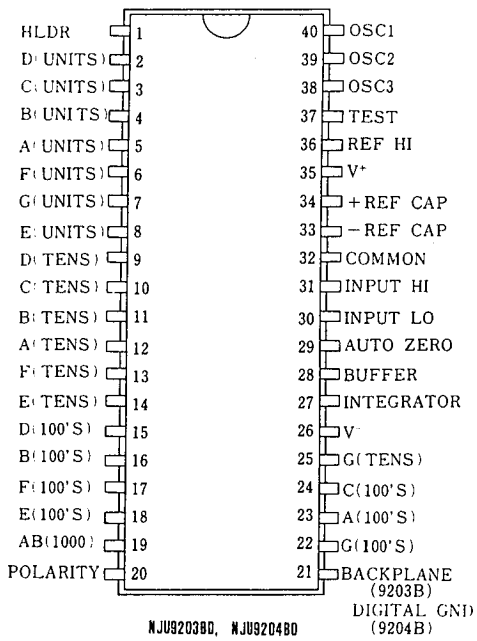


NJU9203BM/9204BM

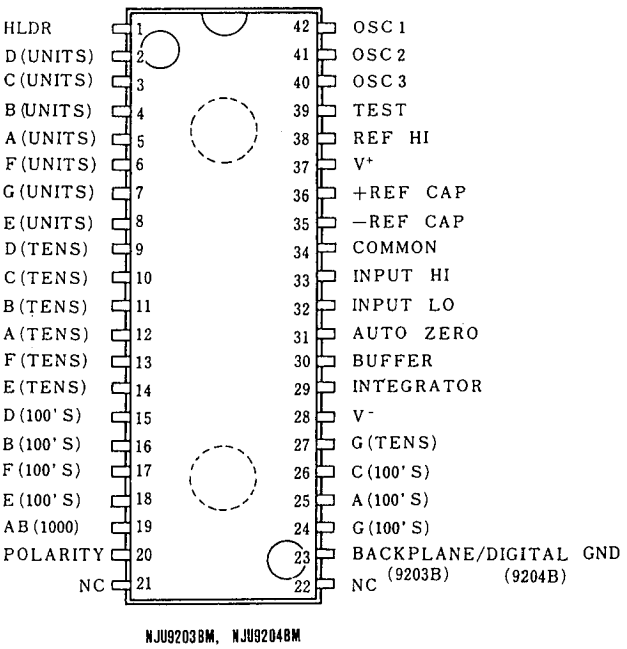
■ FEATURES

- Display Hold Function
- Guaranteed 0 reading for 0 input on all scales
- Polarity detection at 0 point
  using a high-accuracy null-detection
- Low Input Current -- 1pA typ.
- True differential input
- Display device direct driving
  NJU9203B -- LCD
  NJU9204B -- LED
- Reference and Oscillation Circuits incorporated
- Low power consumption
- No external active components required
- Package Outline -- DIP 40 /DMP 42
- C-MOS Technology

■ PIN CONFIGURATION



NJU9203BD, NJU9204BD



NJU9203BM, NJU9204BM

■ ABSOLUTE MAXIMUM RATINGS

(Ta=25℃)

PARAMETER	DEVICE	SYMBOL	RATINGS	UNIT
Supply Voltage	9203B Only 9204B Only 9204B Only	$V^+ - V^-$ V^+ V^-	15 +6 -9	V
Analog Input Voltage	9203B/9204B	V_{IN}	$V^+ \sim V^-$	V
Reference Input Voltage	9203B/9204B	Vref	$V^+ \sim V^-$	V
Clock Input	9203B Only 9204B Only	V_{CLK}	Test $\sim V^+$ GND $\sim V^+$	V
Power Dissipation	9203B/9204B	P_D	300 / 800	mW
Operating Temperature Range	9203B/9204B	T_{opr}	0 $\sim$ + 75	℃
Storage Temperature Range	9203B/9204B	T_{stg}	-40 $\sim$ +125	℃

Note 1) The input current is limit by ±100uA when the input voltage is over supply voltage.

■ ELECTRICAL CHARACTERISTICS

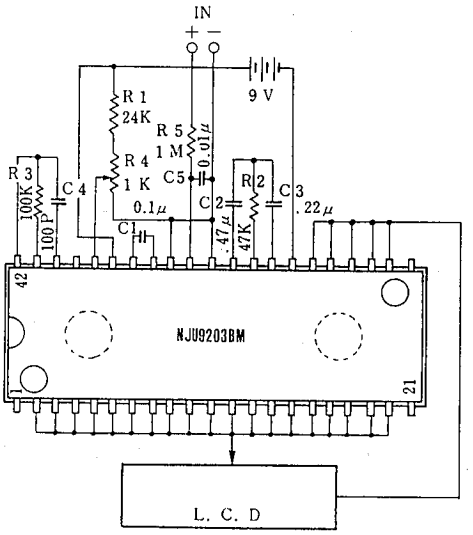
(Ta=25℃, f_{clock}=48kHz)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNIT
Zero Input Reading	No	V_{IN}=0.0V,FS=200.0mV	-000.0	±000.0	+000.0	Counts
Ratiometric Reading	N1000	V_{IN}=Vref,Vref=100mV	999	999/1000	1000	
Rollover Error	Err	$-V_{IN}$=$+V_{IN}$-200.0mV	-2	±0.5	+2	Counts
Linearity	Lin	Full Scale=200mV	-2	±0.5	+2	Counts
Common Mode Rejection Ratio	C_{MRR}	Vcm=±1V,VIN=0V, Full Scale=200.0mV		50		μV/V
Noise(P-P Value)	V_{NI}	V_{IN}=0V,FS=200.0mV		30		μV
Leakage Current	I_L	V_{IN}=0V		1	10	pA
Zero Reading Drift	Z_D	V_{IN}=0V,0<Ta<75℃		0.2	1	μV/℃
Scale Factor Temp. Coeff.	Ftemp	V_{IN}=199.0mV,0<Ta<75℃		1	5	ppm/℃
Operating Current	I_{DD}	V_{IN}=0V,No Load		0.8	1.8	mA
Analog Common Voltage		25kΩ Between Common and Positive Supply	2.4	3.0	3.2	V
Temp. Coeff.of Analog Common				80		ppm/℃
Seg. Drive Voltage (9203B)		V_{DD}=9V	4	5	6	V
BackPlane Drive Volt.(9203B)		V_{DD}=9V	4	5	6	
Seg. Sinking Current (9204B)		V_{DD}=5V, Except Term.19	5.0	8.0		mA
Seg. Sinking Current (9204B)		Seg.V=3V Term.19 only	10	16		

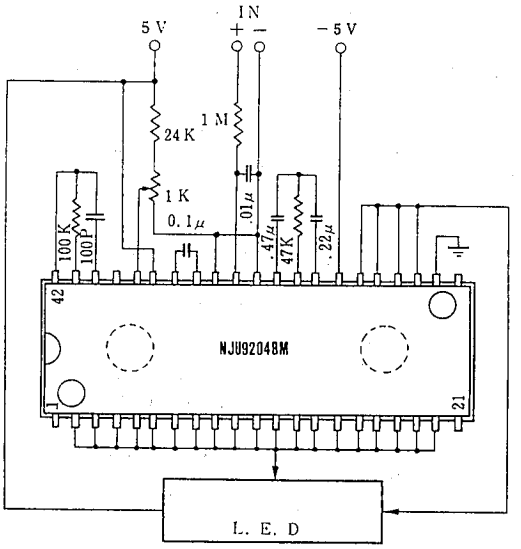
Note 2) Differential read out value of positive and negative voltage input.
3) Error from the input-output linear characteristics getting from positive and negative full-scale input read out.
4) The peak value of noise must be not over 95% period in the measurement time.

■ APPLICATION CIRCUITS

NJU9203B



NJU9204B



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