

6 Digit 6 Function LCD Alarm Watch with EL Backlight

HN6191E

Features

- ◆ Split operative stopwatch (Accurate to 1/100 second)
- ◆ Hour, Minute, Second, Month and Date normal display
- ◆ 6 digit chronograph : Auto ranging after 30 minutes to hour, minute, second from minute, second, 1/100 second
- ◆ Alarm output for melody IC
- ◆ Alarm function with 4 to 5 minutes snooze
- ◆ User selectable 12/24 format & 4 year calendar
- ◆ Chime on every hour
- ◆ One touch correction of time error within ± 30 seconds
- ◆ Oscillating built-in capacitor
- ◆ Direct drive of piezo buzzer
- ◆ Pad 7 is built-in Transistor
- ◆ EL Backlight control

特点

- ◆ 跑表有分段计时功能(准确至1/100 秒)
- ◆ 可显示时、分、秒、月、日及星期
- ◆ 6 位定时器：以分、秒、1/100 秒显示方式，计满30 分后自动转换至时、分、秒
- ◆ 可接音乐电路芯片作音乐闹铃输出
- ◆ 具有4~5 分钟再闹功能
- ◆ 整点发声报时
- ◆ 可选用12 或24 小时制；4 年日历
- ◆ 按键一次可校正 ± 30 秒时间误差
- ◆ 振荡器内置电容
- ◆ 可直接驱动压电蜂鸣片
- ◆ 七脚为内置三极管输出端
- ◆ EL 背光控制输出

ELECTRICAL CHARACTERISTICS 电特性

Characteristics	Sym	Min	Typ	Max	Unit	Remarks
Operating Voltage 工作电压	V _{DD}	1.2	1.5	1.8	V	-
Quiescent Current 静态电流	I _{SB}	-	-	2	uA	No Load
Operating Current 工作电流	I _{DD}	-	6	9	mA	EL
Input High Voltage 输入高电压	V _{IH}	V _{DD} -0.3	-	V _{DD}	V	-
Input Low Voltage 输入低电压	V _{IL}	V _{SS}	-	V _{SS} +0.3	V	-
Swich Activation Current 按键开关消耗电流	I _{SW}	-	-	4	uA	V _{IN} =V _{DD}
Oscillating Start Voltage 振荡起振电压	V _{OSC}	-	-	1.2	V	Within 3 seconds
Alarm Drive Current 闹铃驱动电流	I _{ALA}	5	10	-	mA	V _{ALA} =0.5V
Oscillation Frequency 振荡频率	F _{OSC}	-	32768	-	Hz	-
LCD Frequency LCD 频率	F _{LCD}	-	32	-	Hz	-
Time Stability 时间稳定度	T _{stb}	-	1	-	ppm	V _{DD} =3.0V
Output Current for IND, DIS 输出电流	I _o	1	-	-	mA	V _o =0.5V

APPLICATION INFORMATION 应用数据

Pad No	Name	Pad No	Name	Pad No	Name	Pad No	Name
1	OSCI	13	C1/B1	25	FIK	37	F5/E5
2	OSCO	14	A2/MON	26	CAP	38	B4/C4
3	L	15	CL/TUE	27	T2	39	G4/D4
4	DIS	16	A3/WED	28	VSS2	40	F4/E4
5	IND	17	A4/THU	29	AC	41	B3/C3
6	VSS1	18	A5/FRI	30	T1	42	G3/D3
7	ALB	19	A6/SAT	31	VDD	43	F3/E3
8	ALA1	20	DATE/AL	32	B6/C6	44	B2/C2
9	M	21	COM1	33	G6/D6	45	G2/D2
10	COM2	22	T3	34	F6/E6	46	F2/E2
11	PM/AM	23	D	35	B5/C5		
12	ADEG1/SUN	24	S	39	G5/D5		

- 注：
1. 芯片底部接VDD
 2. T1 到T3 及AC 为测试端
 3. COM & SEGMENT to LCD 的顺序与VS5290AQ 完全相同

SETTING SEQUENCE AND SWITCH OPERATION

