



## TSic<sup>™</sup> 106 Rapid Response, Low-Cost Temperature Sensor IC with Digital Output

#### Data Sheet

#### TSic<sup>™</sup> 106 Features

- Low cost, precision temperature sensor
- Single-wire 11-bit digital serial signal output
- Communication range > 10 meters
- Resolution: 0.1°C
- Accuracy: ±0.5°C at room temperature; ±1.0°C over span of 40°C
- Wide measurement range: –50 to +150 °C
- Signal read-out every 0.1s (other rates available on request)
- Supply Voltage 3.0V to 5.5V
- Package: 8-pin SOIC, 3-pin e-line, Chip on Flex, Die on Wafer
- Low quiescent current to minimize self-heating and power consumption (45µA typ.)
- System-on-chip based on advanced mixedsignal CMOS technology integrating precision temperature sensing bandgap reference with proportional-to-absolute-temperature (PTAT) output, digital signal processor (DSP) core, and electrically erasable memory (EEPROM)

#### TSic™ Family

The TSic<sup>™</sup> temperature sensor IC family are fully tested and calibrated sensors with absolute measurement accuracy on delivery – no further calibration needed. The TSic<sup>™</sup> combines outstanding accuracy with long term stability, yet it is very simple to use.

The TSic<sup>™</sup> series is specifically designed for high performance, cost-effective solutions for sensing temperature in building automation, automotive, industrial, office automation, white goods and low-power/mobile applications.

TSic<sup>™</sup> employs a high precision bandgap reference with PTAT output; a low-power, precision ADC; and an on-chip DSP core with EEPROM to precisely calibrate the output temperature signal. The TSic<sup>™</sup> series includes ICs with two linear analog signal output options, such as standard 0~1Vout (Supply voltage (V+) = 3.0V to 5.5V) or ratiometric (10~90% of supply voltage); or the digital serial output signal for interfacing with microcontrollers.

#### **Benefits**

- Several accuracy classes available with 100% upward compatibility
- No calibration by customer needed; absolute calibration specified
- Simple to integrate, reducing cost and time for application-development
- Fast data measurement optimal for temperature control
- Packages for standard SMD, THT or application specific assembly
- Miniaturized solutions with Bare-Chip (COB, COF, CSP\*) or e-line packages – very fast response time for COF
- Very low power consumption ideal for mobile and standard applications
- Field reconfiguration/recalibration option available (high volume customers only)
- Outstanding long term stability

\* COB: Chip-On-Board; COF: Chip-On-Flex; CSP: Chip Scale Packaging

#### **Application Support**

For TSic<sup>TM</sup> evaluation ZMD provides a special Evaluation Tool. (Ordering Code: TSic Lab Kit) Further application support is available through the hotline: *email*: <u>tsic@zmd.de</u>

Phone: +49 351 8822-916

TSic<sup>™</sup> 106 Data Sheet, Rev.3.5, August 9, 2006

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#### **Absolute Maximum Ratings**

PARAMETER	MIN	MAX	UNITS
Supply Voltage (V+)	-0.3	6.0	V
Voltages at analog I/O – Pins (V <sub>INA</sub> , V <sub>OUTA</sub> )	-0.3	V <sub>DDA</sub> +0.3	V
Storage Temperature Range (T <sub>stor</sub> )	-50	150	°C

#### **Operating Conditions**

PARAMETER	MIN	TYP	MAX	UNITS
Supply <sup>1</sup> Voltage to Gnd (V+)	3.0	5.0	5.5	V
Supply Current $(I_{V+})^2$ @ V+ = 3.3V, RT	30	45	80	μA
Ambient Temperature <sup>3</sup> Range (T <sub>amb</sub> )	-50		150	°C
External Capacitance between V+ and $Gnd^4$ (C <sub>V+</sub> )	80	100	470	nF
Output Load Capacitance (C <sub>L</sub> )			15	nF
Output Load Resistance between signal and Gnd (or V+)	1			MΩ

#### **Temperature Accuracy<sup>5</sup>**

PARAMETER	MIN	TYP	MAX	UNITS
Wide Range Device for -50° to 150°C				
T1 At room temperature	-0.5	±0.3	0.5	°C
T2 +0°C to +40°C T2	-1.0		+1.0	°C
T3 -50 to 0, +40 to 150°C		+1.5		°C

- <sup>1</sup> With supply voltage 2.7V 3.0V accuracy reduced.
- <sup>2</sup> Without load
- <sup>3</sup> Output signal is limited to this ambient temperature ±3°C (with regard to calibration, offset and gain)
- <sup>4</sup> Recommended as close to TSic V+ and Gnd-Pins as possible

<sup>5</sup> Accuracy = specification plus quantization error of 1 bit (0.1°C), 2g value.

Other TSic products with customer specific calibration available on request: i.e. with special calibration where the 80°C span (bandgap) with the high precision temperature range of  $\pm 0.5$  °C is shifted to another (lower or higher) temperature range. Temperature range limits T1, T2:  $\pm 0.1$ °C; T3:  $\pm 3$ °C

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#### Output Examples for TSic<sup>™</sup> Devices

		Temperature Measurement Range -50°C to 150°C or -58°F to 302°F (Wide Range Device)	
		TSic-106	
Temp (°C)	Temp (°F)	Digital	
-50	-58	0×000	
-10	14	0x199	
0	32	0x200	
25	77	0x2FF	
60	140	0x465	
125	257	0x6FE	
150	302	0x7FF	

Temperature =  $(Digital Signal / 2047 * 200 - 50)^{\circ}C$ 

#### Lifetime for TSic<sup>™</sup> Devices

TSic<sup>™</sup> device lifetime is dependent upon its operating temperature.

Operating Temperature	expected Lifetime
140°C150°C	min. 1500h
125°C140°C	min. 3000h

#### Package Information

TSic<sup>™</sup> 106 SOP8: 150mil, Standard SMT Package, SOIC, Based on IEC 191-2Q, Type 076E35 B. Other packages available on demand: TSic<sup>™</sup> 106 e-line; 3 pin THT package; Chip on Flex; TSic<sup>™</sup> 106 wafer level.

For further information see also Technical Note:

"TSic™ Die and Package Specifications for TSic<sup>™</sup> Temperature Sensor IC"

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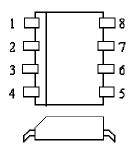
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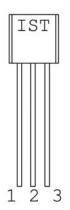
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#### SOP8 Package



Pin	Name	Description
1	V+	Supply voltage (3.0-5.5V)
2	Signal	Temperature output signal
4	GND	Ground
3, 5-8	TP/NC	Test pin / NC Do not connect

#### E-Line Package



Pin	Name	Description
1	GND	Ground
2	Signal	Temperature output signal
4	V+	Supply Voltage (3.0-5.5V)

#### Related products and ordering information

For related products and ordering information see <u>www.zmd.biz</u> and ZMD "*TSic<sup>™</sup> Ordering Guide*".

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