

TOSHIBA Field Effect Transistor Silicon P Channel MOS Type (U-MOS IV)

TPC8111

Lithium Ion Battery Applications Notebook PC Applications Portable Equipment Applications

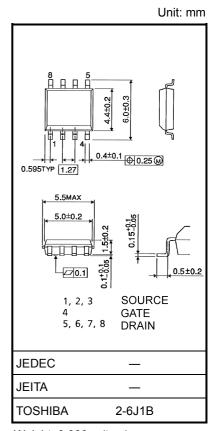
- Small footprint due to small and thin package
- Low drain-source ON resistance: $RDS(ON) = 8.1 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance: $|Y_{fs}| = 23 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = -10 \mu A \text{ (max) (V}_{DS} = -30 \text{ V)}$
- Enhancement-mode: $V_{th} = -0.8 \text{ to } -2.0 \text{ V (V}_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA})$

Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	-30	V
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V_{DGR}	-30	V
Gate-source voltage		V _{GSS}	±20	V
Drain current	DC (Note 1)	I _D	-11	Α
Diam current	Pulse (Note 1)	I_{DP}	-44	
Drain power dissipatio	n (t = 10 s) (Note 2a)	P_{D}	1.9	W
Drain power dissipation (t = 10 s) (Note 2b)		P _D	1.0	W
Single pulse avalanche energy (Note 3)		E _{AS}	31.5	mJ
Avalanche current		I _{AR}	-11	Α
Repetitive avalanche energy (Note 2a) (Note 4)		E _{AR}	0.19	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C

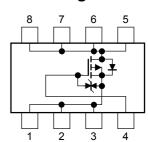
Note: For (Note 1), (Note 2), (Note 3) and (Note 4), please refer to the next page.

This transistor is an electrostatic sensitive device. Please handle with caution.



Weight: 0.080 g (typ.)

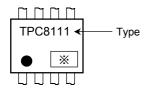
Circuit Configuration



Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to ambient (t = 10 s) (Note 2a)	R _{th (ch-a)}	65.8	°C/W
Thermal resistance, channel to ambient (t = 10 s) (Note 2b)	R _{th (ch-a)}	125	°C/W

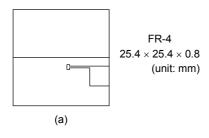
Marking (Note 5)

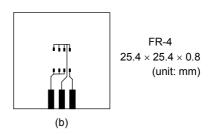


Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2:

(a) Device mounted on a glass-epoxy board (b) Device mounted on a glass-epoxy board (b)





Note 3: $V_{DD} = -24~V$, $T_{ch} = 25^{\circ}C$ (initial), L = 0.2~mH, $R_G = 25~\Omega$, $I_{AR} = -11~A$

Note 4: Repetitive rating: pulse width limited by maximum channel temperature

Note 5: • on lower left of the marking indicates Pin 1.

** shows lot number. (year of manufacture: last decimal digit of the year of manufacture, month of manufacture: January to December are denoted by letters A to L respectively.)

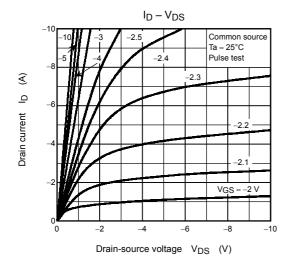
Electrical Characteristics (Ta = 25°C)

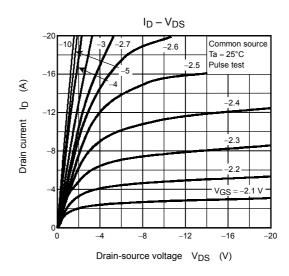
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Gate leakage curren	t	I _{GSS}	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ	
Drain cut-OFF curre	nt	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μА	
Drain-source breakdown voltage		V _{(BR)DSS}	$I_D = -10$ mA, $V_{GS} = 0$ V	-30	_	_	V	
Diaiii-Source breakd	own voltage	V _{(BR)DSX}	$I_D = -10$ mA, $V_{GS} = 20$ V	-15 — —				
Gate threshold volta	ate threshold voltage		$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.8	_	-2.0	V	
Drain-source ON resistance		R _{DS (ON)}	$V_{GS} = -4 \text{ V}, I_D = -5.5 \text{ A}$		12	18	- mΩ	
			$V_{GS} = -10 \text{ V}, I_D = -5.5 \text{ A}$		8.1	12		
Forward transfer adr	mittance	Y _{fs}	$V_{DS} = -10 \text{ V}, I_D = -5.5 \text{ A}$	11	23		S	
Input capacitance		C _{iss}		_	5710	_		
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	560	_	pF	
Output capacitance		Coss		_	590	_		
	Rise time	t _r	VGS -10 V		18		- ns	
Reverse transfer capa	Turn-ON time	t _{on}		_	23	_		
	Fall time	t _f		_	109	_		
	Turn-OFF time	t _{off}	$V_{DD} \simeq -15 \text{ V}$ Duty \leq 1%, $t_W = 10 \text{ μs}$	_	396	_		
Total gate charge (gate-source plus gate-drain)		Qg	$V_{DD} \simeq -24 \text{ V}, V_{GS} = 10 \text{ V},$	_	107	_		
Gate-source charge 1		Q _{gs1}	$I_D = -11 \text{ A}$	_	12	_	nC	
Gate-drain ("miller")	charge	Q _{gd}		_	20	_		

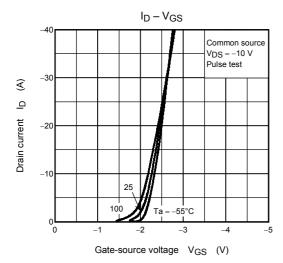
Source-Drain Ratings and Characteristics (Ta = 25°C)

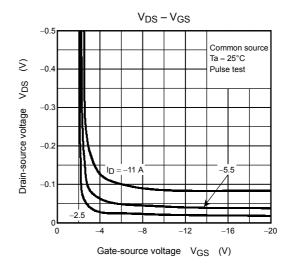
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current Pulse	(Note 1)	I _{DRP}	_	_	_	-44	Α
Forward voltage (diode)		V_{DSF}	$I_{DR} = -11 \text{ A}, V_{GS} = 0 \text{ V}$			1.2	V

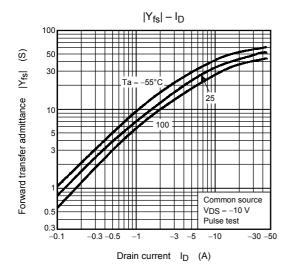
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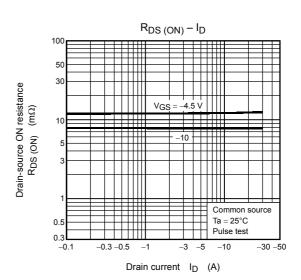




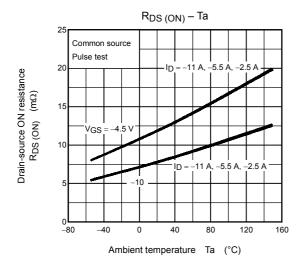


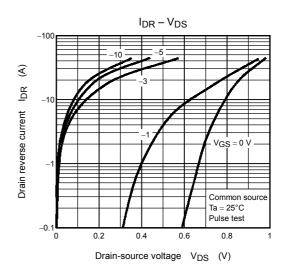


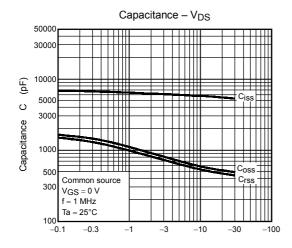


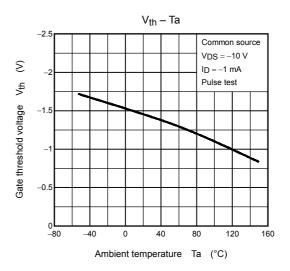


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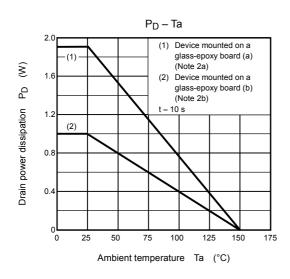


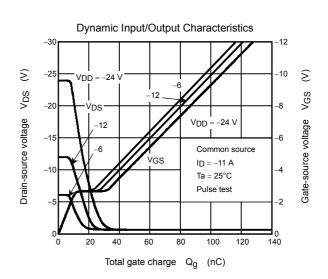




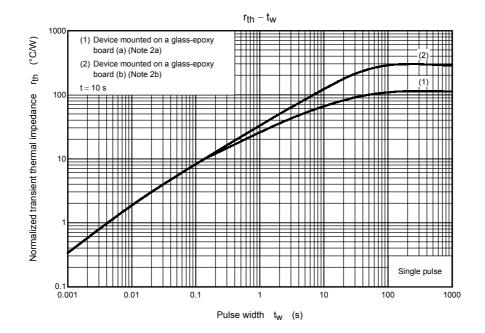


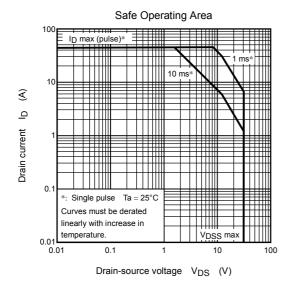
Drain-source voltage V_{DS} (V)





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