

TOSHIBA CMOS Integrated Circuit Silicon Monolithic

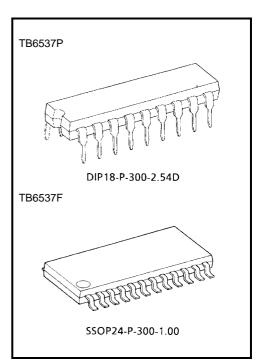
TB6537P,TB6537F

3-Phase Full-Wave Sensorless Controller for Brushless DC Motors

TB6537P/F is a 3-phase full-wave sensorless controller for brushless DC motors. It is capable of controlling voltage by PWM signal input. When combined with various drive circuits it can be used for various types of motors.

Features

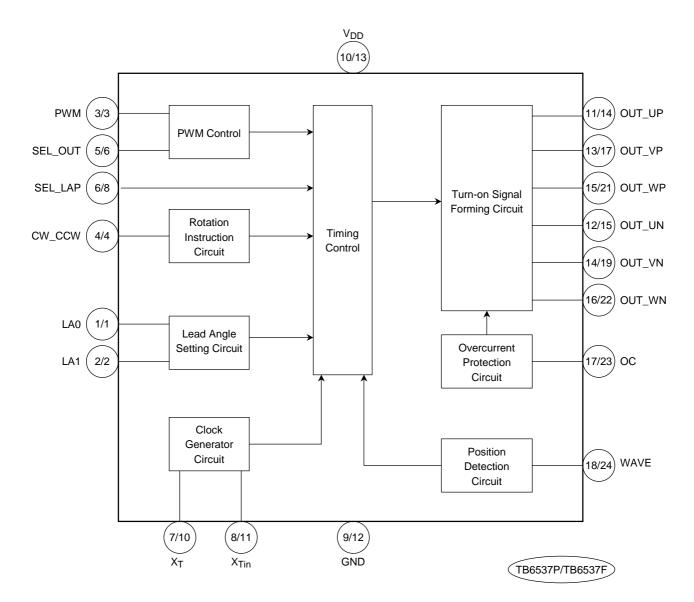
- 3-phase full-wave sensorless drive
- PWM control (PWM signal is supplied from external sources.)
- Turn-on signal output current: 20 mA
- Overcurrent protection function
- Forward/reverse modes
- Lead angle control function (0, 7.5, 15 and 30 degrees)
- Built-in lap turn-on function
- Two types of PWM output (upper PWM and upper/lower alternate PWM)



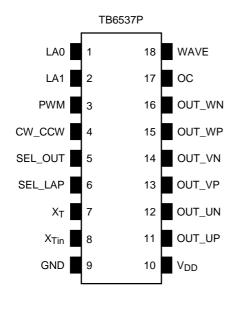
Weight

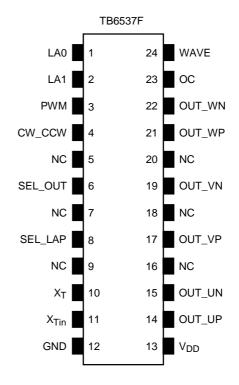
DIP18-P-300-2.54D: 1.47 g (typ.) SSOP24-P-300-1.00: 0.32 g (typ.)

Block Diagram



Pin Assignment





Pin Description

Pin No.				Description			
TB6537P	TB6537F	Symbol	I/O	Description			
1	1	LA0	ļ	 Lead angle setting signal input pin LA0 = Low, LA1 = Low: Lead angle 0 degree LA0 = High, LA1 = Low: Lead angle 7.5 degree 			
2	2	LA1	I	 LA0 = Low, LA1 = High: Lead angle 15 degree LA0 = High, LA1 = High: Lead angle 30 degree Built-in pull-down resistor 			
3	3	PWM	I	 PWM signal input pin Inputs Low-active PWM signal Built-in pull-up resistor Disables input of duty-100% (Low) signal High for 250 ns or longer is required. 			
4	4	cw_ccw	I	 Rotation direction signal input pin High: Reverse (U → W → V) Low, Open: Forward (U → V → W) Built-in pull-down resistor 			
—	5	NC	_	Not connected			
5	6	SEL_OUT	I	 Pin to select the synthesis method of burn-in signal and PWM signal Low: Upper PWM High: Upper/Lower alternate PWM Built-in pull-down resistor 			
—	7	NC		Not connected			
6	8	SEL_LAP	I	Lap turn-on select pin Low: Lap turn-on High: 120 degrees turn-on Built-in pull-up resistor 			
_	9	NC	_	Not connected			
7	10	X _T	_	Resonator connecting pin			
8	11	X _{Tin}	_	• Selects starting commutation frequency. Starting commutation frequency f_{st} = Resonator frequency $f_{xt}/(6 \times 2^{17})$			
9	12	GND		Connected to GND.			
10	13	V _{DD}	_	Connected to 5-V power supply.			
11	14	OUT_UP	0	 U-phase upper turn-on signal output pin U-phase winding wire positive ON/OFF switching pin ON: Low, OFF: High 			
12	15	OUT_UN	ο	 U-phase lower turn-on signal output pin U-phase winding wire negative ON/OFF switching pin ON: High, OFF: Low 			
—	16	NC		Not connected			
13	17	OUT_VP	0	 V-phase upper turn-on signal output pin V-phase winding wire positive ON/OFF switching pin ON: Low, OFF: High 			
—	18	NC	_	Not connected			
14	19	OUT_VN	0	 V-phase lower turn-on signal output pin V-phase winding wire negative ON/OFF switching pin ON: High, OFF: Low 			

Pin	Pin No.		I/O	Description			
TB6537P	TB6537F	Symbol	¹ O	Description			
	20	NC	_	ot connected			
15	21	OUT_WP	0	 W-phase upper turn-on signal output pin W-phase winding wire positive ON/OFF switching pin ON: Low, OFF: High 			
16	22	OUT_WN	0	 W-phase lower turn-on signal output pin W-phase winding wire negative ON/OFF switching pin ON: High, OFF: Low 			
17	23	ос	I	 Overcurrent signal input pin High on this pin can put constraints on the turn-on signal which is performing PWM control. Built-in pull-up resistor 			
18	24	WAVE	I	Positional signal input pinInputs majority logic synthesis signal of three-phase pin voltage.Built-in pull-up resistor			

Functional Description

1. Sensorless Drive

On receipt of PWM signal start instruction turn-in signal for forcible commutation (commutation irrespective of the motor's rotor position) is output and the motor starts to rotate. The motor's rotation causes induced voltage on winding wire pin for each phase.

When signals indicating positive or negative for pin voltage (including induced voltage) for each phase are input on respective positional signal input pin, the turn-on signal for forcible commutation is automatically switched to turn-on signal for positional signal (induced voltage).

Thereafter turn-on signal is formed according to the induced voltage contained in the pin voltage so as to drive the brushless DC motor.

2. Starting commutation frequency (resonator pin and counter bit select pin)

The forcible commutation frequency at the time of start is determined by the resonator's frequency and the number of counter bit (within the IC).

Starting commutation frequency f_{st} = Resonator frequency $f_{xt}/(6 \times 2^{(bit + 3)})$ bit = 14

The forcible commutation frequency at the time of start can be adjusted using inertia of the motor and load.

• The forcible commutation frequency should be set higher as the number of magnetic poles increases.

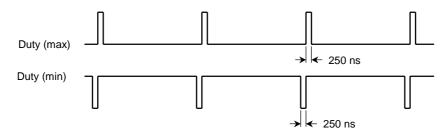
• The forcible commutation frequency should be set lower as the inertia of the load increases.

3. PWM Control

PWM signal can be reflected in turn-on signal by supplying PWM signal from external sources.

The frequency of the PWM signal shoud be set adequately high with regard to the electrical frequency of the motor and in accordance to the switching characteristics of the drive circuit.

Because positional detection is performed in synchronization with the rising edges of PWM signal, positional detection cannot be performed with 0% duty or 100% duty.

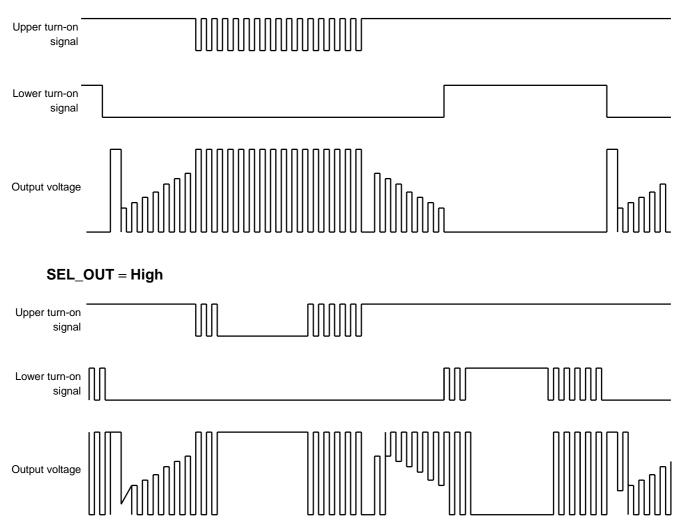


The voltage applied to the motor is duty 100% because of the storage time of the drive circuit even if the duty is 99%.

4. Selecting PWM Output Form

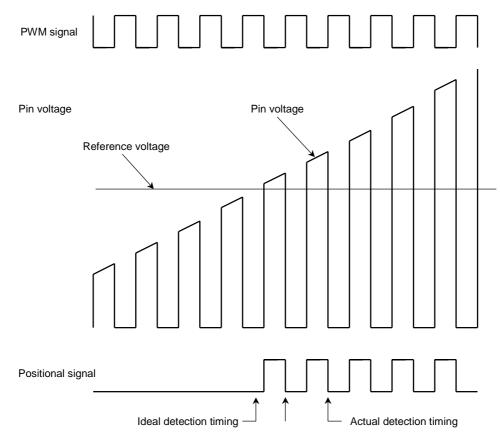
PWM output form can be selected using SEL_OUT.





5. Positional Variation

Since positional detection is performed in synchronization with PWM signal, positional variation occurs in connection with the frequency of PWM signal. Be especially careful when the IC is used for high-speed motors.



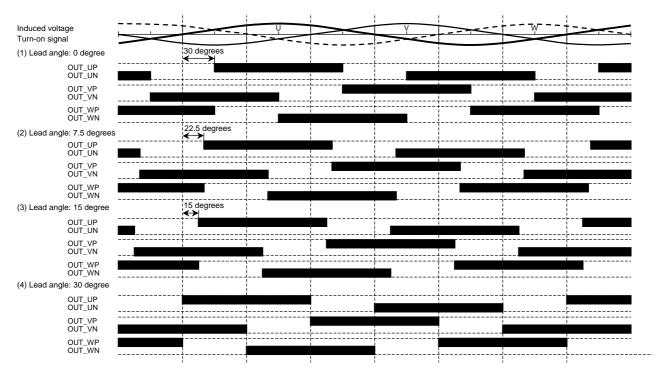
 $\label{eq:Variation} \begin{array}{ll} \mbox{Variation is calculated by detecting at two consecutive rising edges of PWM signal.} \\ 1/f_p < \mbox{Detection time variation} < 2/f_p & f_p \end{tabular} \begin{array}{ll} \mbox{PWM signal.} \\ \mbox{Figure 1} \end{array}$

6. Overcurrent protection function

An active phase which controls PWM is turned off by the rising-edge of the OC signal. The inactive phase is turned on by the timing of the next PWM signal.

7. Lead Angle Control

The lead angle is 0 degree during the starting forcible commutation and when normal commutation is started, automatically changes to the lead angle which has been set using LA0 and LA1. However, if both LA0 and LA1 are set for High, the lead angle is 30 degrees in the starting forcible commutation as well as in normal commutation.



8. Lap Turn-on Control

When SEL_LAP = High, the turn-on degree is 120 degrees. When SEL_LAP = Low, Lap Turn-on Mode starts.

In Lap Turn-on Mode, the time between zero-cross point and the 120 degrees turn-on timing becomes longer (shaded area in the below chart) so as to create some overlap when switching turn on signals. The lap time differs depending ong the lead angle setting.

duced voltage		+			V		+	+
urn-on signal	\rightarrow						VV	
) Lead angle: 0 degree								
OUT_UP			i 				·	
OUT_VP OUT_VN		· *	·					
OUT_WP OUT_WN		+						
) Lead angle: 7.5 degrees								
OUT_UP OUT_UN		8888						
OUT_VP OUT_VN	883	-†	· · · · · · · · · · · · · · · · · · ·					
OUT_WP OUT_WN						, xxxx		
) Lead angle: 15 degree								
OUT_UP OUT_UN) }						
OUT_VP		·†						
OUT_WP OUT_WN								
) Lead angle: 30 degree								
OUT_UP OUT_UN		 			_		·····	
OUT_VP OUT_VN		 						
		1		- I.	_!	_ !		

9. Start/Stop Control

Start/Stop is controlled using PWM signal input pin.

A stop is acknowledged when PWM signal duty is 0, and a start is acknowledged when ON-signal of a frequency 4 times higher than the resonator frequency or even higher is input continuously.

Timing chart

PWM signal			
Detection timing			
Start		•	
512 periods at the resonator frequency	First detection	↑ Second detection	Start
PWM signal Detection timing			
Stop			
512 periods at the resonator frequency		First detection	Second detection and stop

Note: Take sufficient care for noise on PWM signal input pin.

Maximum Ratings (Ta = 25°C)

Characteristics	Symbol	Rating		Unit	
Power supply voltage	V _{DD}	5.5		V	
Input voltage	V _{in}	-0.3 to V _{DD} + 0.3		V	
Turn-on signal output current	IOUT	20		mA	
Rower discipation	PD	TB6537P	1.25	W	
Power dissipation	۲D	TB6537F	0.59	vv	
Operating temperature	T _{opr}	-30 to 85		°C	
Storage temperature	T _{stg}	-55 to 150		°C	

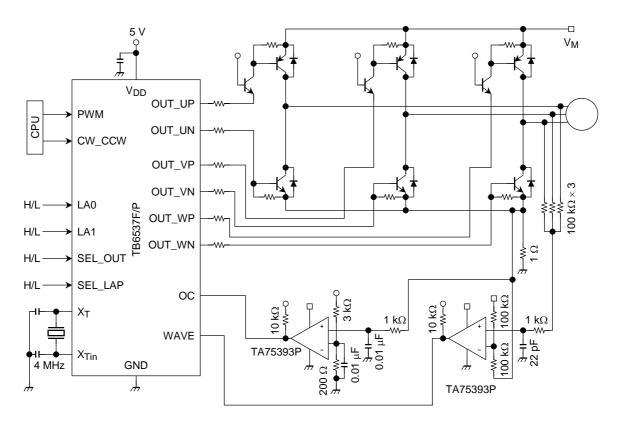
Recommended Operating Conditions (Ta = -30 to 85° C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Power supply voltage	V _{DD}	—	4.5	5.0	5.5	V
Input voltage	V _{in}	_	-0.3		V _{DD} + 0.3	V
PWM frequency	f _{PWM}	—	_	16	_	kHz
Oscillation frequency	f _{osc}	_	1.0		10	MHz

Electrical Characteristics (Ta = 25° C, V_{DD} = 5 V)

Characteristics	Symbol	Test Circuit	Test Condition	Min	Тур.	Max	Unit
Static power supply current	I _{DD}	—	PWM = H, X _{Tin} = H	_	0.1	0.3	mA
Dynamic power supply current	I _{DD (opr)}	—	PWM = 50% Duty, X _{Tin} = 4 MHz	_	1	3	mA
	I _{IN-1} (H)	_	V _{IN} = 5 V, PWM, OC, WAVE_U, SEL_LAP	_	0	1	
Input ourrent	I _{IN-1} (L)	_	V _{IN} = 0 V, PWM, OC, WAVE_U, SEL_LAP	-75	-50	_	
Input current	I _{IN-2} (H)	_	V _{IN} = 5 V, CW_CCW, LA0, LA1, SEL_OUT	_	50	75	μΑ
	I _{IN-2} (L)	_	V _{IN} = 0 V, CW_CCW, LA0, LA1, SEL_OUT	-1	0	_	
	V _{IN} (H)	_	PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1, SEL_OUT	3.5 — 5		5	
Input voltage	V _{IN} (L)	_	PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1, SEL_OUT	GND		1.5	V
Input hysteresis voltage	V _H	_	PWM, OC, SEL_LAP, CW_CCW WAVE_U, LA0, LA1, SEL_OUT	_	0.6		V
	V _{O-1} (H)	_	I _{OH} = -1 mA OUT_UP, OUT_VP, OUT_WP	4.3		V _{DD}	v
Ou double up ho no	V _{O-1} (L)	_	I _{OH} = 20 mA OUT_UP, OUT_VP, OUT_WP	GND		0.5	
Output voltage	V _{O-2} (H)	_	I _{OH} = -20 mA OUT_UN, OUT_VN, OUT_WN	4.0		V _{DD}	V
	V _{O-2} (L)	_	I _{OH} = 1 mA OUT_UN, OUT_VN, OUT_WN	GND	GND —	0.5	1
Output look summert	IL (H)	_	V _{DD} = 5.5 V, V _{OUT} = 0 V OUT_UP, OUT_VP, OUT_WP OUT_UN, OUT_VN, OUT_WN	_	0	10	
Output leak current	۱ _۲ (۲)	_	V _{DD} = 5.5 V, V _{OUT} = 5.5 V OUT_UP, OUT_VP, OUT_WP OUT_UN, OUT_VN, OUT_WN	_	0	10	μA
Output delay time	t _{pLH}		PWM-Output	_	0.5	1	
Output delay lime	t _{pHL}] _	PWM-Output		0.5	1	μS

Application Circuit Example

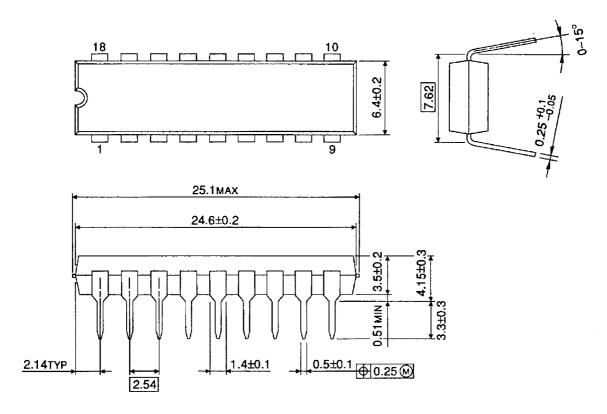


- Note 1: Take enough care in designing output V_{DD} line and GND line to avoid short circuit between outputs, V_{DD} fault or GND fault which may cause the IC to break down.
- Note 2: The above application circuit and values mentioned are just an example for reference. Since the values may vary depending on the motor to be used, appropriate values must be determined through experiments before using the device.

Package Dimensions

DIP18-P-300-2.54D

Unit : mm

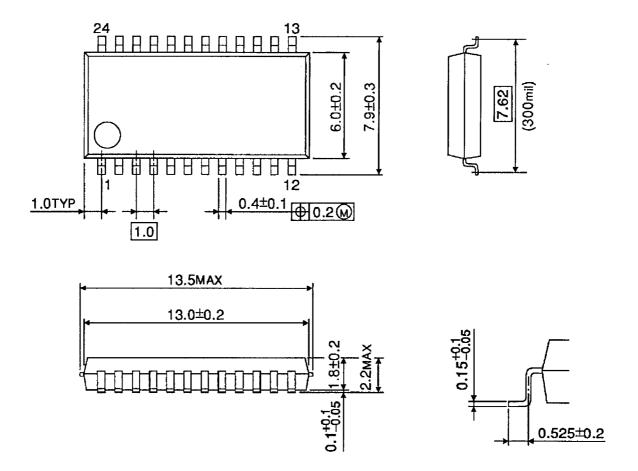


Weight: 1.47 (typ.)

Package Dimensions

SSOP24-P-300-1.00

Unit : mm



Weight: 0.32 (typ.)

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Handbook" etc..

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