

No.4316

2SJ306

P-Channel MOS Silicon FET

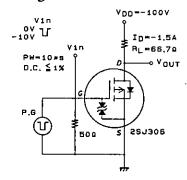
Very High-Speed Switching Applications

Features

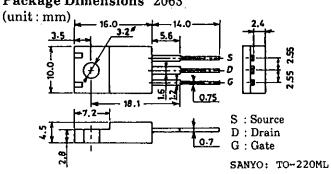
- · Low ON resistance.
- Very high-speed switching.
- · Low-voltage drive.
- · Micaless package facilitating mounting.

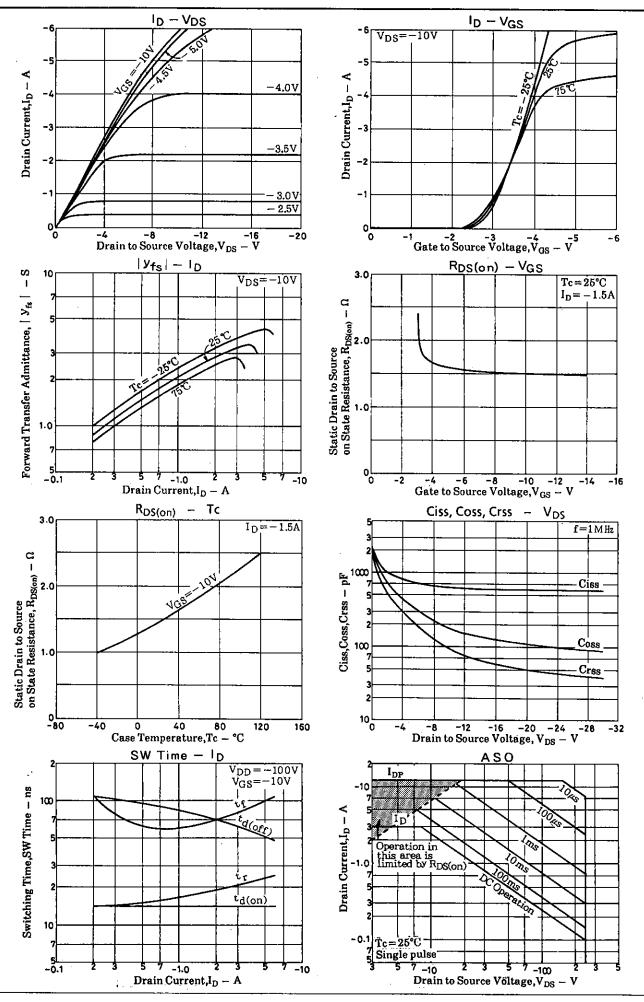
Absolute Maximum Ratings at Ta = 25°C					unit	
Drain to Source Voltage	V_{DSS}		-250		V	
Gate to Source Voltage	V_{GSS}^{-33}		=	£30	V	
Drain Current(DC)	ID			-3	Α	
Drain Current(Pulse)	I_{DP}	PW≦10μs, duty cycle≦1%	_	-12	Α	
Allowable Power Dissipation	$P_{\mathbf{D}}$			2.0	W	
*	D	$Tc = 25^{\circ}C$		25	w	
Channel Temperature	Tch			150	$^{\circ}\mathrm{C}$	
Storage Temperature	Tstg		-55 to +150		°C	
Electrical Characteristics at Ta=			min	typ	max	unit
D-S Breakdown Voltage		$I_D = -1 \text{mA}, V_{GS} = 0$	-250			V
G-S Breakdown Voltage	$V_{(BR)GSS}$	$I_{G} = \pm 100 \mu A, V_{DS} = 0$	± 30			V
∫Zero Gate Voltage	$I_{ m DSS}$	$V_{\rm DS} = -250 \rm V, V_{\rm GS} = 0$			-100	μ A
Drain Current						•
Gate to Source Leakage Current	I_{GSS}	$V_{GS} = \pm 25V, V_{DS} = 0$			±10	μ A
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = -10V, I_D = -1mA$	-1.5		-2.5	·V
Forward Transfer Admittance	y _{fs}	$V_{DS} = -10V, I_D = -1.5A$	1.5	2.5		S
Static Drain to Source	$R_{DS(on)}$	$I_D = -1.5A, V_{GS} = -10V$		1.5	2.0	Ω
on State Resistance	, ,					
Input Capacitance	Ciss	$V_{DS} = -20V, f = 1MHz$		600		\mathbf{pF}
Output Capacitance	Coss	$V_{DS} = -20V_{f} = 1MHz$		110		pF
Reverse Transfer Capacitance	Crss	$V_{DS} = -20V, f = 1MHz$		50		рF
Turn-ON Delay Time	$\mathbf{t_{d(on)}}$	See specified Test Circuit.		14		ns
Rise Time	tr	- "		18		ns
Turn-OFF Delay Time	$t_{d(off)}$	"		75		ns
Fall Time	$\mathbf{t_f}$	"		65		ns
Diode Forward Voltage	v_{SD}	$I_{S} = -3A, V_{GS} = 0$			-1.5	V

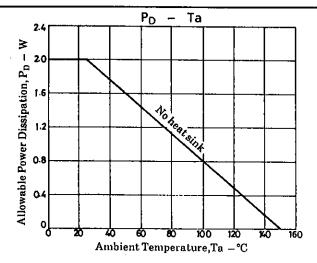
Switching Time Test Circuit

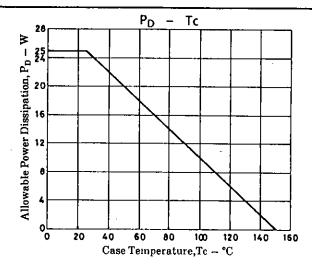


Package Dimensions 2063









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