

MSP430 Gang Programmer (MSP-GANG430)

User's Guide

2006

MSP430

SLAU101F

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Preface

Read This First

About This Manual

This user's guide documents the MSP430 gang programmer MSP–GANG430.

How to Use This Manual

This document contains the following chapters:

- □ Chapter 1 Introduction, Installation, and Setup
- □ Chapter 2 Operation
- □ Chapter 3 Firmware
- Chapter 4 Dynamic Link Library GANG430.DLL
- □ Chapter 5 Hardware
- □ Chapter 6 Schematics

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Contents

1	Intro	duction,	Installation, and Setup	1-1
	1.1	Introdu		1-2
	1.2	Softwa		1-3
	1.3	Hardwa		1-3
2	Oper	ation		2-1
-	21	Progra	mming MSP430 Flash Devices Using the GUI	2-2
	2.1	211		2-2
		212	Description of the MSP-GANG430 GUI	2-4
		213	Status Messages	2-5
		214	Frror Messages	2-6
		215	Description of the Gang430 ini File	2-6
		216	Target Connector Functional Check	2-8
	22	Progra	mming MSP/30 Flash Devices in Standalone Mode	2-0
	2.2	Progra	mming MSP 430 Flash Devices With User-Configured HART Handler	2-3
	2.5	Progra	mining the MSP430 Plash Devices With CANC420 DLL	2-3
	2.4	Fillyia		2-10
3	Firm	ware		3-1
	3.1	Comma	ands	3-2
	3.2	Firmwa	are Interface Protocol	3-2
	3.3	Synchr	onization Sequence	3-2
	3.4	Data F	rame	3-2
		3.4.1	Frame Structure	3-3
		3.4.2	Checksum	3-3
	3.5	Comma	ands—Detailed Description	3-4
		3.5.1	General	3-4
		3.5.2	Load Parameters	3-4
		3.5.3	Start	3-5
		3.5.4	Transmit Diagnostic	3-5
		3.5.5	Erase Image	3-5
		3.5.6	Load Image Block	3-6
		3.5.7	Write Target Selective	3-6
		3.5.8	Read Target Selecive	3-6
		3.5.9	Set Target VCC	3-6
		3.5.10	Load Image Checksum	3-7
		3.5.11	Select Baud Rate	3-8
		3.5.12	Execute Self Test	3-9
		3.5.13	Set Signals	3-9
		3.5 14	Firmware Commands	3-9
		0.0.14		00

4	Dyna	mic Link Library GANG430.DLL	4-1
	4.1	GANG430.DLL Description	4-2
	4.2	Return Values/Error Codes From the GNAG430.DLL	4-8
5	Hard	ware	5-1
	5.1	Specifications	5-2
	5.2	Programming Times vs Code Size for the Gang Programmer	5-2
	5.3	Recommendations for Target Connections	5-3
	5.4	MSP-GANG430 Target Connector Signals	5-3
	5.5	MSP-GANG430 Schematics	5-7
	5.6	MSP-GANG430 Component Locations	5-8
	5.7	Gang_Exp Target Expansion Board Layout	5-9
6	Sche	matics	6-1
Α	Intel I	Hex Object Format	A-1
	A.1	Intel Hex Object Format	A-2
	A.2	TI-TXT File Format	A-3

Figures

1–1	MSP-GANG430 Gang Programmer	1-4
2–1	MSP-GANG430 GUI	2-3
5–1	25-Pin Sub-D at the MSP-GANG430	5-4
5–2	14-Pin Connector at the End of the Interconnect Cable	5-4
5–3	Signal Connections for 4-Wire JTAG Communication	5-6
5–4	Signal Connections for 2-Wire JTAG Communication (Spy-Bi-Wire)	5-7
5–5	MSP-GANG430 Component Locations	5-8
5–6	Gang_Exp Layout	5-9
A–1.	Intel Hex Object Format	A-2

Tables

2–1	Function Buttons and Descriptions	2-4
2–2	Status Messages	2-5
2–3	Error Messages	2-6
2–4	Control Bit Definitions for Target Connector Pins	2-8
3–1	Data Frame of Firmware Commands	3-3
5–1	MSP-GANG430 Hardware Specifications	5-2
5–2	MSP-GANG430 Target Connector Signal Functions	5-5
5–3	MSP-GANG430 Signal Levels	5-5

Chapter 1

Introduction, Installation, and Setup

This chapter introduces the MSP-GANG430 and guides you through the installation of the software and hardware.

TopicPage1.1Introduction1-21.2Software Installation1-31.3Hardware Installation1-3

1.1 Introduction

The MSP-GANG430 is an MSP430 device programmer that can program up to eight MSP430 flash devices at the same time. The MSP-GANG430 connects to the host PC using a standard RS232 serial connection. The MSP-GANG430 provides flexible device programming options as described in Chapter 2.

The MSP-GANG430 is not a gang programmer in the traditional sense; there are not eight sockets provided to program the target devices. Instead, the MSP-GANG430 is designed to connect to the target devices in-circuit (i.e., the target devices are mounted in the customer's final circuit/system). The MSP-GANG430 accesses the target devices using connectors to the JTAG signals.

Chapter 6 contains a schematic that documents how the signals from the MSP-GANG430 can be brought out to each of the target devices via an MSP430-standard JTAG connector. The circuit could easily be modified to connect the signals to the target device pins directly (via a socket) if a traditional gang programmer was desired.

The MSP–GANG430 is provided with an expansion board that implements the interconnections between the MSP–GANG430 and multiple target devices. Eight cables are provided that connect the expansion board to eight target devices (via a JTAG connector).

1.2 Software Installation

To install the MSP-GANG430 software:

- Insert the MSP430 CD-ROM into the CD-ROM drive of the computer. The setup routine automatically opens the default browser of the computer and displays the MSP430 start page. Alternatively, open the file *index.htm* located in the root directory of the CD-ROM using a browser. The MSP430 start page is displayed in the browser window.
- 2) Select Tool Software
- 3) Select MSP-GANG430 Gang Programmer
- 4) Select *Save this program to disk* under the *File Download* options and click *OK*. A *Save As* dialog is displayed.
- 5) Using the *Save As* dialog, save the GANG430_Rxxx.exe program to the computer. Make a note of the directory path to this file.
- 6) Navigate to this file (GANG430_Rxxx.exe) and execute it. A welcome message is displayed on the screen.
- 7) The setup program guides you through the installation process. Follow the setup instructions on the screen.
- 8) On completion of the setup program execution, the MSP-GANG430 program icons are created in the selected folder. Click on the *GANG430 Read Me First* icon to obtain important information about the MSP-GANG430 hardware and software.
- 9) The appropriate program group and icons are added to the Windows program manager.
- 10) To start the MSP-GANG430 software, click the *GANG430* icon in the selected program group (the default program group is ADT430).

1.3 Hardware Installation

To install the MSP-GANG430 hardware:

- 1) Connect the MSP-GANG430 to the serial port (COM1–COM15 or extension board) of the PC using the 9-pin SUB-D connector.
- 2) Connect an external power supply to the MSP-GANG430. The voltage of the power supply must be between 9 V and 15 V dc and must be capable of providing a minimum current of 300 mA. The center post of the power supply connector on the MSP-GANG430 is the positive voltage terminal.

The power supply connection status is indicated by the yellow system LED on the MSP-GANG430.

3) The expansion board should be attached to the 25-pin SUB-D connector on the MSP–GANG430. It provides connectivity for up to eight targets, via the included eight 14-pin cables. The target MSP430 flash devices can be in standalone sockets or can be on an application's PCB and are accessed via the JTAG signals.

Note: Maximum Signal Path Length: 60 cm

The maximum length of the signal path between the 25-pin SUB-D connector on the MSP-GANG430 and a target device is 60 cm. If cables between the programmer and targets are longer then 20 cm, serial resistors of 100 Ω should be inserted on signals TCK and TMS on the expansion board. TCK is on pin 4 and TMS is on pin 3 of the 25-pin SUB-D connector.

- 4) The MSP–GANG430 provides the selected power supply voltage V_{CC} (VCC_MSP on pins 7 and 14 of the 25-pin SUB-D target connector; pin 2 on the 14-pin cable) to the target devices in application PCBs. A maximum total current of 150 mA can be provided by the programmer to the targets.
- 5) When an external supply voltage V_{CC} is used for the target systems, it should also be fed into the sense input pin (MSP_VCC_IN on pin 15 of the 25-pin SUB-D target connector; pin 4 on the 14-pin cable) of the programmer. This connection allows the integrated level shifters to match the target JTAG signal levels with the external supply voltage.

When an external supply voltage is used for the target systems, it is very important to disconnect VCC_MSP from the targets to avoid power supply conflicts that could potential damage the MSP-GANG430 and the targets. Connect the external supply voltage to MSP_VCC_IN.

It is not possible to use the MSP-GANG430 with targets powered by the MSP-GANG430 and with targets powered by an external supply simultaneously.

Figure 1–1. MSP-GANG430 Gang Programmer



Chapter 2

Operation

This chapter describes the various methods for programming MSP430 flash devices using the MSP-GANG430.

Topi	C	Page
2.1	Programming MSP430 Flash Devices Using the GUI	2-2
2.2	Programming MSP430 Flash Devices in Standalone Mode	2-9
2.3	Programming MSP430 Flash Devices With User-Configured UAF Handler	RT 2-9
2.4	Programming MSP430 Devices With GANG430.DLL	2-9

2.1 Programming MSP430 Flash Devices Using the GUI

2.1.1 Procedure

The following sequence must be followed to start the GUI and program MSP430 flash devices using the MSP-GANG430:

- 1) Connect the MSP-GANG430 hardware and the targets as suggested in Section 1.3.
- 2) Click on the GANG430 icon located in the program group specified during installation of the software (default: ADT430). The MSP430 FLASH Gang Programmer GUI is displayed on the screen (see Figure 2–1). The status line in the GUI displays the message MSP-GANG430 Gang Programmer connected. If this message is not displayed, check the COM Port selection in the communication settings and the MSP-GANG430 connections.
- 3) Select the required device using the <u>Device Type</u> menu.
- Select the object code file to be programmed into the device(s) using the *File <u>Name</u>* menu. The formats supported for the object code file are TI TXT (.txt) and Intel hex (.a43, .hex, .int).
- 5) Use the <u>Load Image</u> button to download the object code file and its checksum to the MSP-GANG430. The data is written to an internal memory called the image buffer.
- 6) Select the supply voltage using the *Supply Voltage* menu.
- 7) Select the options in *Main Process* as required.
- 8) Click on the <u>Start</u> button in *Main Process* to start the gang programming operation. The progress and completion of the operation is displayed in status. Before the gang programming operation starts, the reset of all targets is performed. The duration of the reset pulse and the delay after reset is specified by "Settling Time". The reset feature is available on hardware V1.30 and later.

Figure 2–1. MSP-GANG430 GUI

MSP430 FLASH Gang Prog	rammer	×
Main Process	Result	
Start	Target # 1 2	3 4 5 6 7 8
Erase	Erase Checked 🔽 🔽	
I♥ Erase Chec <u>k</u>	Verified 🔽 🔽	
Program	Fuse Blown	
l ✓ Verify		
Blow Fuse	Over All	
Result of the Operation: S	ee Result Section.	
Supplied by Programmer	Supply Voltage	Settling Time
✓ Switch Vcc off	3.0 V 💌	0.2 s 💌
Target Program / Device	Device Information RAM: 128 Byte FLAS File Name	H: 2 KByte (+256 Byte)
Load Image	S:\msp430_tools\msp4	30_tools\Projects
Erase / Check Erase Main Memory	🔽 In <u>f</u> o Memory	Calibration data
Maintenance	JTAG	Communication Settings
Category		COM Port
Read out Target	I Use SpyBjWire	
Execute	MSP 430	115200

2.1.2 Description of the MSP-GANG430 GUI

Table 2–1 describes the function buttons and option settings in the MSP-GANG430 GUI.

Button Group	Functions	Description
	Erase	Erase the target's flash memories as selected on the panel Erase/Check Erase
Start – Main Process	Erase check	Check if the target's memories are erased as selected on the panel Erase/Check Erase
function(s) selected	Program	Program the image buffer into the target's flash memory
in Main Process	Verify	Compare the target's flash memory and the image buffer
	Blow fuse	Blow the target's JTAG fuse after successful completion of the selected operations
	Supplied by programmer	The connected targets are powered by the MSP-GANG430 via VCC_MSP pin.
Target Power	Switch V_{CC} off	The supply voltage provided by the VCC_MSP pin is switched off after programming the targets.
	Supply voltage	Select the voltage supplied to the targets
	Settling time	Time to allow the target capacitors to fully charge
	Load image	Load the selected object code file into the image buffer.
Load Image –Target	Device Type	Select the memory model of the target
Program/Device	Device Information	Memory details of the selected device type
	File name	The file containing the object code
	Main memory	Check if the main memory should be erased or checked according to setting in group Main Process
Erase/Check Erase	Info memory	Check if the info memory should be erased or checked according to setting in group Main Process
	Calibration data	Check if the calibration protected info memory should be erased and/or checked according to setting in group Main Process. If this check box is checked, the calibration data is lost. It can be used if the calibration data is not needed in the application.
	H/W self test	Initiate hardware self test. All green LEDs on – test pass. All red LEDs on – test fail.
	Update firmware	Update MSP-GANG430 firmware with the firmware update file
Execute – Maintenance	Read out Target	Read out exclusively one of eight target modules.
Maintenance	Erase image	Erase the image buffer
	Target connector (test)	Set the selected pin on the 25-pin target connector of the MSP-GANG430.
JTAG	Use SpyBiWire	Check this box if the 2-wire SpyBiWire interface should be used for device operation. This option is not available for all devices. Note: The 2-wire SpyBiWire interface operation is slower than the regular 4-wire JTAG.
Communication	COM port	Select the PC serial port used to communicate with the MSP-GANG430.
Seangs	Baud rate	Select the baud rate of the serial port

Table 2–1. Function Buttons and Descriptions

Button Group	Functions	Description
	System menu mode	The System Menu is displayed by clicking on the icon at the upper left corner of GUI (or {Alt} and {Space} pressed together).
System menu	Lock settings	All settings on the GUI are locked. Only the <i>Start</i> button, the <i>Result</i> group box, and the <i>Status</i> group box are displayed. Other group boxes are hidden.
Gystem menu	Unlock settings	The locked settings and the hidden group boxes are enabled.
	Help	Opens the user help window
	About gang programmer	Desktop application software and firmware version and filename information is displayed (or press F1)

2.1.3 Status Messages

Table 2–2 shows the status messages that can be displayed in the MSP-GANG430 GUI during operation.

Table 2–2. Status Messages

Status Type	Status Messages
Progress	Executing main process
	Erasing image buffer
	Loading image buffer
	Loading checksum of image buffer
	Erasing firmware section
	Loading firmware section
	Initializing firmware
	Changing baud rate
	Executing self test
	Setting signals at target connector
System	Operation successful
	MSP-GANG430 gang programmer connected.
	Result of the operation: See the result section.
Setting	COM port settings modified. MSP-GANG430 gang programmer connected.
	Target power settings modified
Maintenance	Operation successful
	Selftest passed

2.1.4 Error Messages

Table 2–3 shows the error messages that can be displayed in the MSP-GANG430 GUI when an error occurs during operation.

Error Type	Error Message
Communication	ERROR: Unable to read Target!
	ERROR: Unable to open COM port – already in use?
	ERROR: Unable to close COM port!
	ERROR: Synchronization failed. Programmer connected?
	ERROR: Time out during operation – Correct COM port selected?
	ERROR: Communication – Frame has errors!
Setting System	ERROR: Select baud rate command not accepted!
	WARNING: Target voltage too low for erase / program operation! Set to > 2.7–V.
	WARNING: Could not set target voltage – Externally supplied?
	ERROR: Value wrong or out of range!
	ERROR: Selected file is of unrecognizable format!
	ERROR: Selected file is not a firmware update file!
	ERROR: Firmware section cannot be erased!
	ERROR: Unable to load firmware. Correct file format?
	ERROR: Unable to refresh gang programmer system parameters!
	ERROR: Unable to load Image buffer! Image buffer erased? Correct file format?
	ERROR: Result of operation not received!
	FATAL ERROR: Image memory corrupted or erased!
	ERROR: Unexpected end of file!
	ERROR: File contains invalid record!
	ERROR: Wrong target device connected!
	ERROR: Image range XXXX–YYYY placed outside of existing memory
	General error!
Maintenance	ERROR: Self test failed – No access to one ore more image buffer devices!
	ERROR: Self test failed – No JTAG access to one or more target channels!
	ERROR: Self test failed – Target voltage generator (V _{CCT}) does not work properly!
	ERROR: Self test failed – system voltage (V _{CC}) not in range!
	ERROR: Self test failed – blow fuse voltage (V _{PP}) not in range!
Windows	ERROR: Unable to open file!
	ERROR: Error during file I/O!
	ERROR: Unable to open INI file!

Table 2–3. Error Messages

2.1.5 Description of the Gang430.ini File

The Gang430.ini file contains the MSP-GANG430 default settings and the last used GUI settings.

2.1.5.1 Section [System]

The last used settings of the MSP-GANG430 GUI are stored in the Gang430.ini file before exiting the GUI application program. This information is stored under the [System] section of the file. There is one entry in this section which setting can be not changed via GUI: JtagSpeed = 0 (default value is 0). Setting it to a value of 1 makes the JTAG connection slower. It can be used for quality testing of the cable between target devices and the MSP-GANG430.

2.1.5.2 Section [User]

The initial settings for the following parameters are in the [User] section and may be modified by the user:

CaptionIndexed = 0 (default)

The caption of the MSP-GANG430 GUI in the default state is *MSP430 FLASH Gang Programmer*.

When CaptionIndexed = 1, the caption is extended with a suffix [*n*] to allow multiinstance operation for programming in automated production environment that uses caption titles for process control. The suffix n represents the unique COM*n* port number for that instance. E.g. for COM3, the caption is displayed as *MSP430 FLASH Gang Programmer* [3].

2.1.5.3 Section [Timing]

The [Timing] section shows the approximate time that has elapsed in milliseconds for various operations of the MSP-GANG430. The parameters under this section are updated after the associated operations are successful. The information on the elapsed time is useful for development and setup. The parameter that is most interesting is the time taken to complete a gang programming cycle, represented by TimeGangProg under this section.

2.1.5.4 Section [Diagnostic]

The [Diagnostic] section is useful for automatic production using the MSP-GANG430. During gang programming operation, Busy = 1. After completion of the operation, the information in the *Result* group box in the GUI is also listed in this section for the following: Connected, EraseChecked, Verified, FuseBlown, OverAll, and MiscErrors, if any. A Busy = 0 terminates that process and the diagnostics can be read from the .ini file.

For Example:

OverAll=239 (hexadecimal 0xEF) means: All channels except channel 5 were successfully processed (channel 1 represents the LSB, channel 8 represents the MSB of a byte).

Note:

When MiscErrors is nonzero, the diagnostic values are not valid for evaluation. Possible values for MiscErrors are shown below.

MiscErrors	Description
0	Operation successful
1	FATAL ERROR: Image memory could be corrupted or erased
11	ERROR: Main process parameters not yet set
16	ERROR: Could not set target voltage (VCCT) – MSP_VCC_IN pin connected?

2.1.6 Target Connector Functional Check

The MSP-GANG430 GUI diagnostics support a functional check of the target connector pins. The functional check is invoked by selecting the *Target Connector* category in the *Maintenance* group box, and clicking the *Execute* button with the appropriate *Set Signals* defined. The *Set Signals* value is the decimal value that represents the combination of the appropriate control bits. The defined pin's signal level is available for measurement after execution of the functional check. The signal levels remain unchanged until another configuration is set. The target connector functional check must always be terminated by executing the End of Check setting (0x8000 = 32768) or by executing an H/W self test to initialize the MSP-GANG430. Table 2–4 shows the details of the control bits.

Bits	Pin Function and Levels	Signal Level	Connector Pins
0x0000	Select TDO/TDI1	-	1,8
0x0001	Select TDO/TDI2	-	9
0x0002	Select TDO/TDI3	-	10
0x0003	Select TDO/TDI4	-	11
0x0004	Select TDO/TDI5	-	12
0x0005	Select TDO/TDI6	-	13
0x0006	Select TDO/TDI7	-	25
0x0007	Select TDO/TDI8	-	24
0x0008	Logic level at TDO/TDI <i>x</i> pin Logic level at TDI/VPP pin	V _{CC} or 0 V _{CC} /VPP or 0	1, 8–13, 25, 24, 2
0x0010	Logic level at TCK pin	V _{CC} or 0	4
0x0020	Logic level at TMS pin	V _{CC} or 0	3
0x0040	Logic level at TEST/VPP pin	V _{CC} –0.25V or 0	17
0x0080	Logic level at TDI/VPP pin	V _{CC} or 0	2
0x0100	VCC on/off at VCC_MSP pin	V _{CC} or 0	7, 14
0x0200	VPP on/off at TDI/VPP pin, overwrites bit 0x0080	VPP or 0	2
0x0400	VPP on/off at TEST/VPP pin	VPP or 0	17
0x7800	Reserved		
0x8000	End of check, reinitialize MSP-GANG430	initial	all

Table 2–4. Control Bit Definitions for Target Connector Pins

Example 1:

To check the logic high level on TDO/TDI3 and VPP voltage level on TEST/VPP:

Enter 0x040A (0x0002 + 0x0008 + 0x0400) into Set Signal and click Execute. The V_{CC} at pin 10 and V_{PP} at pin 17 can be measured now. All other pins remain at 0 V. The connector case is electrically grounded, as are pins 19–23.

Example 2:

To let free run target 3 programmed via SpyBiWire (2-Wire Mode):

Select TDO/TDI30x0002Logic Level at TDO/TDIx0x0008VCC on at VCC_MSP pin0x0100Enter 0x010a (0x0002 + 0x0008 + 0x0100) into Set Signal and clickExecute.

2.2 Programming MSP430 Flash Devices in Standalone Mode

The MSP-GANG430 supports a standalone mode of programming the target MSP430 flash devices.

The programming options for the MSP-GANG430 while operating in standalone mode are configured using the GUI. When the *Start* button in Main Process is clicked, all selected options are downloaded into the flash memory of the MSP-GANG430. Once the download is complete, the MSP-GANG430 can be disconnected from the PC and used standalone to program the target devices. The *START* push-button on the MSP-GANG430 operates identical to the *Start* button of the GUI. Progress of the operation in standalone mode is indicated by the flashing yellow LED.

The result status is represented by the rows of green and red LEDs on the MSP-GANG430. For each channel, a green LED indicates a successful operation and a red LED indicate a failed operation. If both red and green LEDs are off for a channel, the target device connected to that channel is not accessible or is not connected.

In standalone mode, the H/W self test of the MSP-GANG430 can be initiated by pressing the START key for more than three seconds. The progress of the test is indicated by alternate flashing of green and red LEDs in groups of four. Upon completion of the self test, all eight green LEDs are on if the self test passes. Any other combinations of the LEDs indicate a failed self test.

2.3 Programming MSP430 Flash Devices With User-Configured UART Handler

The MSP-GANG430 programmer supports programming the MSP430 flash devices by using generic firmware commands. The generic firmware commands and protocol are described in Chapter 3.

2.4 Programming the MSP430 Devices With GANG430.DLL

The MSP–GANG430 programmer supports programming the MSP430 flash devices by using DLL functions. The usage of the DLL functions and their prototypes (calling conventions) are described in Chapter 4: Dynamic Link Library GANG430.DLL.

Chapter 3

Firmware

Page

This chapter describes the firmware commands and protocol for controlling the MSP-GANG430 via its RS-232 serial communication interface.

Topic

3.1Commands3-23.2Firmware Interface Protocol3-23.3Synchronization Sequence3-23.4Data Frame3-23.5Commands—Detailed Description3-4

3.1 Commands

The MSP-GANG430 can be controlled via firmware commands received on its RS-232 serial port.

The following firmware commands are supported:

- Load Parameters
- Start
- Transmit Diagnostics
- Erase Image
- Load Image Block
- Load Image Checksum
- Erase Firmware
- Load Firmware Block
- Finalize Firmware
- □ Select Baud Rate
- Execute Self Test
- Set Signals (at target connector)
- Read Target Selective
- U Write Target Selective
- Set Target Vcc

3.2 Firmware Interface Protocol

The MSP-GANG430 supports the following UART communication protocol:

- □ Baud rates from 9600 to 115200 baud in half duplex mode. The default baud rate at startup is 115200 baud, and is the recommended baud rate.
- One start bit, eight data bits, even parity bit, and one stop bit.
- Software handshake by (not)acknowledge character.

3.3 Synchronization Sequence

To synchronize with the MSP-GANG430, the host serial handler transmits a SYNC character (80h) to the MSP-GANG430. The MSP-GANG430 acknowledges successful reception of the SYNC by responding with a DATA_ACK character (0x90). If the SYNC is not received correctly, a DATA_NAK character (0xA0) is sent back. This sequence is required to establish the communication channel and/or react immediately to line faults.

Note:

The synchronization character is not part of the data frame described later in this chapter.

3.4 Data Frame

The data frame format used follows the TI MSP430 serial standard protocol (SSP) rules extended with a preceding synchronization sequence (SS), as previously described. The MSP-GANG430 is considered the receiver in Table 3–1.

The obvious redundancy of some parameters results from the adaptation of the SSP, or to save boot ROM space.

3.4.1 Frame Structure

The data frame format of the firmware commands is shown in Table 3–1:

- The first 8 bytes (HDR through LH) are mandatory (xx represents dummy data).
- Data bytes D1 to Dn are optional.
- Two bytes (CKL and CKH) for checksum are mandatory.
- □ Acknowledge done by the MSP-GANG430 is mandatory except with the transmit diagnostic command.

3.4.2 Checksum

The 16-bit (2 bytes) checksum is calculated over all received/transmitted bytes B1 ... Bn in the data frame except the checksum bytes themselves by XORing words (2 successive bytes) and inverting the result.

Formula:

CHECKSUM = INV [(B1 + 256 x B2) XOR (B3 + 256 x B4) XOR ... XOR (Bn - 1 + 256 x Bn)]

or

CKL = INV [B1 XOR B3 XOR ... XOR Bn–1] CKH = INV [B2 XOR B4 XOR ... XOR Bn]

Received MSP- GANG430 Firmware Command	H D R	C M D	L1	L2	A1	A2	А3	A4	LL	LH	D1	D2 Dn	CKL	скн	A C K
Load Parameters	80	36	06	06	D1	D2		—	D3	D4	D5	D6	CKL	СКН	ACK
Start	80	31	04	04	00	00	—	—	00	00	_	—	CKL	СКН	ACK
Transmit Diagnostic	80	32	04	04	00	00	-	—	00	00	_	—	CKL	СКН	—
GANG430 responds	80	00	1E	1E	D1	D2	-	_	D3	D4	D5	D06D1E	CKL	СКН	—
Erase Image	80	33	04	04	00	00	-	—	00	00	_	—	CKL	СКН	ACK
Load Image Block	80	34	n	n	A1	A2	A3	0	n–4	00	D1	D2Dn-4	CKL	СКН	ACK
Load Image Checksum	80	37	06	06	D1	D2	—	—	D3	D4	D5	D6	CKL	СКН	ACK
Execute Self test	80	35	04	04	00	00	—	—	00	00	_	—	CKL	СКН	ACK
Set Signals	80	30	04	04	D1	D2	—	_	D3	D4	_	—	CKL	СКН	ACK
Erase Firmware	80	39	04	04	D1	D2	-	—	D3	D4	_	—	CKL	СКН	ACK
Load Firmware Block	80	ЗA	n	n	AL	AH	_	—	n–4	00	D1	D2Dn-4	CKL	СКН	ACK

Table 3–1. Data Frame of Firmware Commands

Received MSP- GANG430 Firmware Command	H D R	C M D	L1	L2	A1	A2	A3	A4	LL	LH	D1	D2 Dn	C K L	С К Н	A C K
Finalize Firmware	80	3B	04	04	00	00	—	—	00	00	—	—	CKL	СКН	ACK
Select Baud Rate	80	38	04	04	D1	D2	—	—	D3	D4	—	—	CKL	СКН	ACK
Write Target Selective	80	3C	n	n	A1	A2	A3	0	n–4	00	D1	D2Dn-4	CKL	СКН	ACK
Read Target Selective	80	3D	06	06	A1	A2	A3	0	n	00	D1	D2	CKL	СКН	_
GANG430 responds	80	00	n	n	D1	D2	—	—	D3	D4	D5	D6Dn	CKL	СКН	
Set Target V _{CC}	80	3E	04	04	D1	D2	-	_	D3	D4	—	—	CKL	СКН	ACK

Note: All numbers are bytes in hexadecimal notation.

ACK is sent back by the MSP-GANG430.

The synchronization sequence (TX: 0x80, RX: 0x90) is not part of the data frame. It must precede every frame.

Abbreviations:

HDR	DATA_FRAME = 0x80 means <i>data frame expected</i> in accordance with TI MSP430 serial standard protocol (SSP).						
CMD	Command identification	1					
L1, L2	Number of bytes consist Restrictions: L1 = L2, L	Number of bytes consisting of AL through Dn Restrictions: L1 = L2, L1 < 255, L1 even.					
A1, A2, A3	Block start address or erase (check) address or jump address LO/HI byte. The bytes stick together to get a 24 bit word as follows: Address = A3 x 10000h + A2 x 100h + A1						
LL, LH	Number of pure data bytes (max. 250) or erase information LO/HI byte or block length of erase check (max. FFFFh)						
D1 Dn	Data bytes						
CLK, CKH	16-bit checksum LO/HI	byte					
XX	Can be any data						
_	No character (data byte) received/transmitted						
ACK	The acknowledge char DATA_ACK = 0x90: DATA_NAK = 0xA0:	acter returned by the MSP-GANG430, can be either Frame was received correctly, command was executed successfully, or Frame not valid (e.g., wrong checksum, $L1 \neq L2$), command is not defined, is not allowed, or was executed unsuccessfully.					
n	Number of bytes						

3.5 Commands—Detailed Description

3.5.1 General

Besides the header byte HDR (0x80) and the command identification CMD, the frame length bytes L1 and L2 (which must be equal) hold the number of bytes following L2, excluding the checksum bytes CKL and CKH.

Bytes A1, A2, A3, A4, LL, LH, D1...Dn are command specific. However, the checksum bytes CKL (low byte) and CKH (high byte) are mandatory.

If the data frame has been received correctly and the command execution was successful, an acknowledge character DATA_ACK = 0x90 is sent back by the

MSP-GANG430. Incorrectly received data frames, unsuccessful operations, commands which are not defined are confirmed with a DATA_NAK = 0xA0.

3.5.2 Load Parameters

The load parameters command is used to download the information the main process needs for gang programming to the MSP-GANG430. It has to be invoked prior to the first gang programming command. The parameters are stored in the system flash memory.

Data bytes D1 to D6 hold the parameters as follows:

- D1 Flags for process control, in any combination: 0x01: executes erasure of info memory 0x02: executes erasure of main memory 0x04: executes erase of protected info memory 0x07: executes erasure of info and main memory 0x08: executes programming of info memory 0x10: executes programming of main memory 0x18: executes programming of info and main memory 0x20: executes verification of info and main memory 0x40: executes fuse blowing 0x80: executes programming of ram memory
- D2 Flags for target supply voltage VCC_MSP: 0x3F: Voltage in 100-mV steps (allowed are 18...36, 0 for external supply) 0x80: Switch VCC_MSP off after programming
- D3 0x01: executes erase check of info memory 0x02: executes erase check of main memory 0x04: executes erase check of protected info memory 0x07: executes erase check whole memory 0x08: use SpyBiWire JTAG interface for this device
- D4 Reserved
- D5, D6 VCC_MSP settle time:

Delay in milliseconds between switching VCC_MSP on and starting programming cycle. Valid values are 1...0xFFFF (>65s). D5 holds the LSB, D6 the MSB

3.5.3 Start

The start command executes the gang programming cycle. The operations are defined with the load parameters command. The result of the command execution can be determined using the transmit diagnostic command described below.

3.5.4 Transmit Diagnostic

The transmit diagnostic command provides the result of the preceding gang programming command.

Data bytes D1 to D30 hold the parameters as follows:

D1 to D5 give information about success of each target channel. The LSB represents channel 1, the MSB represents channel 8:

D1 Channels which are connected to the programmer (device detected).

- D2 Channels which passed the erase check successfully.
- D3 Channels which passed the verification successfully.

D4 Channels which passed the blow fuse successfully, fuse already blown.

D5 Channels which passed all operations successfully.

D6 Miscellaneous errors. See the Section [Diagnostic] paragraph.

D7 to D8 are reserved.

D9 to D10 hold the hardware version number: D9 (MSByte), D10 (LSByte).

D11 to D12 hold the firmware version number: D11 (MSByte), D12 (LSByte).

D13 to D30 hold a zero terminated character string representing the firmware file name e.g. GANG430_100.TXT.

3.5.5 Erase Image

The erase image command erases the entire image memory and verifies the erasure.

3.5.6 Load Image Block

The load image block command loads the data bytes into the image buffer of the MSP-GANG430, and verifies them.

D1 to Dn–4 contains the data bytes.

3.5.7 Write Target Selective

The write target selective command loads the data bytes into one exclusively selected target flash memory and verifies them.

Before applying this operation stable supply voltage at the targets is assumed (if not, use Set Target V_{CC} command and optionally wait for capacitor's settle time).

- D1 Target number (0..7)
- D2 0
- D3 to Dn-4 contain the data bytes (start address and number of bytes must be even numbered).

3.5.8 Read Target Selective

The read target selective command is used for any read access to the flash memory/RAM or peripheral module control registers at 0100h–01FEh of the selected target.

The 20 bit block start address is defined as follows: Address = $A3 \times 10000h + A2 \times 100h + A1$

The 16-bit block length is defined in LL (low byte) and LH (high byte). Because pure data bytes are limited to a maximum of 250, LH is always 0.

For this operation stable supply voltage at the targets is necessary. If not, use Set Target VCC command and wait for capacitor's settle time as required.

- D1 Target number (0..7)
- D2 0

The GANG430 responds with the requested data block. No acknowledge character is necessary.

D1 to Dn contain the data bytes (start address and number of bytes must be even numbered).

3.5.9 Set Target V_{CC}

The Set Target VCC command sets the VCC_MSP pin voltage of the programmer's target connector to the given value.

- D1 Target supply voltage:
 - 0 VCC_MSP output voltage is equal to MSP_VCC_IN input voltage
 - 1 VCC_MSP output voltage is switched off
 - 2 VCC_MSP output voltage is same as loaded by the Load Parameters command
 - 18+ VCC_MSP is set in hundreds of millivolts (e.g. D1 = 36 for 3.6 V)

D2 to D4 are 0x00.

3.5.10 Load Image Checksum

The load image checksum command writes the checksums of all non-empty memory segments (Info_x, main, RAM) and the memory model of the target device into the system flash memory. The gang programmer uses the checksums to verify the correct download of data to the image buffer and the correct programming of the target device.

On devices having protected info-memory segment, e. g. SegA on F2xxx devices, it can be indicated if this segment should be excluded from erasure when the other info-segments are to be erased.

The number of parameters depends on the memory model of the selected device:

Data bytes D1 to D4 hold the parameters as follows:

D1 0x80: VPP to TEST; 0x00 VPP to TDI.

D2 0x01: enable erasing of protected info memory; 0x00: disable erase of protected info memory.

- D3 Reserved.
- D4 Reserved.

Now for each memory segment of the device (main memory is treated as one segment) a set of 8 bytes indicating memory type, start address, length and checksum follows:

- D5 Memory type:
 - 0x03: Main

0x02: Info

0x05: protected Info

0x01: RAM

0x00: terminates the list of memory segment descriptions

D6 0x01: indicates the memory segment is empty (= 0xFFFF); 0x00: segment is not empty

- D7 Segment start address, LSByte
- D8 Segment start address, MSByte
- D9 Segment length, LSByte
- D10 Segment length, MSByte

D12 Checksum (PSA) for memory segment, LSByte

D13 Checksum (PSA) for memory segment, MSByte

The list must be terminated by a single byte containing 0x00.

D5 to D10 correspond to the 'MemDefx=' entries in the 'GANG430.ini'-File. Protected Info-memory-segments can be treated in two ways:

If the calibration data, which was stored during factory testing, is not needed, this segment can be used as normal Info-memory segments and D2 should be set to 0x01.

If calibration data should be preserved, D2 should be set to 0x00 to avoid erasing of the protected Info-memory segment when normal Info-memory is erased. The length of the protected Info-memory should be reduced by the number of bytes holding calibration data to exclude this data from the checksum.

The pseudo signature analysis (PSA) is computed using the following algorithm (using the C language):

```
for (PSA = StartAddr – 2, i = 0; i < Length; i++)
{
    if (PSA & 0x8000)
        PSA = ((PSA ^ 0x0805) << 1) | 1;
    else
        PSA <<= 1;
        PSA ^= Data[i];
    }
```

Where StartAddr is the beginning address of the memory region and length is the length of the memory region, data, in words.

3.5.11 Select Baud Rate

The select baud rate command sets the rate of the serial communications. The default is 115200 baud.

Data bytes D1 to D4 hold the parameters as follows:

- D1 Baud rate index 0 to 4 representing
 - 0: 9600 baud 1: 19200 baud 2: 38400 baud 3: 57600 baud 4: 115200 baud

D2 to D4 are 0x00.

The select baud rate command takes effect (i.e., changes the baud rate) immediately.

3.5.12 Execute Self Test

The execute self test command performs a test of the MSP-GANG430 hardware. In the event of failure, the MSP-GANG430 transmit diagnostic command can be used to obtain detailed information about the failure. D6 (miscellaneous errors) holds the following error codes:

- 0x04 Self test failed Control connections to image buffer devices invalid
- 0x05 Self test failed No JTAG access to one or more target channels
- 0x06 Self test failed target voltage generator (VCCT) does not work properly
- 0x07 Self test failed System voltage (V_{CC}) not in range
- 0x08 Self test failed Blow fuse voltage (VPP) not in range

3.5.13 Set Signals

Data bytes D1 to D4 hold the parameters as follows:

See the target connector functional check paragraph.

- D1 Control bits low byte
- D2 Control bits high byte
- D3 V_{CC} voltage in hundreds of mV (18 to 36 represent 1.8 V to 3.6 V)
- D4 0x00

3.5.14 Firmware Commands

Commands which affect the firmware such as *Erase Firmware, Load Firmware Block,* and *Finalize Firmware* are not recommended to be invoked from other than the MSP-GANG430 GUI.

Chapter 4

Dynamic Link Library GANG430.DLL

This chapter discusses the dynamic link library GANG430.DLL.

Торі	c P	age
4.1	GANG430.DLL Description	4-2
4.2	Return Values/Error Codes From the GNAG430.DLL	4-8

4.1 GANG430.DLL Description

The GANG430.dll is used to communicate with the MSP–GANG430 programmer unit and the connected MSP430 device(s). The file GANG430.ini must be in the same directory as GANG430.dll.

The configuration of the MSP–GANG430 should be done with the following sequence:

InitCom	// Open communication port
GangEraseImage	// Erase image flash memory
GangLoadImage	// Write target code into image flash memory
GangLoadParameters	// Determine process flow
ReleaseCom	// Close communication port

Once the configuration is completed all process specific parameters are stored in the flash memory of the MSP–GANG430 programmer unit.

The subsequent gang programming process could be done as follows:

InitCom	// Open communication port
GangSelectBaudrate	// Optional: slow down communication speed
GangMainProcess	<pre>// Execute gang programming process</pre>
GangGetResult	// Receive result for diagnostic
GangAccessTargetSFF	R // Optional: stimulate device pins
GangProgramTarget	// Optional: write serial numbers to targets
ReleaseCom	// Close communication port

Several examples showing how the DLL could be used are located in the DLL_Usage_Examples subdirectory of the GANG430 system. Every function returns an error code listed in the consecutive paragraph.

This dll could be used separately using the following conventions:

InitCom

long int InitCom(char* lpszComPort, long int lBaudRate)

InitCom initializes (opens) the given communications port, establishes communication with the MSP-GANG430 hardware, and sets the baud rate of the MSP-GANG430. If successful, the MSP-GANG430 is reset.

IBaudRate: Valid baud rates are: 9600, 19200, 38400, 57600, and 115200 Baud. The default baud rate after initialization is 115200 baud.

IpszComPort: The name of the communication port - COM1, COM2, COM3, or COM4

Example:

lFuncReturn = InitCom("COM1", 115200);

ReleaseCom

long int ReleaseCom (void)

This function is the counterpart to InitCom. It allows closing a communication with the MSP–GANG430 hardware.

Example:

IFuncReturn = ReleaseCom();

GangSelectBaudrate

long int GangSelectBaudrate (long int lBaudrateIndex)

Sets the baud rate of the MSP–GANG430 programming unit. It offers the capability of transmissions at lower baud rates than the default 115200 baud.

IBaudrateIndex:

- 0: 9600 Baud
- 1: 19200 Baud
- 2: 38400 Baud
- 3: 57600 Baud
- 4: 115200 Baud (default after power up)

Example:

lFuncReturn = GangSelectBaudrate(2); // selects 38400 Baud

GangEraseImage

long int GangEraseImage(void)

Clears (presets with 0xFF) the image memory of the MSP-GANG430 programming unit.

Performs a succeeding erase check over the address range 0x1000 to 0xFFFFF.

Example:

```
lFuncReturn = GangEraseImage();
```

GangLoadImage

long int GangLoadImage(char* lpszFileName, char* lpszDeviceName)

This function writes data from a file either of type TI-txt or of type Intel-hex (auto-detect) into the MSP–GANG430 programming unit's image flash memory. It also writes the memory model parameters of the selected device type and the checksums for used memory segments into the programmer's flash system memory.

IpszFileName: Name of the file to be loaded (full path) or NULL if function is only called to set up the target device.

IpszDeviceName: Name of the device in file Gang430.ini. Take care of the <space> between MSP430 and Fxxx.

Example:

```
lFuncReturn = GangLoadImage(FileName, "MSP430 F1121A");
lFuncReturn = GangLoadImage(NULL, "MSP430 F1121A");
```

GangLoadParameters

long int GangLoadParameters(long int IFlags, long int ISupply, long int IVccSettleTime)

The GangLoadParameters function is used to download the information the main process needs for gang programming to the MSP–GANG430. It has to be invoked prior to the first GangMainProcess call. The parameters are stored in the system flash memory.

IFlags:		
F_ERASE_INFO	0x0001	// executes erasure of info memory
F_ERASE_MAIN	0x0002	// executes erasure of main memory
F_ERASE_PINFO	0x0004	// executes erasure of protecte info memory
F_ERASE_MASS	0x0007	// executes erasure of info and main memory
F_PROGRAM_INFO	0x0008	// executes programming of info memory
F_PROGRAM_MAIN	0x0010	// executes programming of main memory
F_PROGRAM_MASS	0x0018	// executes programming of info and main memory
F_VERIFY	0x0020	// executes verification of info and main memory
F_BLOW_FUSE	0x0040	// executes fuse blowing
F_PROGRAM_RAM	0x0080	// executes programming of ram memory
F_ERASE_CHECK_INFO	0x0100	// executes erase check of info memory
F_ERASE_CHECK_MAIN	0x0200	// executes erase check of main memory
F_ERASE_CHECK_PINFO	0x0400	// executes erase check of protected info memory
F_ERASE_CHECK_MASS	0x0700	// executes erase check whole memory
F_USE_SBW	0x0800	// use SpyBiWire JTAG interface for this
F_JTAG_SPEED1	0x0000	// fastest JTAG speed mode
F_JTAG_SPEED2	0x1000	// slower JTAG speed mode

ISupply:

0x7F: Supply voltage VCC_MSP in hundreds of millivolts (30 -> 3 V).

0x80: Switch VCC_MSP off after main process execution (symbolic F_VCC_OFF=0x80).

IVccSettleTime: Capacitor settle time in milliseconds, max 0xFFFF -> >65s.

Example:

```
lFuncReturn = GangLoadParameters(0x3F, 30, 100);
```

// executes Mass Erase, Erase Check, Mass Program, Verification; VCC_MSP = 3 V, VCC_MSP settle time is 100 ms.

GangMainProcess

long int GangMainProcess(long int ITimeout)

This function executes the main process determined by the parameters loaded through the Gang-LoadParameters function. Use a succeeding GangGetResult call to receive the result of the executed main process.

ITimeout: The time out in hundreds of milliseconds until the programming unit must respond.

Example:

lFuncReturn = GangMainProcess(120); // with 12s time out

GangGetResult

long int GangGetResult(void *lpData)

This function reads out the result of the precedent execution of the main process at the MSP–GANG430 programming unit.

It also can be used to get detailed error information about the precedent execution of some other functions like GangSelftest.

IpData: Pointer points to a buffer that receives the result data.

Data bytes D00 to D29 hold the parameters as follows:

D0 to D4 give information about success of each target channel. The LSB represents channel 1 the MSB represents channel 8:

- D00 Channels which are connected to the programmer (device detected).
- D01 Channels which passed the erase check successfully.
- D02 Channels which passed the verification successfully.
- D03 Channels which passed the blow fuse successfully, fuse already blown.
- D04 Channels which passed all operations successfully.
- D05 Miscellaneous errors. See paragraph Description of the GANG430.ini file / Section[Diagnostic].

D06 to D07 are reserved.

D08 to D09 hold the hardware version number: D08 (MSByte), D09(LSByte).

D10 to D11 hold the firmware version number: D10 (MSByte), D11(LSByte).

D12 to D29 hold a zero terminated character string representing the firmware file name e.g. GANG430–120.TXT.

Example:

```
lFuncReturn = GangGetResult(lpBuffer);
```

GangAccessTargetSFR

Long int GangAccessTargetSFR(long int ITarget, long int iFlags, long int IAddress, void* lpData)

Accesses one byte or word within the special function register area (SFR) of one selectable target. The user has to take care about the memory attributes. Read access also can be used over the entire address range.

The first (last) access must be additionally se to the SFR_START (SFR_STOP) flag.

Before applying this operation stable supply voltage at the targets is assumed (if not, use GangSetVccTarget operation and optionally wait for capacitor's settle time).

ITarget: The target number – 1 (resp. target 1..8 at programmer unit)

IFlags:

0		
SFR_READ	0x00	read access
SFR_WRITE	0x01	write access, all targets are affected
SFR_START	0x02	start sequence (stop watchdog, halt CPU)
SFR_STOP	0x04	stop sequence (release CPU)

IAddress: Address of memory to be accessed. If address is lower than 0x0100 byte access is performed, word access otherwise.

IpData: Pointer points to a buffer that holds the data to be read/written.

Example:

lFuncReturn = GangAccessTargetSFR(7, 0, 0x120, &lpData);

// reads WDTCTL register of target 8 wordwise to lpData.

GangProgramTarget

long int GangProgramTarget(long int ITarget, long int IStart, long int ILength, void *lpData) Programs data exclusively into the flash memory of one selectable target. It is mainly used for giving away serial numbers.

Before applying this operation stable supply voltage at the targets is assumed (if not, use Gang-SetVccTarget operation and optionally wait for capacitor's settle time).

ITarget: The target number – 1 (resp. target 1..8 at programmer unit)

IStart, ILength: Startaddress of destination memory and number of bytes (both must be even).

IpData: Pointer points to a buffer that holds the data to be programmed (the source data buffer). Example:

lFuncReturn = GangProgramTarget(7, 0x1000, 0x10, lpData);

// programs 16 bytes exclusively into target 8 starting at address 0x1000.

GangReadTarget

long int GangReadTarget(long int ITarget, long int IStart, long int ILength, void *lpData);

Reads out data exclusively from one selectable target device into a buffer.

Before applying this operation stable supply voltage at the targets is assumed (if not, use GangSetVccTarget operation and optionally wait for capacitor's settle time).

ITarget: The target number - 1 (resp. target 1..8 at programmer unit)

IStart: Start address of the area to be read out (must be even). Allowed values: 0x0100 – 0x1FFFE (see memory map of the corresponding device).

ILength: Length of the area (must be even). Allowed values: 0x0000 – 0xFFFE (see memory map of the corresponding device).

IpData: Pointer points to a buffer the data to be written to (the destination data buffer).

Example:

lFuncReturn = GangReadTargetFile(0, 0xF000, 0x1000, lpDest);

// reads 4k bytes exclusively from target 1 starting at address 0xF000 into a buffer.

GangReadTargetFile

long int GangReadTargetFile(long int ITarget, long int IStart, long int ILength, char* lpszFileName, long int iFileType);

Reads out data exclusively from one selectable target device into a file either of type TI-txt or of type Intel-hex.

Before applying this operation stable supply voltage at the targets is assumed (if not, use GangSetVccTarget operation and optionally wait for capacitor's settle time).

ITarget: The target number - 1 (resp. target 1..8 at programmer unit)

IStart: Start address of the area to be read out (must be even). Allowed values: 0x0100 – 0xFFFFE (see memory map of the corresponding device).

ILength: Length of the area (must be even). Allowed values: 0x0000 – 0xFFFE (see memory map of the corresponding device).

lpszFileName: Name of the file (full path) to receive data. If the file does not exist, it will be created; if the file already exists, it will be overwritten.

iFileType:

FILETYPE_TI_TXT 0x01 file type is TI-txt FILETYPE_INTEL_HEX 0x02 file type is Intel-hex Example:

lFuncReturn = GangReadTargetFile(7, 0xF000, 0x1000, FileName, 1);

// reads 4k bytes exclusively from target 8 starting at address 0xF000 into a file with TI-txt format.

GangSetVccTarget

long int GangSetVccTarget(long int IVoltage)

This function sets the VCC_MSP pin voltage of the programming adapter's target connector to the given value.

IVoltage:

VCC_EXT	0	VCC_MSP output voltage is equal to MSP_VCC_IN input voltage
VCC_OFF	1	VCC_MSP output voltage is switched off
VCC_NOM	2	VCC_MSP output voltage is same as loaded by the <code>GangLoadParameters</code> function.
Others 18+		VCC_MSP is set in hundreds of millivolts
Example:		

lFuncReturn = GangSetVccTarget(36); // Set target V_{CC} to 3.6 V

GangSelftest

Long int GangSelftest(long int ITimeout)

GangSelftest performs a self test at the MSP–GANG430 programmer unit. Use a succeeding Gang-GetResult call to receive the result of the executed self test.

For detailed information about possible errors, see the Firmware/Execute Self Test paragraph.

ITimeout: The time out in hundreds of milliseconds until the programming unit must respond. Example:

IFuncReturn = GangSelftest(60); // with 6s time out

GangSetSignals

long int GangSetSignals(long int ISignals, long int IVoltage)

Sets specified signals at the MSP–GANG430 programmer unit's target connector.

For detailed information about bit definitions, see the Operation/Target Connector Functional Check paragraph.

ISignals: Determines output signals.

IVoltage: Determines logic high level voltage of signals in hundreds of millivolts.

Example:

lFuncReturn = GangSetSignals(0x0400, 27);

// V_{PP} at pin 17 (TEST/VPP); $V_{CC} = 2.7V$

InitProgress

long int InitProgress(long int hStatusWnd, long int hProgBar)

Passes the handles of the status window and the progress bar of the front end application. It offers the capability of updating a status window and/or a progress bar e.g. during file I/O operations.

hStatusWnd: Window handle of a status line.

hProgBar: Window handle, esp. of a progress bar.

Example:

GetErrorString

char* GetErrorString(long int IErrorNumber)

Determines the string associated with the error number. At invalid error numbers a pointer to Invalid error number! is returned.

IErrorNumber: The error number.

Example:

lpszErrorString = GetErrorString(lFuncReturn);

4.2 Return Values/Error Codes From the GANG430.DLL

Status	Return Value	Comment
ERR_NONE	0	Operation successful
ERR_COMM	1	Communication – Frame has errors
ERR_OPEN_COMM	2	Unable to open COM port – already in use?
ERR_CLOSE_COMM	3	Unable to close COM port
ERR_SET_COMM_STATE	4	Unable to modify COM port state
ERR_SYNC	5	Synchronization failed. Programmer connected?
ERR_RX_HDR_TIMEOUT	6	Timeout during operation – Correct COM port selected?
ERR_CMD_NOT_COMPLETED	7	Command did not complete correctly
ERR_CMD_FAILED	8	Command failed or not defined or Target not accessible
ERR_WRONG_BAUDRATE	9	Wrong baud rate specified
ERR_READ_INI	10	Could not read GANG430.ini
ERR_BAD_RECORD	11	File contains invalid record
ERR_FILE_END	12	Unexpected end of file
ERR_FILE_IO	13	Error during file I/O
ERR_FILE_DETECT	14	Selected file is of unrecognizable format
ERR_FILE_OPEN	15	Unable to open file
ERR_ARGUMENT	16	Function argument(s) out of range
ERR_IMAGE_CORRUPTED	31	Image Memory corrupted or erased
ERR_IMAGE_JTAGPORT	32	Self test – No JTAG access to Image Buffer device
ERR_IMAGE_DATAPORT	33	Self test – Data connections to Image Buffer device invalid
ERR_IMAGE_HANDSHAKE	34	Self test – No access to one ore more Image Buffer devices
ERR_TARGET_JTAGPORT	35	Self test – No JTAG access to one or more Target channels
ERR_TARGET_VOLTAGE	36	Self test – Target Voltage Generator (VCCT) does not work properly
ERR_SYSTEM_VOLTAGE	37	Self test – System Voltage (VCC) not in range
ERR_BLOWFUSE_VOLTAGE	38	Self test – Blow Fuse Voltage (VPP) not in range
ERR_TARGET_NOACCESS	39	Target not accessible
ERR_VERIFY_FAILED	40	Verification failed
ERR_NO_PARMS	41	Main Process Parameters not yet set

ERR_IMAGE_ERASE	42	Could not erase Image Buffer
ERR_IMAGE_LOAD	43	Could not load Image Buffer
ERR_PARMS_LOAD	44	Could not load Main Process Parameters
ERR_SEL_BAUDRATE	45	Could not select Baud Rate
ERR_SET_VCC	46	Could not set target voltage (VCCT) – Short circuit or settling time too short
ERR_WRONG_CMD	47	Invalid firmware command
ERR_LOW_SUPPLY_VOLTAGE	48	Power supply voltage too low
WNG_LOW_VCC_IN	49	Sense voltage out of range – Check pin MSP_VCC_IN
ERR_WRONG_DEVICE	50	Wrong target device connected (target doesn't match selected device)
ERR_DEVICE_MEM_MISMATCH	51	Image doesn't fit memory model of selected device.

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Chapter 5

Hardware

This chapter presents the MSP-GANG430 specifications and describes the required interconnections between the MSP-GANG430 and the target devices.

Topic Page 5.1 Programming Times vs Code Size for the Gang Programmer 5-2 5.2 5.3 Recommendations for Target Connections 5-3 MSP-GANG430 Target Connector Signals 5-3 5.4 5.5 5.6 MSP-GANG430 Component Locations 5-8 5.7 Gang_Exp Target Expansion Board Layout 5-9

5.1 Specifications

The specifications for the MSP-GANG430 hardware are shown in Table 5–1.

Table 5–1. MSP-GANG430 Hardware Specifications

Power supply	9 V–15 V dc, 300 mA minimum
Output voltage (V _{CC})	1.8 V3.6 V \pm 100 mV dc, 150 mA maximum
Output voltage (VPP)	6.5 V \pm 400 mV dc, 150 mA maximum
Signal path length between MSP-GANG430 and each target. If cables between programmer and targets are longer then 20 cm, serial resistors of 100 Ω should be inserted on signals TCK and TMS on the expansion board. TCK is on pin 4, TMS on pin 3 of the 25-pin SUB–D connector.	< 60 cm
Temperature range	10°C–45°C (50°F–113°F)
Humidity	40%-70%
Dimensions	150 mm(W) x 30 mm(H) × 82 mm(D)

5.2 Programming Times vs Code Size for the Gang Programmer

 $T_{total} \text{ [ms]} \sim 210 + T_{S} + \text{E x } 220 + (\text{EC} + \text{V}) \text{ x } 7.6 \text{ x } \text{size}_{mem} + \text{P x } 175 \text{ x } \text{size}_{code}$

Where,

E = 1, if Erase checkbox is selected, 0 otherwise,

EC = 1, if Erase Check checkbox is selected, 0 otherwise,

P = 1, if Program checkbox is selected, 0 otherwise,

V = 1, if Verify checkbox is selected, 0 otherwise,

 T_S : VCCT settle time in ms, defined in [User] section of GANG430.ini (e.g. VCCTSettleTime = 100)

Sizeme: Flash memory size in KBytes of the selected device

Sizecode : Code size in KBytes

Example for MSP430F149, all functions selected, $T_S = 100$ ms, full memory programmed:

 T_{total} [ms] = 210 + 100 + 220 + 2 x 7.6 x 60 + 175 x 60 = 11942; \Rightarrow 12 sec

Simple formula for: all functions selected, Ts = 100 ms, nearly full memory of device programmed:

T_{total} [ms] ~ 530 + 190 x size_{code}

NOTE: Using SpyBiWire mode, it takes about 3 seconds to program 2 kByte. As devices are programmed consecutively, the total time for eight devices is 24 seconds.

5.3 Recommendations for Target Connections

The following hardware connections are recommended when connecting the target MSP430 flash devices to the MSP-GANG430 without usage of the expansion board:

- The V_{CC} pins of all the targets must be tied together and connected to the positive terminal of the supply.
- □ The VSS pins of all targets must be tied together and connected to the GND or negative terminal of the supply.
- □ For targets without Test/VPP, five interconnections are needed: TMS, TCK, TDI/VPP, TDO/TDI, and VSS.
- □ For targets with Test/VPP, six interconnections are needed: TMS, TCK, TDI, TDO/TDI, VSS, and TEST/VPP.
- □ Use short cables to connect the target to the MSP-GANG430. Less than 60 cm is recommended. If cables between programmer and targets are longer then 20 cm, serial resistors of 100 Ω should be inserted on signals TCK and TMS on the expansion board. TCK is on pin 4, TMS on pin 3 of the 25-pin SUB–D connector.
- □ Ensure low-impedance interconnections, especially for the path of the JTAG fuse blow voltage—TDI/VPP or TEST/VPP.

NOTE: If targets which are already powered are connected to the MSP-GANG430, an accidental fuse-check can occur while connecting. This can falsely indicate a blown fuse. To avoid this accidental fuse-check, the TMS signal (pin 5 on the 14-pin JTAG connector) should be connected to VCC_MSP (pin 2 on the 14-pin JTAG connector) via a 3.3-k Ω pullup resistor on the programmer side. VCCO should not be connected to the supply voltage of the target, as the target is powered externally already. From the GUI or the DLL, VCC_MSP should be selected to be permanently on to drive TMS to 1 while the target is being connected to the programmer. Using the GUI this can be achieved by unchecking the box Switch Vcc off.

5.4 MSP-GANG430 Target Connector Signals

Figure 5–1, Figure 5–2, and Figure 5–3 show the target connector signals for the MSP-GANG430. Chapter 5 presents a schematic of the connections necessary to program multiple target devices.

When an external supply voltage is used for the target systems, it is very important to disconnect VCC_MSP from the targets to avoid power supply conflicts that could potential damage the MSP-GANG430 and the targets. Connect the external supply voltage to MSP_VCC_IN.

It is not possible to use the MSP-GANG430 with targets powered by the MSP-GANG430 and with targets powered by an external supply simultaneously.





Figure 5–2. 14-Pin Connector at the End of the Interconnect Cable



Signal Name	Required	Function/Comment
TMS	Yes	IEE1149.1 test mode select input
ТСК	Yes	IEE1149.1 test clock input
TDI/VPP	Yes	IEE1149.1 test data input multiplexed with fuse blow voltage input.
TDO/TDI1 to TDO/TDI8	Yes	IEE1149.1 test data output multiplexed with Test data input for use during fuse blow fuse operation.
GND	Yes	GND is the 0-V terminal
VCC_MSP	Yes (if internal supply voltage is used)	Software selectable supply voltage V_{CC} to power the targets from the programmer.
MSP_VCC_IN	Yes (if external supply voltage is used)	External supply voltage sense input, to adapt the internal level shifter outputs.
TEST/VPP	Yes (depending on device)	Signal used with MSP430 flash devices with TEST/VPP pin.
RESET	Yes (depending on end application)	Provides the reset of target device. The duration and delay after reset is to be set by "Settling Time".

Table 5–2. MSP-GANG430 Target Connector Signal Functions

The output signal levels of the MSP-GANG430 are near GND or VCC_MSP.

- □ The programming procedure (handling of the SW) is described in Chapters 1 and 2 of this manual.
- ☐ The connections from the MSP430 terminals must follow EMI rules, including short trace lengths and use of ground planes.

Table 5–3. MSP-GANG430 Signal Levels

Signal/Pin	Signal/Pin Levels
TMS	V _{SS} or VCC_MSP
ТСК	V _{SS} or VCC_MSP
TDI/VPP	V _{SS} or VCC_MSP or VPP
TDO/TDI18	V _{SS} or VCC_MSP
GND	V _{SS}
VCC_MSP	V _{SS} or VCC_MSP
MSP_VCC_IN	Input: External V _{CC} of MSP430
TEST/VPP	V _{SS} or VCC_MSP or VPP
RESET	V _{SS} or HiZ (external pullup resistor required)



Figure 5–3. Signal Connections for 4-Wire JTAG Communication

- [†] Make either connection J1 in case a local target power supply is used OR connection J2 to power target from the debug/programming adapter.
- [‡] The RST/NMI pin R1/C1 configuration is device family dependent. See the respective MSP430 Family user's guide for the recommended configuration. When using 2-wire JTAG communication capable devices in 4-wire JTAG mode the upper limit for C1 should not exceed 2.2 nF. This applies to both TI FET Interface modules (LPT/USB FET).
- § The TEST/VPP pin is only available on MSP430 family members with multiplexed JTAG pins. See the device data sheet to see if this pin is available.
- If The connection to the JTAG connector RST pin is optional when using 4-wire JTAG communication mode capable-only devices and not required for device programming or debugging. However, this connection is required when using 2-Wire JTAG communication mode capable devices in 4-Wire JTAG mode.



Figure 5–4. Signal Connections for 2-Wire JTAG Communication (Spy-Bi-Wire)

- [†] Make either connection J1 in case a local target power supply is used OR connection J2 to power target from the debug/programming adapter.
- * Note that the device RST/NMI/SBWTDIO pin is used in 2-wire mode for bidirectional communication with the device during JTAG access and that any capacitance attached to this signal may affect the ability to establish a connection with the device. The upper limit for C1 is 2.2 nF when using current TI FET Interface modules (USB FET).
- R2 is used to protect the JTAG debug interface TCK signal against the JTAG security fuse blow voltage that is supplied by the TEST/VPP pin during the fuse blow process. In the case that fuse blow functionality is not needed, R2 is not required (becomes 0 Ω) and the connection TEST/VPP must not be made.

5.5 MSP-GANG430 Schematics

The MSP-GANG430 schematics are presented in Chapter 6.

5.6 MSP-GANG430 Component Locations

Figure 5–5. MSP-GANG430 Component Locations



5.7 Gang_Exp Target Expansion Board Layout

Figure 5–6. Gang_Exp Layout



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Chapter 6

Schematics

This chapter shows the schematics of the MSP-GANG430 and the connections necessary to program multiple target devices.













Revision History

Revision 1.2

- Initial released hardware version
- Only the 2-wire JTAG mode is supported on Spy-Bi-Wire capable devices

Revision 1.3 (December 2005)

- Serial resistors of 100R (R53–R55 and R57–R64) are added on JTAG signals (TCK, TMS, TDI, TDO1 –TDO8) to improve stability if target connections are longer than 20 cm and/or bed-of-nails is used
- Resistors (R56, R65) of 33 kΩ are added on case-to-GND connections of DSUB9 and DSUB25 connectors
- New R68 on VCCO: discharges targets
- New U31, R66, R52, C38: adds Reset capability
- U31 assembled SN74LVC1G07DBV instead of SN74LVC1G125DBV (--> open-drain Reset)
- C27 changed from 1 µF Tantal to 100 µF / 35 V Aluminum
- R51 (3k3) added between TTMS and VCCO; should avoid bouncing on TMS if already supplied targets are connected (irregular fuse-check)
- Both 2-wire and 4-wire JTAG modes are supported on Spy-Bi-Wire capable devices

Assembly change on 1.3 (January 2005)

R52 changed from 22 kΩ to DNP

Appendix A

Hex Object Formats

This appendix discusses the Intel hex object format.

Торі	c Page
A.1	Intel Hex Object Format A-2
A.2	TI-TXT File Format A-3

A.1 Intel Hex Object Format

The Intel hex object format supports 16-bit addresses and consists of a 9-character (4-field) prefix which defines the start of record, byte count, load address, record type, and a 2-character sumcheck suffix.

The few record types, which are represented in the 9-character prefix, are described below:

00 = Data record (begins with the colon start character)

01 = End-of-file record

Record type 00, the data record, begins with the colon (:) start character and is followed by the byte count, the address of the first data byte, the record type (00), and the sumcheck. The sumcheck is the 2s complement (in binary) of the preceding bytes in the record including the byte count, address, and data bytes.

Record type 01, the end-of-file record, also begins with the colon (:) start character. The colon is followed by the byte count, the address, the record type (01), and the sumcheck.

In order to represent the address bus more then 16 bit these other record types are defined:

02 = Extended Segment Address Record, segment-base address. Used when 16 bits is not enough, identical to 80x86 real mode addressing. The address is specified by the 02 record is multiplied by 16 (shifted 4 bits left) and added to the subsequent 00 record addresses. This allows addressing of up to a megabyte of address space. The address field of this record has to be 0000, the byte count is 02 (the segment is 16-bit). The least significant hex digit of the segment address is always 0.

04 = Extended Linear Address Record, allowing for fully 32 bit addressing. The address field is 0000, the byte count is 02. The two data bytes represent the upper 16 bits of the 32 bit address, when combined with the address of the 00 type record.





A.2 TI-TXT File Format

The TI-TXT file format used by the tool is shown as follows:

@ADDR1 DATA01 DATA02 DATA16 DATA32 DATA17 DATA32 DATAmDATAn @ADDR2 DATA01 DATAn q Whereas: @ADDR is the start address of a section (hexadecimal) DATAn represents a data byte (hexadecimal) q is the termination of the file For example: @F000 31 40 00 03 B2 40 80 5A 20 01 D2 D3 22 00 D2 E3 21 00 3F 40 E8 FD 1F 83 FE 23 F9 3F @FFFE 00 F0 Q **Restrictions:** The number of sections is unlimited. The start address must be even. Each line can have a maximum of 16 data bytes, except the last line of a section. Data bytes are separated by a single space. The termination tag q indicates end-of-file is mandatory.