



5-BIT 2:1 MULTIPLEXER

SY10E158
SY100E158

FEATURES

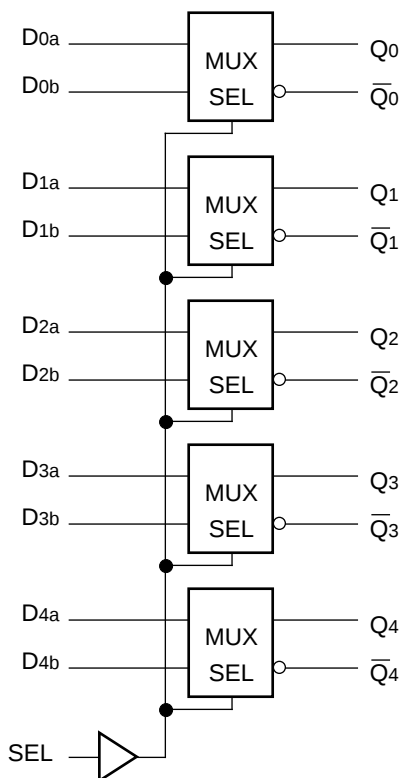
- 550ps max. D to output
- Extended 100E VEE range of -4.2V to -5.5V
- 775ps max. SEL to output
- Differential outputs
- Fully compatible with industry standard 10KH, 100K ECL levels
- Internal 75K Ω input pulldown resistors
- Fully compatible with Motorola MC10E/100E158
- Available in 28-pin PLCC package

DESCRIPTION

The SY10/100E158 offer five 2:1 multiplexers with differential outputs, designed for use in new, high-performance ECL systems.

The multiplexer operation is controlled by the SEL (Select) signal which selects one of the two bits of input data at each mux to be passed through.

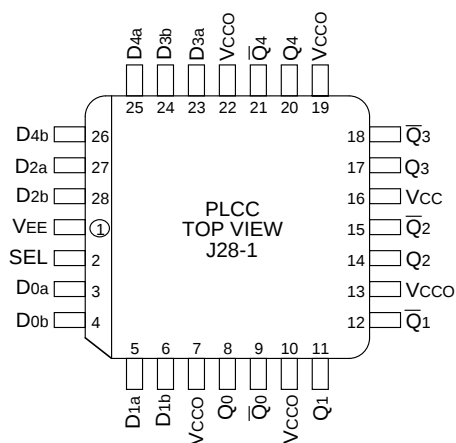
BLOCK DIAGRAM



PIN NAMES

Pin	Function
D0a–D4a	Input Data a
D0b–D4b	Input Data b
SEL	Select Input
Q0–Q4	True Outputs
$\bar{Q}0$ – $\bar{Q}4$	Inverted Outputs
Vcco	Vcc to Output

PACKAGE/ORDERING INFORMATION



28-Pin PLCC (J28-1)

Ordering Information⁽¹⁾

Part Number	Package Type	Operating Range	Package Marking	Lead Finish
SY10E158JC	J28-1	Commercial	SY10E158JC	Sn-Pb
SY10E158JCTR ⁽²⁾	J28-1	Commercial	SY10E158JC	Sn-Pb
SY100E158JC	J28-1	Commercial	SY100E158JC	Sn-Pb
SY100E158JCTR ⁽²⁾	J28-1	Commercial	SY100E158JC	Sn-Pb
SY10E158JZ ⁽³⁾	J28-1	Commercial	SY10E158JZ with Pb-Free bar-line indicator	Matte-Sn
SY10E158JZTR ^(2, 3)	J28-1	Commercial	SY10E158JZ with Pb-Free bar-line indicator	Matte-Sn
SY100E158JZ ⁽³⁾	J28-1	Commercial	SY100E158JZ with Pb-Free bar-line indicator	Matte-Sn
SY100E158JZTR ^(2, 3)	J28-1	Commercial	SY100E158JZ with Pb-Free bar-line indicator	Matte-Sn

Notes:

1. Contact factory for die availability. Dice are guaranteed at $T_A = 25^\circ\text{C}$, DC Electricals only.
2. Tape and Reel.
3. Pb-Free package is recommended for new designs.

TRUTH TABLE

SEL	Data
H	a
L	b

DC ELECTRICAL CHARACTERISTICSV_{EE} = V_{EE} (Min.) to V_{EE} (Max.); V_{CC} = V_{CCO} = GND

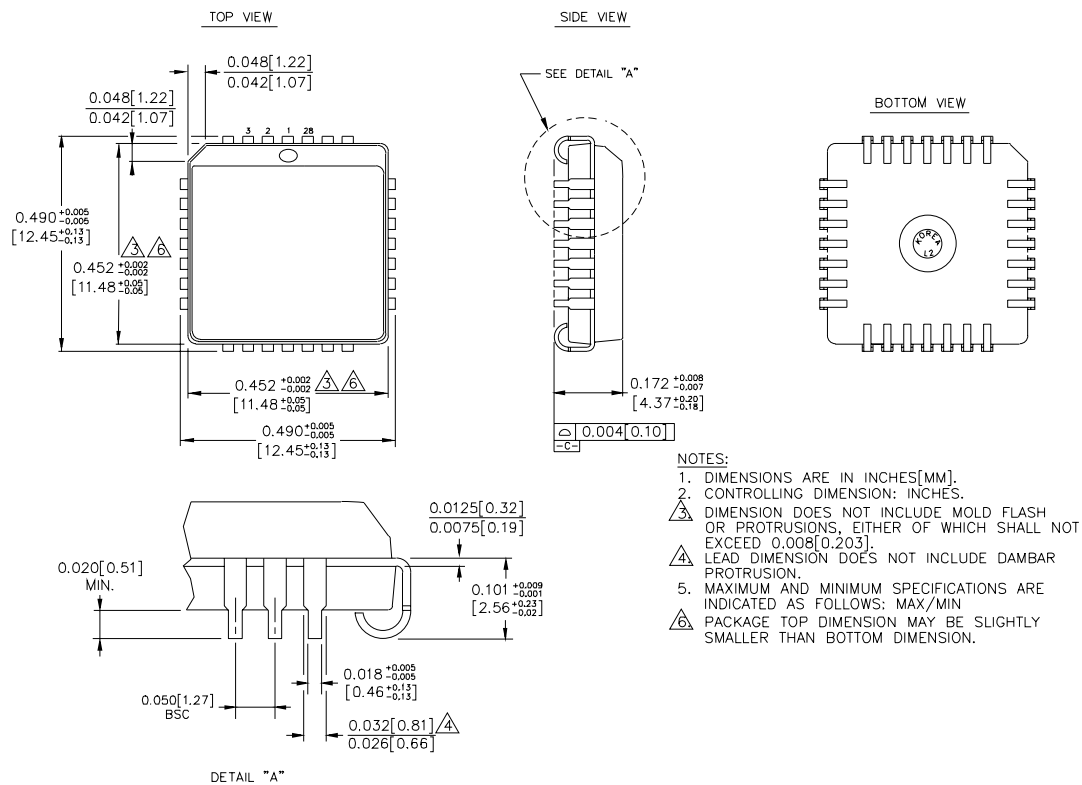
Symbol	Parameter	T _A = 0°C			T _A = +25°C			T _A = +85°C			Unit	Condition
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
I _{IH}	Input HIGH Current D SEL	—	—	200 150	—	—	200 150	—	—	200 150	μA	—
I _{EE}	Power Supply Current 10E 100E	—	33 33	40 40	—	33 33	40 40	—	33 38	40 46	mA	—

AC ELECTRICAL CHARACTERISTICSV_{EE} = V_{EE} (Min.) to V_{EE} (Max.); V_{CC} = V_{CCO} = GND

Symbol	Parameter	T _A = 0°C			T _A = +25°C			T _A = +85°C			Unit	Condition
		Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.		
t _{PD}	Propagation Delay to Output D SEL	225 400	385 600	550 775	225 400	385 600	550 775	225 400	385 600	550 775	ps	—
t _{skew}	Within-Device Skew	—	60	—	—	60	—	—	60	—	ps	1
t _r t _f	Rise/Fall Time 20% to 80%	275	425	650	275	425	650	275	425	650	ps	—

Note:

1. Within-device skew is defined as identical transitions on similar paths through a device.

28-PIN PLCC (J28-1)

Rev. 03

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