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Truth tables and state tables still are used to specify the behavior of logic circuits, and Boolean algebra is still a basic mathematical tool for logic design. Even when programmable logic devices are used instead of individual gates and flip-flops, reduction of logic equations is still desirable in order to fit the equations into smaller PLDs.

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Each $(d_{3i-1}b_2 + d_{3i-2}b_1 + d_{3i-3}b_0)$ has a value from 0 to $(b-1)(b_2 + b_1 + b_0)$. Consequently, d_i can be represented as a base b number in the form $(b-1)(b_2 + b_1 + b_0)$. $(e_{3i-1}b_2 + e_{3i-2}b_1 + e_{3i-3}b_0)$ so it is a valid digit in a base b^3 number. Where each e_j has a value from 0 to $(b-1)$.

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$Z = \lambda(S)$ Here is a tip: As the clock is applied, the state of flip-flop change is termed as the next state. Explanation. δ represents the next state of circuit. When present state is S_3 , input is 1, then next state is S_2 according to the given table.

[Solved] Chapter 13, Problem 6 - Fundamentals of Logic ...

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