

VISHAY IRL520PbF-BE3 Power Mosfet User Guide

Contents

1 VISHAY IRL520PbF-BE3 Power Mosfet User Guide
1.1 PRODUCT SUMMARY
1.2 FEATURES
1.3 DESCRIPTION
1.4 ORDERING INFORMATION
1.5 ABSOLUTE MAXIMUM RATINGS
1.6 THERMAL RESISTANCE RATINGS
1.7 SPECIFICATIONS
1.8 TYPICAL CHARACTERISTICS
2 Documents / Resources
2.1 References

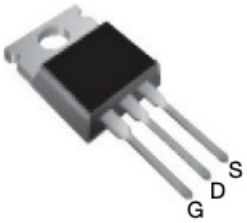
VISHAY IRL520PbF-BE3 Power Mosfet User Guide

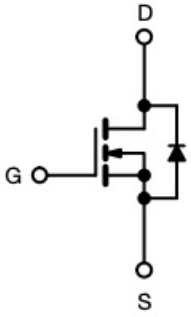
www.vishay.com

IRL520
Vishay Siliconix

Power MOSFET

TO-220AB





N-Channel MOSFET

PRODUCT SUMMARY

PRODUCT SUMMARY		
V_{DS} (V)	100	
$R_{DS(on)}$ (Ω)	V_{GS} = 5.0 V	0.27
Q_g (Max.) (nC)	12	
Q_{gs} (nC)	3.0	
Q_{gd} (nC)	7.1	
Configuration	Single	

FEATURES

- Dynamic dV/dt rating
- Repetitive avalanche rated
- Logic-level gate drive
- RDS(on) specified at VGS = 4 V and 5 V
- 175 °C operating temperature
- Fast switching
- Ease of paralleling
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Available
RoHS*
Available

Note

* This datasheet provides information about parts that are RoHS-compliant and / or parts that are non RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information / tables in this datasheet for details

DESCRIPTION

Third generation power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost the TO-220AB contribute to its wide acceptance throughout the industry.

ORDERING INFORMATION

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	IRL520PbF
Lead (Pb)-free and halogen-free	IRL520PbF-BE3

ABSOLUTE MAXIMUM RATINGS

ABSOLUTE MAXIMUM RATINGS (T_C = 25 °C, unless otherwise noted)					
PARAMETER			SYMBOL	LIMIT	UNIT
Drain-source voltage			V_{DS}	100	V
Gate-source voltage			V_{GS}	± 10	
Continuous drain current	V_{GS} at 5 V	T_C = 25 °C	I_D	9.2	A
		T_C = 100 °C		6.5	
Pulsed drain current [a]			I_{DM}	36	
Linear derating factor				0.40	W/°C
Single pulse avalanche energy [b]			E_{AS}	170	mJ
Repetitive avalanche current [a]			I_{AR}	9.2	A
Repetitive avalanche energy [a]			E_{AR}	6.0	mJ
Maximum power dissipation	T_C = 25 °C		P_D	60	W
Peak diode recovery dV/dt [c]			dV/dt	5.5	V/ns
Operating junction and storage temperature range			T_J, T_{stg}	-55 to +175	°C
Soldering recommendations (peak temperature) [d]	For 10 s			300 [d]	
Mounting torque	6-32 or M3 screw			10	lbf · in
				1.1	N · m

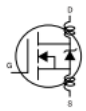
Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
b. V_{DD} = 25 V, starting T_J = 25 °C, L = 3.0 mH, R_g = 25 Ω, I_{AS} = 9.2 A (see fig. 12)
c. I_{SD} ≤ 9.2 A, dI/dt ≤ 110 A/µs, V_{DD} ≤ V_{DS}, T_J ≤ 175 °C
d. 1.6 mm from case

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum junction-to-ambient	R_{thJA}	-	62	°C/W
Case-to-sink, flat, greased surface	R_{thCS}	0.50	-	
Maximum junction-to-case (drain)	R_{thJC}	-	2.5	

SPECIFICATIONS

SPECIFICATIONS (T_J = 25 °C, unless otherwise noted)						
PARAMETER	**SYMBOL**	**TEST CONDITIONS**	**MIN.**	**TYP.**	**MAX.**	**UNIT**
Static						
Drain-source breakdown voltage	V_{DS}	V_{GS} = 0 V, I_D = 250 µA	100	-	-	V
V_{DS} temperature coefficient	$\Delta V_{DS}/T_J$	Reference to 25 °C, I_D = 1 mA	-	0.12	-	V/°C
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} = V_{GS}, I_D = 250 µA	1.0	-	2.0	V
Gate-source leakage	I_{GSS}	V_{GS} = ± 10 V	-	-	± 100	nA
Zero gate voltage drain current	I_{DSS}	V_{DS} = 100 V, V_{GS} = 0 V	-	-	25	µA
		V_{DS} = 80 V, V_{GS} = 0 V, T_J = 150 °C	-	-	250	
Drain-source on-state resistance	$R_{DS(on)}$	V_{GS} = 5.0 V, I_D = 5.5 A[b]	-	-	0.27	Ω
		V_{GS} = 4.0 V, I_D = 4.6 A[b]	-	-	0.38	
Forward transconductance	g_{fs}	V_{DS} = 50 V, I_D = 5.5 A	3.2	-	-	S
Dynamic						
Input capacitance	C_{iss}	V_{GS} = 0 V, V_{DS} = 25 V, f = 1.0 MHz, see fig. 5	-	490	-	pF
Output capacitance	C_{oss}		-	150	-	
Reverse transfer capacitance	C_{rss}		-	30	-	
Total gate charge	Q_g	V_{GS} = 5.0 V, I_D = 9.2 A, V_{DS} = 80 V, see fig. 6 and 13[b]	-	-	12	nC
Gate-source charge	Q_{gs}		-	-	3.0	
Gate-drain charge	Q_{gd}		-	-	7.1	
Turn-on delay time	$t_{d(on)}$	V_{DD} = 50 V, I_D = 9.2 A, R_g = 9.0 Ω, R_D = 5.2 Ω, see fig. 10[b]	-	9.8	-	ns
Rise time	t_r		-	64	-	
Turn-off delay time	$t_{d(off)}$		-	21	-	
Fall time	t_f		-	27	-	
Internal drain inductance	L_D	Between lead, 6 mm (0.25") from package and center of die contact 	-	4.5	-	nH
Internal source inductance	L_S		-	7.5	-	
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I_S	MOSFET symbol showing the integral reverse p - n junction diode 	-	-	9.2	A
Pulsed diode forward current [a]	I_{SM}		-	-	36	
Body diode voltage	V_{SD}	T_J = 25 °C, I_S = 9.2 A, V_{GS} = 0 V[b]	-	-	2.5	V
Body diode reverse recovery time	t_{rr}	T_J = 25 °C, I_F = 9.2 A, dI/dt = 100 A/µs[b]	-	130	190	ns
Body diode reverse recovery charge	Q_{rr}		-	0.83	1.0	µC
Forward turn-on time	t_{on}	Intrinsic turn-on time is negligible (turn-on is dominated by L_S and L_D)				

Notes

a. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
b. Pulse width ≤ 300 µs; duty cycle ≤ 2 %

TYPICAL CHARACTERISTICS

(25 °C, unless otherwise noted)

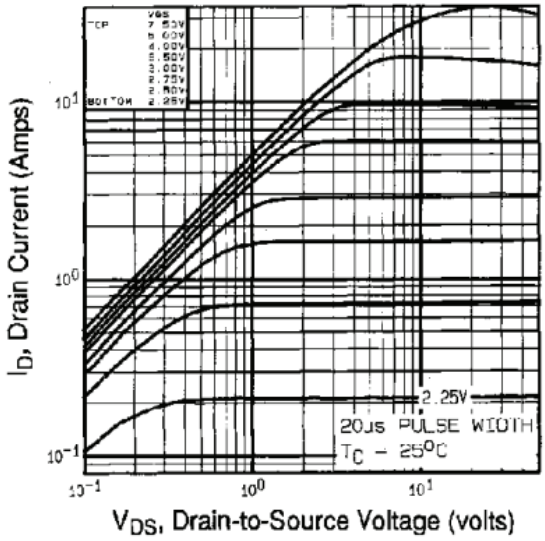


Fig. 1 - Typical Output Characteristics, T_C = 25 °C

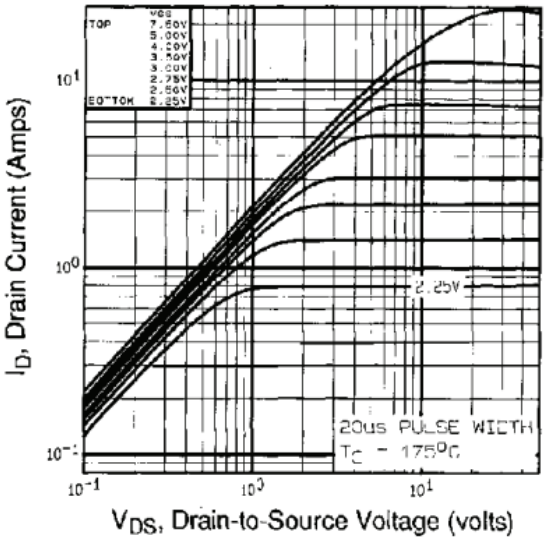


Fig. 2 - Typical Output Characteristics, T_C = 175 °C

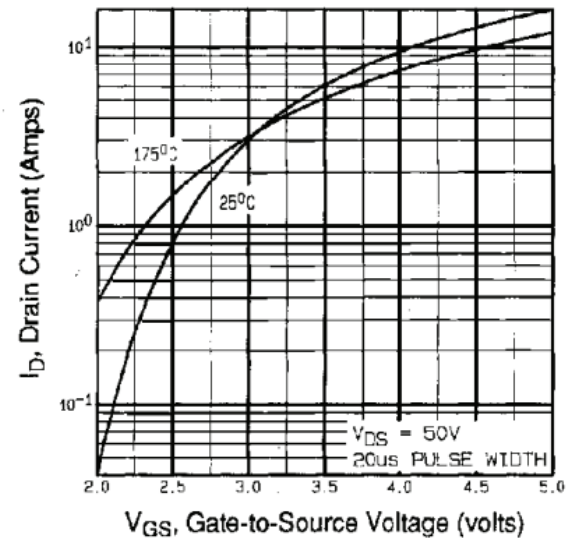


Fig. 3 - Typical Transfer Characteristics

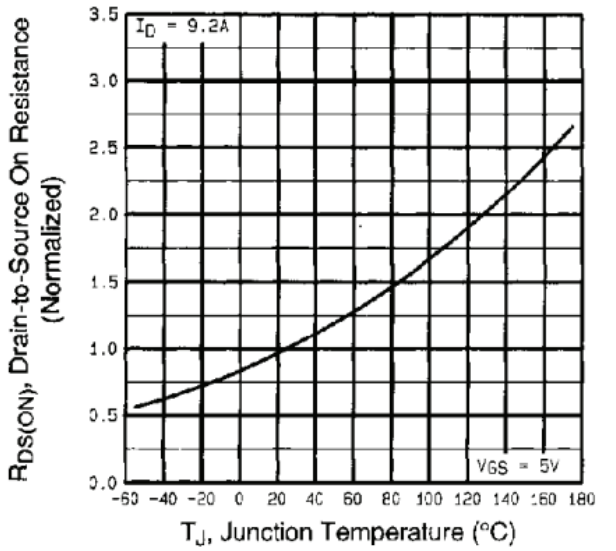


Fig. 4 - Normalized On-Resistance vs. Temperature

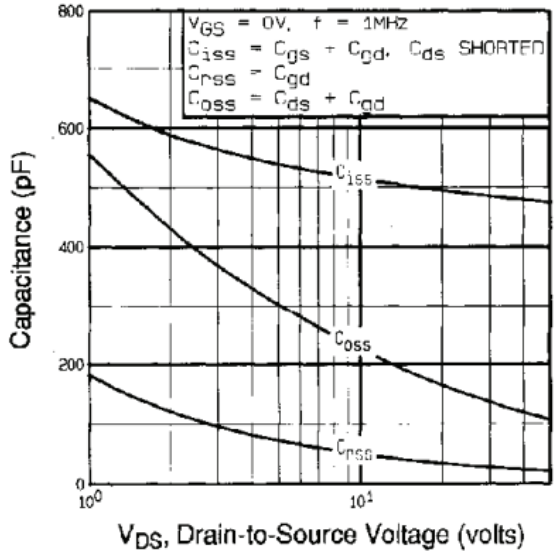


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

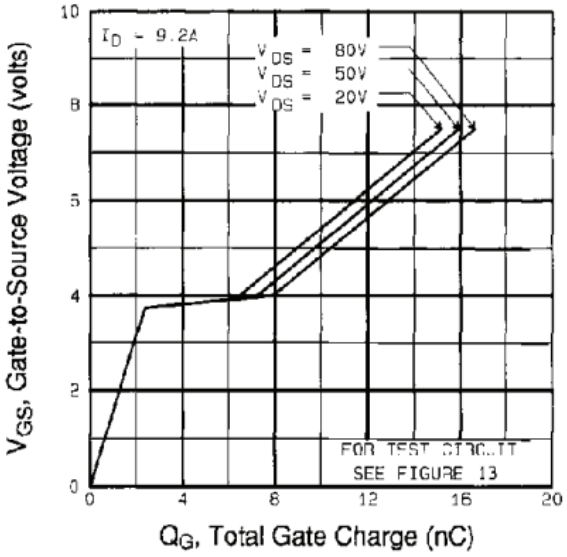


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

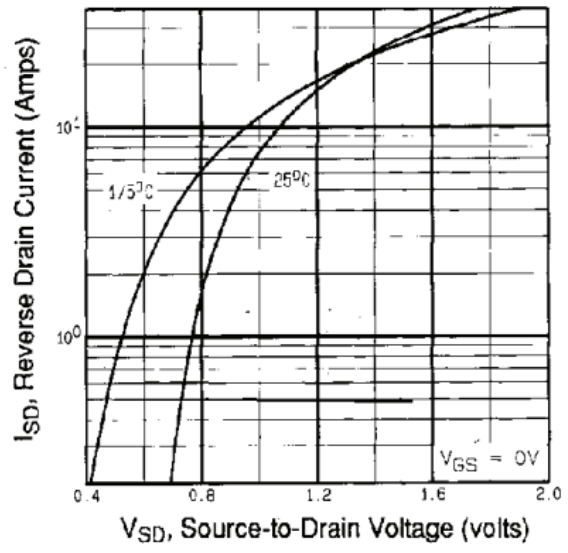


Fig. 7 - Typical Source-Drain Diode Forward Voltage

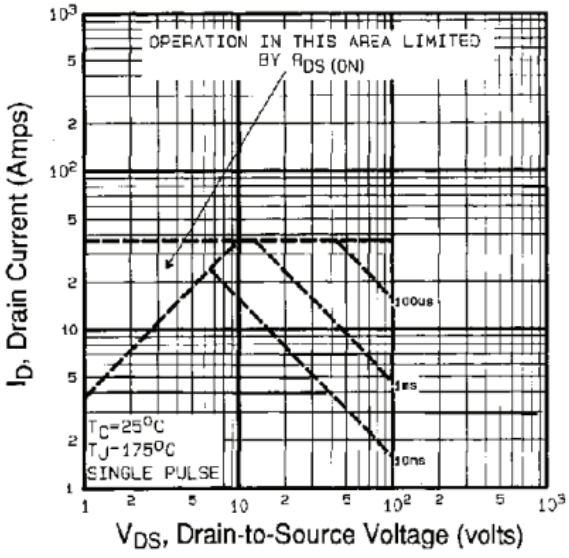


Fig. 8 - Maximum Safe Operating Area

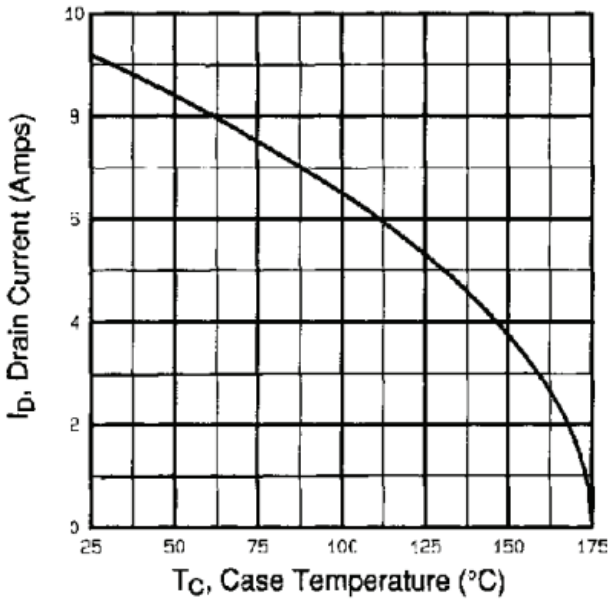


Fig. 9 - Maximum Safe Operating Area

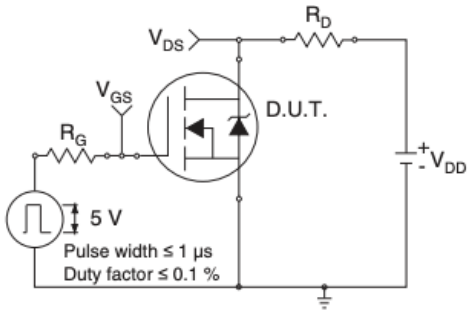


Fig. 10a - Switching Time Test Circuit

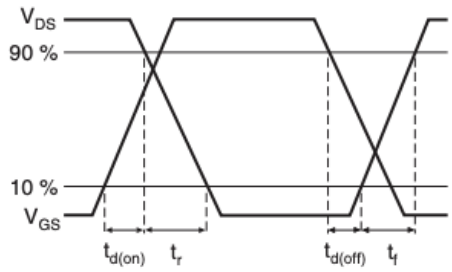


Fig. 10b - Switching Time Waveforms

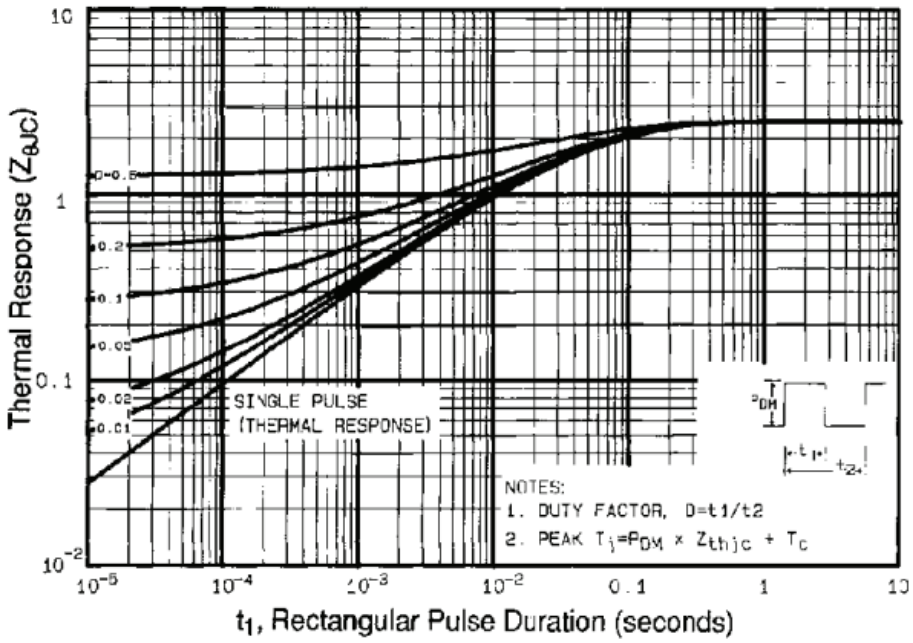


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

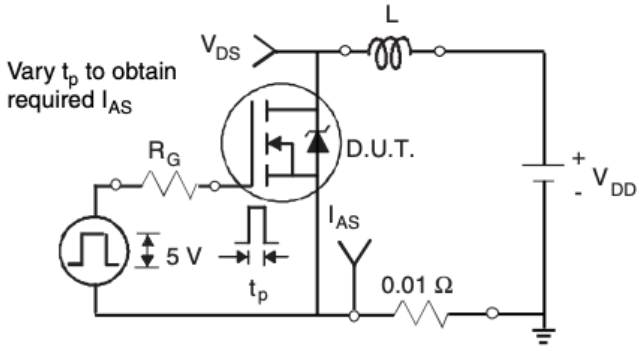


Fig. 12a - Unclamped Inductive Test Circuit

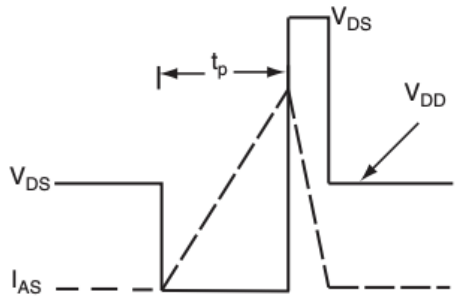


Fig. 12b - Unclamped Inductive Waveforms

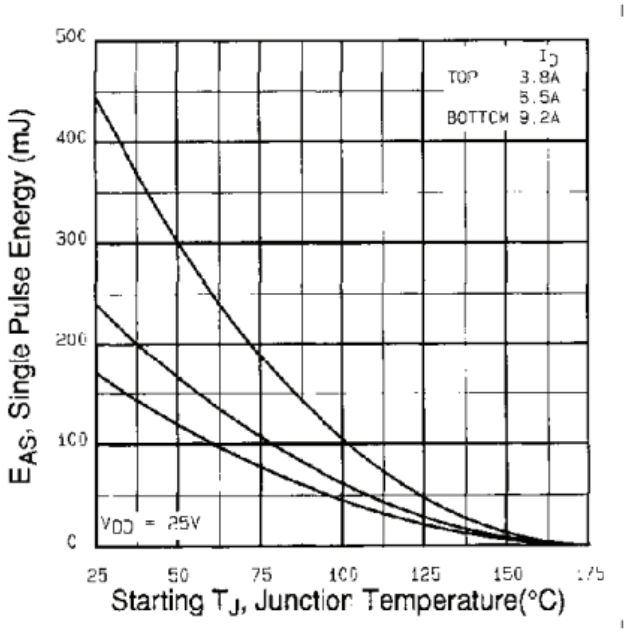


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

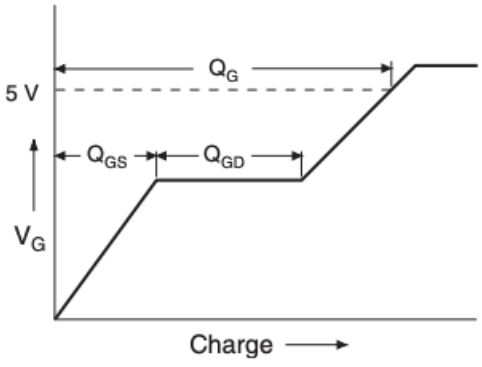


Fig. 13a - Basic Gate Charge Waveform

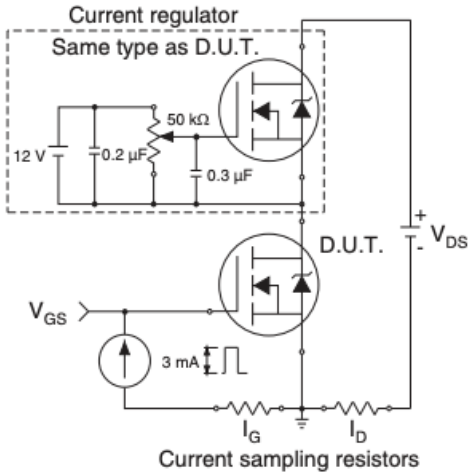


Fig. 13b - Gate Charge Test Circuit

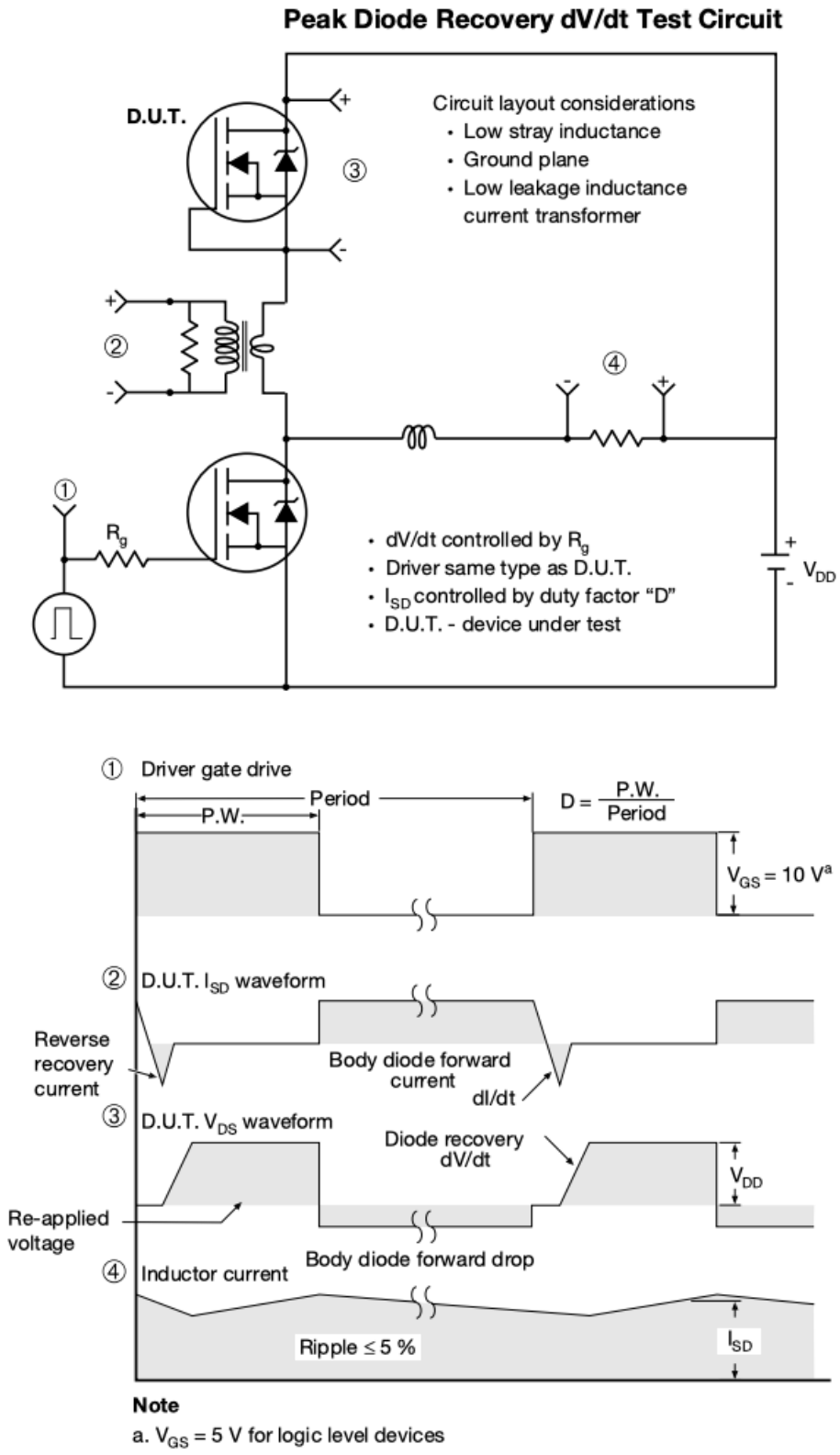


Fig. 14 - For N-Channel

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see ***www.vishay.com/ppg?91298****.*

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, “Vishay”), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay’s knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer’s responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer’s technical experts. Product specifications do not expand or otherwise modify Vishay’s terms and conditions of purchase, including but not limited to the warranty expressed therein.

Hyperlinks included in this datasheet may direct users to third-party websites. These links are provided as a convenience and for informational purposes only. Inclusion of these hyperlinks does not constitute an endorsement or an approval by Vishay of any of the products, services or opinions of the corporation, organization or individual associated with the third-party website. Vishay disclaims any and all liability and bears no responsibility for the accuracy, legality or content of the third-party website or for that of subsequent links.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.

© 2024 VISHAY INTERTECHNOLOGY, INC. ALL RIGHTS RESERVED

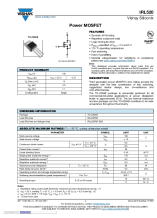
Revision: 01-Jan-2024

Document Number: 91000

For technical questions, contact:

THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT **www.vishay.com/doc?91000**

Documents / Resources

	**VISHAY IRL520PbF-BE3 Power Mosfet** [pdf] User Guide IRL520PbF-BE3 Power Mosfet, IRL520PbF-BE3, Power Mosfet, Mosfet

References

- Vishay Intertechnology: Passives & Discrete Semiconductors
- vishay.com/doc?91000
- vishay.com/doc?99912
- IRL520 MOSFETs | Vishay
- User Manual

Manuals+, Privacy Policy

This website is an independent publication and is neither affiliated with nor endorsed by any of the trademark owners. The "Bluetooth®" word mark and logos are registered trademarks owned by Bluetooth SIG, Inc. The "Wi-Fi®" word mark and logos are registered trademarks owned by the Wi-Fi Alliance. Any use of these marks on this website does not imply any affiliation with or endorsement.